Stretchable Oxide TFTs on PI/SEBS Substrate

Chanju Park, Suhui Lee and Jin Jang

Department of Information Display, Advanced Display Research Center, Kyung Hee University, Seoul 02447, South Korea

Keywords: Amorphous indium-gallium-zinc-oxide (a-IGZO), Thin-film transistor (TFT), Stretchable substrate

ABSTRACT

We present stretchable amorphous indium-gallium-zincoxide (a-IGZO) thin-film transistors (TFTs) transferred onto styrene ethylene/butylene styrene (SEBS) thermoplastic elastomer. The fabricated stretchable oxide TFT showed electrical properties even after 40% strain without mechanical and electrical degradations. This stiff island on the stretchable substrate was demonstrated to enable for stretchable electronics.

1 INTRODUCTION

Recently, researches for realizing stretchable electronics have received much attention [1]-[4]. However, in the case of electronic devices using rigid inorganic materials, cracks are easily generated within layers due to the brittleness of material itself when mechanical stress is applied. To eliminate such drawbacks, various structures have been studied to reduce mechanical stress for electronic devices.

Especially, researches on amorphous indium-galliumzinc-oxide (a-IGZO) thin-film transistors (TFTs) on stretchable substrate are of increasing attention due to 1) its excellent electrical characteristics such as high mobility (>10 cm²/Vs), high on-off current ratio (>10⁷), and stability under positive and negative bias despite amorphous structure semiconductor of and 2) large-area manufacturing [5]-[7]. However, issues that degradation of electrical properties due to its brittleness of metal-oxidesemiconductor layer even with a small strain limit the scalability of applications using a-IGZO TFTs.

In order to minimize the stress applied to the semiconductor layer, several types of research have been studies to form a neutral plane or to adopt additional dualgate to ensure the more stable electrical characteristics [8]-[9]. In addition, when the carrier glass and the flexible layer are separated after fabricating the device, carbon nanotubes/graphene oxide (CNT/GO) releasing layer or roll transfer technique to reduce damages on the flexible circuit has been studied [10]. Further, employing various structures such as a stiff island, serpentine pattern, prestretched elastomers have been reported to ensure stable operation of the electronic devices even after mechanical stress is applied on the stretchable substrate [11]-[13]. The various researches for implementing stretchable electronics are presented, however, there are no clear standards for producing practical devices, and still various attempts have been made.

In this study, we fabricated a-IGZO with backchannel-etched (BCE) structure on polyimide (PI) substrate and confirmed that mechanical stress can be minimized during detachment by embedding CNT layer between PI layer and glass substrate. In addition, this layer is applied to styrene CNT releasing ethylene/butylene styrene (SEBS) in the same way, which reduces the load when peeling off as well as selfstanding. The fabricated a-IGZO TFT/PI transferred onto SEBS showed stable electrical properties without degradation under 40 % strain and confirmed to be robust even after repeated for 1000 times at 20 % strain. In this experiment, stretchable oxide TFTs using PI/SEBS substrate can be proposed as a suitable device structure for wearable electronics.



Figure 1. Schematic illustration of fabrication of stretchable a-IGZO TFTs on PI/SEBS substrate. (a) SEBS formation. (b) Cutting OCA using laser and detachment. (c) OCA transfer onto SEBS. (d) Cutting fabricated a-IGZO TFTs on PI cut using laser and detachment. (e) Transfer a-IGZO TFTs on OCA film. (f) Detach SEBS film from carrier glass.

2 **EXPERIMENT**

2.1 Fabrication process of stretchable oxide TFTs on PI/SEBS substrate

The detailed fabrication process of the a-IGZO TFTs on PI film appears elsewhere [14]. Figure 1 shows the fabrication process of stretchable oxide TFTs. To fabricate SEBS substrate, CNT was sprayed on glass and annealed at 290 °C for 2 hours. After coating the prepared SEBS solution, cured at 110 °C on a hot plate for 10 minutes. (Optically clear adhesive) OCA film is cut into an appropriate size using laser and then transferred to the SEBS substrate as shown in Figure 1(b). After that, the fabricated flexible oxide TFTs were cut into the same size as the OCA layer (Figure 1(d)) and then transferred onto the OCA film (Figure 1(e)). Finally, the SEBS layer could be detached from the glass substrate in Figure 1(f).

2.2 Fabrication process of a-IGZO TFTs on PI

Figure 1(d) shows the schematic cross-section of fabricated flexible oxide TFTs. To fabricate flexible oxide TFTs, CNT was formed on the glass by spray-coating and then annealed at 290 °C for 2 hours. The CNT layer can be used to reduce the adhesion force of polyimide (PI) layer. After spin-coating PI, annealed at 450 °C for 1 hour. Then multilayer of SiO₂/SiN_x was deposited to form a gas barrier. To form the gate electrode, 120nm-thick Mo was deposited by sputtering and then patterned. The double layer of SiO₂/SiN_x layer for the gate insulator is deposited at 380 °C using plasma-enhanced chemical vapor deposition (PECVD). Then 20 nm of a-IGZO (InO₃:Ga₂O₃:ZnO = 1:1:1 mol%) is deposited using reactive sputtering without vacuum breaking and then patterned. A 150 nm-thick Mo is then deposited to form the source/drain electrode. Finally, 300 nm-thick SiO₂ for passivation layer is deposited.



Figure 2. (a) Self-standing property of SEBS film with and without CNT. (b) SEM image of SEBS embedding CNTs. (c) Adhesion force of SEBS film with and without CNT. (d) Stretchability of SEBS.

3 RESULTS and DISCUSSION

Figure 2(a) shows the SEBS film detached from the carrier glass. In the absence of CNT releasing layer, the detached SEBS films could be easily agglomerated, but when CNTs are embedded in SEBS films, self-standing property was confirmed. Figure 2(b) shows the scanning electron microscopy (SEM) image of surface of SEBS layer with uniformly embedded CNTs. The CNT layer reduces the adhesion force of the PI on carrier glass and thus can be used as a buffer layer for mechanical detach.

Figure 2 (c) shows the measured adhesion force when the SEBS film with and without CNT releasing layer is detached to 90 degrees from the carrier glass. The adhesion force with CNT release layer is around 300 mN, whereas 600 mN is needed only the SEBS layer is directly attached to carrier glass as shown in Figure 2(c). The SEBS film with CNT embedded shows very ductile properties that can be stretched up to 880% in Figure 2(d).



Figure 3. (a) Cross-sectional schematic illustration of a-IGZO TFT on PI/SEBS substrate. (b) Optical image of the fabricated a-IGZO TFT.

Figure 3(a) and (b) shows cross-sectional schematic illustration and optical image of fabricated a-IGZO TFT on PI/SEBS substrate. The fabricated a-IGZO TFT adopts conventional BCE structure, and the width and length of the channel are 40 μ m and 4 μ m, respectively.



Figure 4. Optical images of (a) stretched state of SEBS up to 40 % with a-IGZO TFTs and (b) repeated tensile test for 1000 cycles at 20 %

Figure 4 shows photograph images of the fabricated stretchable a-IGZO TFT on PI/SEBS substrate. Each image in Figure 4(a) shows that the strain is applied sequentially by 10 % from the initial state to 40 % and then returned to the initial state. Figure 4(b) shows durability of SEBS after 100, 200, 500, and 1000 repeated cycle tests at 20% strain. This tells us that the SEBS could be one of the options for stretchable substrate because of mechanical robustness.

Figures 5(a) and (b) shows transfer and output characteristics of the a-IGZO TFTs when the SEBS was stretched to 40% from the initial state and then returned. Electrical characteristics of a-IGZO TFT at initial state

extracted from transfer curve exhibited threshold voltage



Figure 5. Transfer and output characteristics after (a), (b) stretched up to 40 % and (c), (d) repeated test for 1000 cycles at 20 % strain.

(V_{TH}) of 1.1 V, field-effect mobility (μ_{FE}) of 22.1 cm²/Vs, and subtreshold swing (S.S) of 0.9 V/decade. Even after stretched up to 40%, V_{TH}, μ_{FE} , and S.S were 1.2 V, 22.1, and 0.91, respectively. This means that stress due to strain applied from SEBS does not have a significant effect on PI substrate. Figures 5(c) and (d) shows the cyclic test results after repeated 1000 times at 20% strain with a-IGZO TFT. Initial V_{TH}, μ_{FE} , and S.S were 1.2 V, 20.9 cm²/Vs, 1.02 V/decade, respectively, but after 1000 cycles, the electrical properties were slightly changed to 1.1 V of V_{TH}, 16.4 cm²/Vs of μ_{FE} , 0.89 V/decade of S.S. In this regard, mobility could be affected due to on-current degradation. These results indicate that a-IGZO TFTs on PI/SEBS substrates can be another candidate for stretchable electronics.

4 CONCLUSIONS

We present stretchable a-IGZO TFTs transferred onto SEBS thermoplastic elastomer. By CNTs in SEBS, it could be feasible to easily detachable with low force when peeling off from glass substrate, as well as self-standing property. Also, even CNTs were embedded in SEBS, they showed more than 880% elasticity. The a-IGZO TFTs fabricated on polyimide (PI) substrates were mechanically cut into proper size using laser and transferred onto SEBS substrates. Further, optically clear adhesive (OCA) was used to attach PI to SEBS, and it may be firmly attached even after repeated stretching. The fabricated stretchable oxide TFT showed electrical properties even after 1000 repetitions at 20% strain with small electrical degradations. This stiff island on the stretchable substrate can be demonstrated to enable wearable electronics.

5 ACKNOWLEDGEMENT

This work was supported by the Technology Innovation Program (or Industrial Strategic Technology Development Program(10080454, Development of Highresolutions OLED Micro-Display and Controller SoC for AR/VR Device) funded By the Ministry of Trade, Industry & Energy(MOTIE, Korea)

REFERENCES

- J. H. Hong, J. M. Shin, G. M. Kim, H. Joo, G. S. Park, I. B. Hwang, M. W. Kim, W. S. Park, H. Y. Chu, S. Kim. "9.1-inch stretchable AMOLED display based on LTPS technology." J. Soc. Inf. Disp., Vol. 25, No. 3, pp. 194-199 (2017).
- [2] M. Ramuz, B. C-K. Tee and Z. Bao. "Transparent, optical, pressure-sensitive artificial skin for largearea stretchable electronics." Adv. Mater. Vol. 24, No. 24, pp. 3223-3227 (2012).
- [3] S.-K. Lee, B. J. Kim, H. Jang, S. C. Yoon, C. Lee, B. H. Hong, J. A. Rogers, J. H. Cho, and J.-H. Ahn, "Stretchable graphene transistors with printed dielectrics and gate electrodes," Nano Lett., Vol. 11, No. 11, pp. 4642-4646 (2011).
- [4] T. Q. Trung, S. Ramasundaram, B.-U. Hwang, and N.-E. Lee, "An all-elastomeric transparent and stretchable temperature sensor for body-attachable wearable electronics," Adv. Mater., Vol. 28, No. 3, pp. 502-509 (2015).
- [5] K. Nomura, H. Ohta, A. Takagi, T. Kamiya, M. Hirano and H. Hosono. "Room-temperature fabrication of transparent flexible thin-film transistors using amorphous oxide semiconductors." Nature, Vol. 432, No. 7016, pp. 488-492 (2004).
- [6] J. S. Park, W.-J. Maeng, H.-S. Kim, and J.-S. Park, "Review of recent developments in amorphous oxide semiconductor thin-film transistor devices," Thin Solid Films, Vol. 520, No.6, pp. 1679–1693 (2012).
- [7] T. Kamiya, K. Nomura, and H. Hosono. "Present status of amorphous In–Ga–Zn–O thin-film transistors." Sci. Technol. Adv. Mater., Vol. 11, No. 4, pp. 044305-044327 (2010).
- [8] Y. H. Kim, E. Lee, J. G. Um, M. Mativenga and J. Jang. "Highly robust neutral plane oxide TFTs withstanding 0.25 mm bending radius for stretchable electronics." Sci. Rep., Vol 6, pp. 25734-25741 (2016).
- [9] M. M. Billah, J.U. Han, M.M. Hasan and J. Jang. "Reduced Mechanical Strain in Bendable a-IGZO TFTs Under Dual-Gate Driving." IEEE Electron Device Lett., Vol. 39, No. 6, pp. 835-838 (2018).
- [10] M. Mativenga, D. Geng, B. Kim and J. Jang. "Fully transparent and rollable electronics." ACS Appl.

Mater. Interfaces, Vol. 7, No. 3, pp. 1578-1585 (2015).

- [11] X. Li, M. M. Hasan, H. M. Kim and J. Jang. "Oxide Electronics Transferred on Stiff-Stripe/PDMS Substrate for High-Resolution Stretchable Displays." IEEE Trans. Electron Devices, Vol. 66, No. 7, pp. 2971-2978 (2019).
- [12] Y. Lee, M. Shin, K. Thiyagarajan. and U. Jeong. "Approaches to stretchable polymer active channels for deformable transistors." Macromolecules, Vol. 49, No. 2, pp. 433-444 (2015).
- [13] K. Park, D. K. Lee, B. S. Kim, H. Jeon, N. E. Lee, D. Whang, H. J. Lee, Y. J. Kim, and J. H. Ahn. "Stretchable, transparent zinc oxide thin film transistors." Adv. Funct. Mater., Vol. 20, No. 20, pp. 3577-3582 (2010).
- [14] M. M. Hasan, M. M. Billah, and Jin Jang. "Tensile stress effect on performance of a-IGZO TFTs with source/drain offsets." IEEE Electron Device Lett., Vol. 39, No. 2, pp. 204-207 (2017).