
Poster Presentation

[3Dp1/3DSAp1]3D and Hyper-realistic Displays and Applications 1

Thu. Nov 28, 2019 10:40 AM - 1:10 PM Main Hall (1F)

10:40 AM - 1:10 PM

[3Dp1/3DSAp1-15]Highly parallel special-purpose computer for electroholography on system on a chip

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Keywords:electroholography, FPGA

For realizing electroholography, a compact and high-performance computer is required. In this study, we implemented highly parallel special-purpose computer for electroholography on system on a chip. As a result, we succeeded in speeding up calculation 200 times faster than a CPU and a GPU.