

Oral Presentation

[AMD3]Driving Technology of Micro/Mini LED Displays

Special Topics of Interest on Micro/Mini LEDs

Chair: Kazumasa Nomoto (Sony)

Co-Chair: Keisuke Omoto (Apple)

Wed. Nov 27, 2019 5:00 PM - 6:35 PM Mid-sized Hall B (1F)

5:50 PM - 6:15 PM

[AMD3-3(Invited)]A 200-ppi Full Color Active Matrix Micro-LED Display with Low-Temperature-Poly-Silicon TFT Backplane

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Keywords:micro-LED, LTPS, TFT backplane, MPRT, HDR

A 1.8-inch 200-ppi full color active matrix micro light emitting diode (LED) display prototype has been developed with a low-temperature-poly-silicon (LTPS) TFT backplane. The frame rate of 240Hz and the luminance of 2000nits, both of which are promising attributes for high motion image quality and high dynamic range (HDR) applications, being superior to existing display technologies, were achieved by our LTPS TFT technology.