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Poster Presentation

## [AMDp1]Oxide TFTs

Thu. Nov 28, 2019 10:40 AM - 1:10 PM Main Hall (1F)

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10:40 AM - 1:10 PM

### [AMDp1-16]a-IGZO TFT Gate Integrated Driver Circuit with AC-driven Pull-down TFTs for High stability

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Keywords: Gate driver circuit, a-IGZO, Pull-down TFT, AC-driven, Duty ratio

*In the proposed gate driver circuit, pull-down TFTs are AC-driven with a duty ratio of 33.3% through CLK pulse instead of DC-driven through VDD power line. The simulation result exhibits output pulse of 1069<sup>th</sup> stage, 1071<sup>st</sup> stage, and 1073<sup>rd</sup> stage output pulse based on FHD, frame frequency of 120 Hz.*