
Poster Presentation

[AMDp1]Oxide TFTs

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[AMDp1-20L]The Development of Back-Channel-Etch Amorphous InGaZnO Thin-Film Transistors with Color Filter on Array Structure for 31 inch 120 Hz 4K GOA LCD

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Keywords:Back-Channel-Etch, InGaZnO, GOA, Color Filter on Array

The BCE a-IGZO TFT covered by Red C/F shows more negative V_{th} , which can be suppressed by optimized PV1 deposition condition. After two month storage, the BCE a-IGZO TFT with good PBTS stability shows poor NBTS stability. We used SiO_x/SiN_x bi-layer PV1 to achieve good PBTS and NBTS stability, simultaneously.