
Poster Presentation

[AMDp1]Oxide TFTs

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[AMDp1-7]Study on Promoting Transmittance on Dielectric Multi-layers for IGZO LCD Displays

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It was demonstrated that the layered structure of SiO_x and SiN_x is a good candidate to be gate insulator and passivation layers in IGZO TFT. However, this multi-layered structure could introduce issues of transmittance at aperture area in single glass of TFT side based optics physics. It is a good solution to improve the transmittance via optimizing structures of GI and PV layers, which could be adopted in IGZO-TFT LCD displays.