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Poster Presentation

## [AMDp1]Oxide TFTs

Thu. Nov 28, 2019 10:40 AM - 1:10 PM Main Hall (1F)

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10:40 AM - 1:10 PM

## [AMDp1-7]Study on Promoting Transmittance on Dielectric Multi-layers for IGZO LCD Displays

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Keywords:IGZO, Transmittance, GI/PV multi-layers

It was demonstrated that the layered structure of SiO<sub>x</sub> and SiN<sub>x</sub> is a good candidate to be gate insulator and passivation layers in IGZO TFT. However, this multi-layered structure could introduce issues of transmittance at aperture area in single glass of TFT side based optics physics. It is a good solution to improve the transmittance via optimizing structures of GI and PV layers, which could be adopted in IGZO-TFT LCD displays.