

Poster Presentation

## [AMDp2]Active-Matrix Devices

Thu. Nov 28, 2019 2:30 PM - 5:00 PM Main Hall (1F)

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### [AMDp2-7]A Novel Pull-down Holding Circuit of a-si Gate Driver on Array

\*Tian hong WANG<sup>1</sup> (1. Shenzhen China Star Optoelectronics Semiconductor Display Technology Co.,Ltd (China))

Keywords:GOA, Pull down holding

This paper proposes a novel pull-down holding circuit which can improve the pull-down holding ability under a-si process. Through adding only one thin film transistor(TFT) that we can achieve this purpose. What's more, this novel circuit can discharge the Pn node in the pull-down holding block.