Short Oral presentation | 10. Spintronics and Magnetics | 10. Spintronics and Magnetics (Poster)

[17p-PB3-1~48]Poster

10.1 Creation of new materials
10.2 Spin torque, spin current, circuits, and measurement technologies
10.3 Giant magnetoresistance (GMR), tunnel magnetoresistance (TMR) and magnetic recording technologies
10.4 Semiconductors, organic, optical, and quantum spintronics

Wed. Sep 17, 2014 4:00 PM - 6:00 PM PB3 (Gymnasium2)
ショート口頭講演時間13:30～15:06
場所：S2

4:00 PM - 6:00 PM

[17p-PB3-33]A study of FANOUT function in current driven spin memory and logic

○Tatsuyuki Maeda[1], Hiroyuki Awano[1], Touma Kanehira[1] (Toyota Inst.[1])

Keywords:spin logic,domain wall

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