SiO₂上横方向成長を用いた極低貫通転位密度 Ge 薄膜

Ultra-low threading dislocation density Ge film by lateral overgrowth on SiO₂

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[Introduction] Ge is a promising material for on-chip devices on Si photonics platform. The challenging issue of Ge on Si is threading dislocations (TDs) due to the lattice mismatch between Ge and Si. TDs behave as the leak pass in photo-detectors and the non-radiative recombination center in LEDs [1]. Epitaxial lateral overgrowth (ELO) is one of the techniques to reduce TD density (TDD) [2, 3]. Ge film formed over patterned SiO₂ has lower TDD (~10⁸/cm²) than that of Ge film on intrinsic Si wafer (10⁶-⁹/cm²). Remained problem on the ELO technique is TDs formed when Ge seeds coalesce (coalescence TDs). Thinner SiO₂ mask is employed to reduce the coalescence TDs in present work.

[Experimental] Thermal oxide is formed on Si wafers and line-shape Si growth seeds are exposed by wet etching using buffered-HF solution. Ge was grown over the patterned wafers by Ultra High Vacuum CVD employing step growth until uniform Ge film is obtained; low temperature buffer growth followed by high temperature main growth. Cross sectional TEM observation, etch pit density (EPD) measurements and XRD measurements were employed.

[Ge coalescence] Ge grown from the line-shape Si seeds coalesces and forms the flat film leaving air voids over the SiO₂. It is notable that no TD is shown in the coalesced Ge film. Considering sample thickness and observed width, TDD should be lower than 10⁸/cm².

[Coalesced Ge film quality] EPD measurements are employed to characterize Ge films. 10 µm x 10 µm area AFM measurements (Fig. 1) show clear difference between (a) coalesced Ge and (b) blanket Ge on the same wafer. The coalesced Ge shows no etch pit (corresponding EPD ≤ 10⁶/cm²) while the blanket Ge shows etch pits at a density of 7.2 x 10⁷/cm². Concrete EPD of the coalesced Ge is given by top view observation by optical microscope as 8 x 10⁴/cm².

[Conclusion] Ge grown from line-shape Si seeds coalesces and forms flat film. The coalesced Ge over the thin SiO₂ mask has ultra-low EPD as 8 x 10⁴/cm².

Fig. 1 Etch pit measurement by AFM on (a) coalesced Ge and (b) blanket Ge