Special Symposium | Special Symposium | SP.2 Harmonic innovations in Semiconductor Devices and Computer Architectures, and its Industrial Roadmaps

## [14p-301-1~10]SP.2 Harmonic innovations in Semiconductor Devices and Computer Architectures,and its Industrial

## Roadmaps

Tue. Mar 14, 2017 1:00 PM - 6:00 PM 301 (301)

 $\bigtriangleup$  : Presentation by Applicant for JSAP Young Scientists Presentation Award

▲ : English Presentation

▼ : Both of Above

No Mark : None of Above

1:30 PM - 2:00 PM

## [14p-301-2]Application Benchmark OSatoshi Matsuoka<sup>1</sup> (1.Tokyo Inst. of Tech.)

Keywords:IRDS