

Special Symposium | Special Symposium | SP.2 Harmonic innovations in Semiconductor Devices and Computer Architectures, and its Industrial Roadmaps

[14p-301-1~10] SP.2 Harmonic innovations in Semiconductor Devices and Computer Architectures, and its Industrial Roadmaps

Tue. Mar 14, 2017 1:00 PM - 6:00 PM 301 (301)

△ : Presentation by Applicant for JSAP Young Scientists Presentation Award

▲ : English Presentation

▼ : Both of Above

No Mark : None of Above

2:00 PM - 2:30 PM

[14p-301-3] ムーアの法則の延伸 ～二次元スケーリングの終焉と三次元スケーリング時代の幕開け～

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Keywords: IRDS