

Special Symposium | Special Symposium | SP.2 Harmonic innovations in Semiconductor Devices and Computer Architectures, and its Industrial Roadmaps

## [14p-301-1~10]SP.2 Harmonic innovations in Semiconductor Devices and Computer Architectures, and its Industrial Roadmaps

Tue. Mar 14, 2017 1:00 PM - 6:00 PM 301 (301)

△ : Presentation by Applicant for JSAP Young Scientists Presentation Award

▲ : English Presentation

▼ : Both of Above

No Mark : None of Above

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3:45 PM - 4:15 PM

### [14p-301-6]半導体実装技術の動向(JJTR/JEITA)

OYuji Ozaki<sup>1</sup> (1.Sony Semiconductor Solutions)

Keywords:IRDS