

## Spin-dependent transport of GaMnAs-based lateral spin MOSFET structures

H. Asahara<sup>1</sup>, T. Kanaki<sup>1</sup>, H. Terada<sup>1</sup>, S. Ohya<sup>1, 2, 3</sup>, and M. Tanaka<sup>1, 2</sup>

<sup>1</sup> Department of Electrical Engineering and Information Systems, The University of Tokyo

<sup>2</sup> Center for Spintronics Research Network, The University of Tokyo

<sup>3</sup> Institute of Engineering Innovation, Graduate School of Engineering, The University of Tokyo

E-mail : asahara@cryst.t.u-tokyo.ac.jp

Spin metal-oxide-semiconductor field-effect-transistors (spin MOSFETs), whose source and drain are composed of ferromagnetic materials, are promising candidates for post-scaling era [1]. In the previous studies of spin MOSFETs, however, the magnetoresistance (MR) ratios reported so far were less than 1% [2, 3]. To improve the MR ratio for practical applications, it is necessary to use ferromagnets that are compatible with semiconductors for the suppression of spin scattering at the interfaces. The ferromagnetic semiconductor GaMnAs is one of the ideal model materials. We can obtain an atomically abrupt interface between GaMnAs and GaAs. In fact, we have recently obtained a high MR ratio up to 60% in a GaMnAs-based vertical spin MOSFET [4]. However, in our previous study of the vertical spin-MOSFET, the gate modulation of the drain current  $I_D$  was small ( $\pm 0.5\%$ ) [4]. In this study, we fabricate GaMnAs-based *lateral* spin MOSFET structures, which are suitable for efficient gate modulation, and investigate the spin-dependent transport properties of these devices. We show a large MR ratio up to  $\sim 10\%$  and successful gate modulation of  $I_D$  of  $\sim 10\%$ .

We fabricated a GaMnAs-based lateral spin-valve device whose channel length  $d$  is 200 nm shown in Fig. 1 (a). We measured the MR characteristics of this device with a magnetic field applied in the plane along the [110] axis [Fig. 1 (b)]. We obtained a high MR ratio ( $\sim 10\%$ ). The MR ratio remained positive for all in-plane magnetic field directions, which confirms that this signal does not originate from tunnel anisotropic magnetoresistance (TAMR) but from the spin-valve effect. Furthermore, we fabricated a lateral spin MOSFET structure with  $d = 100$  nm shown in Fig. 2 (a), and successfully modulated  $I_D$  by applying the gate voltage  $V_{GS}$  [Figs. 2 (b)-(d)].

This work was partly supported by Grants-in-Aid for Scientific Research including Specially Promoted Research, Project for Developing Innovation Systems of MEXT, and Spintronics Research Network of Japan (Spin-RNJ).

### References

- [1] S. Sugahara and M. Tanaka, Appl. Phys. Lett. **84**, 2307 (2004).
- [2] R. Nakane, T. Harada, K. Sugiura, and M. Tanaka, Jpn. J. Appl. Phys. **49**, 113001 (2010).
- [3] T. Tahara *et al.*, Appl. Phys. Express **8**, 113004 (2015).
- [4] T. Kanaki, H. Asahara, S. Ohya, and M. Tanaka, Appl. Phys. Lett. **107**, 242401 (2015).

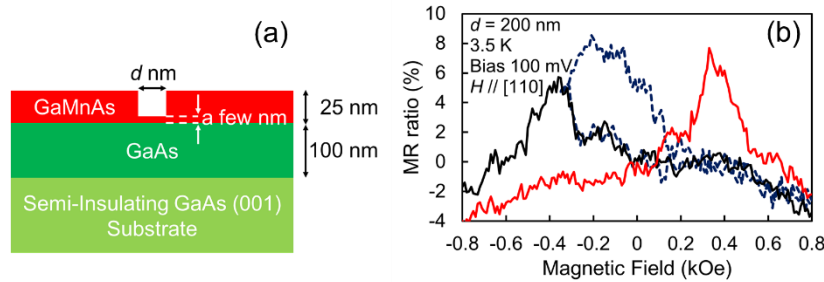


Fig. 1 (a) Schematic sample structure of the GaMnAs-based lateral spin-valve device with the channel length  $d$ . (b) Magnetic field dependence of the MR ratio measured at 3.5 K with a source-drain voltage of 100 mV and with a magnetic field applied in the plane along the [110] direction when  $d$  is 200 nm.

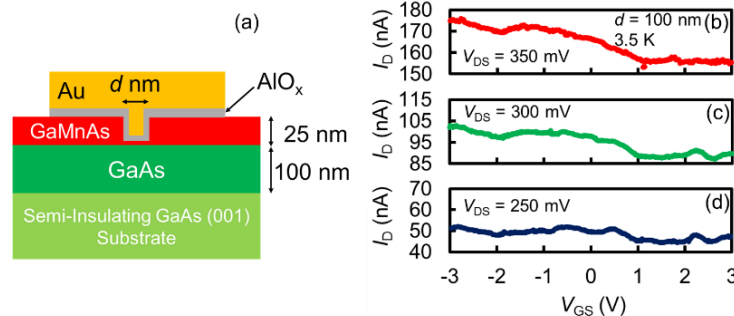


Fig. 2 (a) Schematic sample structure of the GaMnAs-based spin MOSFET. (b), (c), (d)  $V_{GS}$  dependence of  $I_D$  with the drain voltage  $V_{DS}$  of (b) 350 mV, (c) 300 mV, and (d) 250 mV at 3.5 K when  $d$  is 100 nm.