Symposium (Oral) | Symposium | To accelerate social implementations of applied-physic technologies related to integrated circuits

## [11p-Z16-1~10]To accelerate social implementations of applied-physic technlogies related to integrated circuits

Hitoshi Wakabayashi(Tokyo Tech), Hitoshi Sai(AIST), Yukiko Kamikawa(AIST)

Fri. Sep 11, 2020 1:30 PM - 6:00 PM Z16

 $\Delta:$  Presentation by Applicant for JSAP Young Scientists Presentation Award

▲ : English Presentation

▼: Both of Above

No Mark: None of Above

2:55 PM - 3:20 PM

## [11p-Z16-5]Business competitiveness shift by advanced VLSI technology

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Keywords: Semiconductor, Business competitiveness