

# A study of Ar/N<sub>2</sub>-sputtering gas pressure on electrical characteristics of LaB<sub>x</sub>N<sub>y</sub> insulator formed by RF sputtering

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## 1. Introduction

The nitrogen-doped (N-doped) LaB<sub>6</sub> has low work function, chemical stability, and low resistivity [1].

Previously, our group has reported LaB<sub>x</sub>N<sub>y</sub> insulator formation by Ar/N<sub>2</sub>-plasma sputtering for organic-based floating-gate memory applications [2].

In this study, Ar/N<sub>2</sub>-plasma sputtering gas pressure dependence on LaB<sub>x</sub>N<sub>y</sub> insulator was investigated to improve the dielectric characteristics.

## 2. Experimental procedure

The p-Si(100) substrate was cleaned by SPM and DHF. Next, thick field oxide 150 nm was formed by thermal oxidation. Then, the N-doped LaB<sub>6</sub> (Metal: M)/LaB<sub>x</sub>N<sub>y</sub> (Insulator: I) with a thickness of 30/10 nm were in-situ formed by RF sputtering at room temperature (RT) using N-doped LaB<sub>6</sub> target (N: 0.4 %). In the case of the LaB<sub>x</sub>N<sub>y</sub> layer, the Ar/N<sub>2</sub>-plasma sputtering gas pressure was carried out from 0.19 Pa to 0.65 Pa by changing the Ar/N<sub>2</sub> gas flow ratio from 4/2.8 sccm to 14/9.8 sccm. The N-doped LaB<sub>6</sub> layer was formed by Ar-plasma sputtering with Ar gas flow rate of 10 sccm. Next, the post metallization annealing (PMA) process was carried out at 400°C for 1 min in N<sub>2</sub> (1 SLM) ambient. The Al contact electrode was formed by thermal evaporation and Al/N-doped LaB<sub>6</sub>/LaB<sub>x</sub>N<sub>y</sub> stacked layers were etched by H<sub>3</sub>PO<sub>4</sub> and diluted HNO<sub>3</sub> solution. The pattern size was 30×30 μm<sup>2</sup>. Finally, the back Al electrode was formed. The electrical characteristics of the MIS diodes were evaluated by C-V measurement.

## 3. Results and Discussion

The C-V and extracted equivalent oxide thickness (EOT) and density of interface states (D<sub>it</sub>) for MIS diodes were shown in Fig. 1. The maximum capacitance of 0.59 μF/cm<sup>2</sup> and smallest EOT of 4.8 nm were shown at sputtering gas pressure of 0.19 Pa. The D<sub>it</sub> was decreased from 6.5×10<sup>12</sup> cm<sup>-2</sup>eV<sup>-1</sup> to 3×10<sup>12</sup> cm<sup>-2</sup>eV<sup>-1</sup> by decreasing the sputtering gas pressure from 0.65 Pa to 0.19 Pa. Improved dielectric characteristic and interface property suggested that dense and smooth LaB<sub>x</sub>N<sub>y</sub> insulator

was formed at low sputtering gas pressure [3].

## 4. Conclusions

We investigated the sputtering gas pressure dependence on LaB<sub>x</sub>N<sub>y</sub> insulator. The dielectric characteristic and interface property were improved at low sputtering pressure of 0.19 Pa. It would be promising for organic-based floating-gate memory with low operation voltage.

## Acknowledgements

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## References

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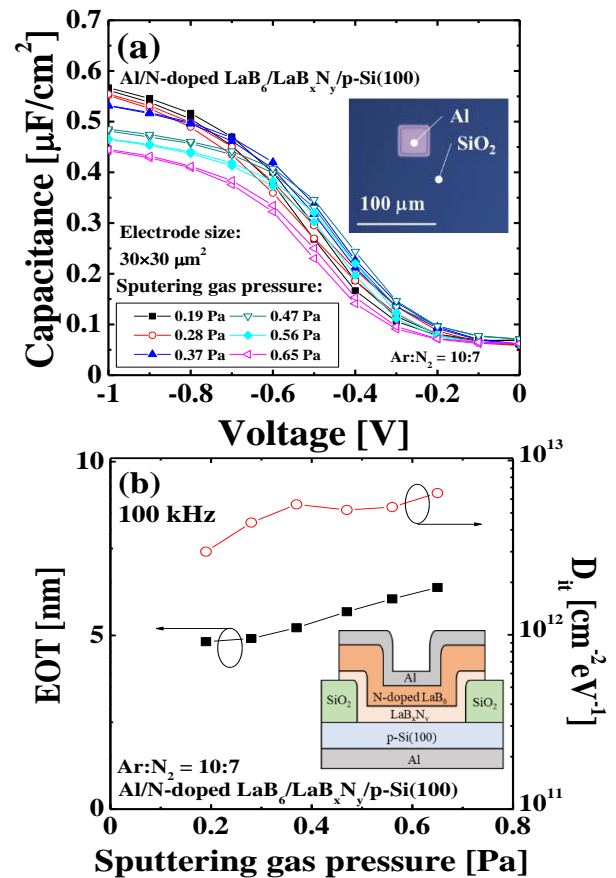


Figure 1. (a) C-V and (b) EOT and D<sub>it</sub> of Al/N-doped LaB<sub>6</sub>/LaB<sub>x</sub>N<sub>y</sub>/p-Si(100) diode.