Design method of diffusion resistors
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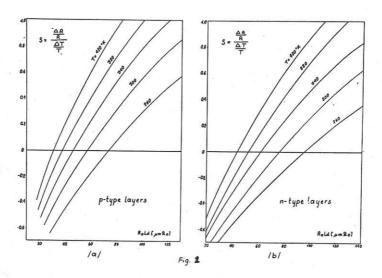
The present paper presents the design method of resistors for monolithic integrated circuits. The following elements have been considered during the analysis:

- 1. Gaussian distribution of impurities in diffusion layers.
- 2. Lattice and impurity scattering are of crucial significance in transport phenomena.
- 3. The fact of incoplete impurity ionization at higer impurities concentration and the dependence of ionization range from temperature.

The dependence of  $\mathcal{R}_{\Box} \mathcal{L}_{d}$  product from impurity surface consentration and temperature after taking into account the previously accepted assumption has been calculated by means of a computer from derived expression:

$$R_0 L d = \frac{1}{2q \int \mu(n,T) n(T,z) dz}$$
 /1/

Figure 1 presents the temperature sensitivity of diffusion resistors as a function of  $\mathcal{R}_{\Pi} \mathcal{L}_{A}$ .



The other problem is a change of diffusion layer resistivity during the drive-in process.

The starting point analysis was the assumption that the impurity charge which occurs in the diffusion layer in silicon, is reduced by the impurity charge, which has came into the oxide in the cours of oxidation.

The number of impurities getting into oxide is decreasing  $d_{r}^{\mu}$ ing the oxidation time. Concentration of impurities in oxide is:

$$N_{sio_2} = N^o \frac{k_*}{\sqrt{\frac{Dt}{O_1 t_2}} + 1} = N^o \frac{k_*}{\sqrt{\frac{D \times^2}{B L d_*^2} + 1}} / 2 / \frac{1}{\sqrt{\frac{D \times^2}{B L d_*^2}}}$$

Parameter  $k_4$  contains the segregation coefficient  $m = \frac{N_s}{N_{sio_2}}$  and  $\frac{N_o}{N_s}$  rate which slightly increase with the oxidation speed. Parameter  $k_4$  is a weak function of  $\sqrt{\frac{D}{B}}$ . In This analysis  $k_4$  constans was assumed which was measured experimentally. Such proceeding can be simply taken from the fact that various segregation coefficients were published by various authors.

The charge of impurities on oxide by integration of /2/ is as follows:

$$Q_{5;0_2} = k_1 N^o Ld, \sqrt{\frac{B}{D}} \text{ arsh } \frac{Ld}{Ld_1}$$
 /3/

Finaly the correction factor is expressed by the relation:

The correction factor resulting from the change of effective carrier mobilities is:

$$g_{\mu} = \left(\frac{Ld}{Ld_{1}} + 1\right)^{\frac{d}{2}a} = \left(\sqrt{\frac{DE}{Dt_{1}}} + 1\right)^{0.12}$$
/5/