

0-2 *INVITED*: FUTURE TRENDS IN SOLID STATE ELECTRONICS

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ABSTRACT: The pace of development in silicon planar technology continues at an anomalously high rate considering its level of maturity. The prospects for this technology during the seventies will be reviewed in the context of digital system requirements. Comparisons will be made with magnetic and cryogenic based technologies during this same time frame and beyond. Impact of hardware developments on systems will be discussed.