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Anodic Oxidation and MOS Devices of GaAs and GaP

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A good insulating film is desired for applications to MOS devices and surface passivation of GaAs and GaP. A good insulating film has to possess high resistivity, high breakdown field, low interface state density, low trap density, chemical stability, and effectiveness in stopping an ion migration. One of the candidates is the native oxide that can be grown by a low temperature process. Revesz and

Zaininger first reported the experimental results on the anodic oxidation of $GaAs^{(1)}$ We have reported some basic results on the properties of oxide films and MOS C-V curves of GaP and $GaAs^{(2)(3)}$ This paper describes mainly the annealing effect on electrical and chemical properties of the anodic oxides on GaAs and GaP, as well as some characteristics of MOS devices.

1) Oxidation and annealing processes: The wafers cut from n-GaP (100) and $(\bar{1}\bar{1}\bar{1})$ n=10¹⁷-10¹⁸ cm⁻³, n-GaAs (100) and $(\bar{1}\bar{1}\bar{1})$ n=10¹⁶-10¹⁸ cm⁻³, p-GaAs (100) p=10¹⁷ cm⁻³ and epitaxial n-GaAsP (100) n=10¹⁷ cm⁻³ were oxidized in the electrolytes of H₂O₂(30%) +H₃PO₄ with PH=2.0 (HP solution) and/or tartaric acid+propylene glycol+NH₄OH with PH=7.0 (TP solution). The samples were annealed at various conditions; from 220 C to 600 C in air, H₂, N₂, or Ar gases for 2 hours after oxidation. The chemical and electrical properties were examined before and after annealing.

2) Electrical properties: The typical change of the current against voltage curves is shown in Fig.1 after various annealing conditions for GaAs. The resistivity of the film is highest after 220 C annealing. By raising the annealing temperature, the leakage current becomes large. The improvement of the resistivity after annea-

ling at 220 C in air will be due to the vapourization of water which was involved during the oxidation ⁽⁴⁾ The increase of leakage current at higher temperature annealing will be due to the change of the compositions. As reported by Spitzer et al.⁽⁵⁾, high temperature annealing changes the as-grown oxide to ⁴ β -Ga₂O₃ which is poly-crystallized. The breakdown electric field of the film on GaAs decreased by raising the annealing temperature from 5.0×10^6 V/cm to 1.6×10^6 V/cm. 3) Chemical properties: The compositions of the films were measured by Auger and XMA. The distribution are shown in Fig.2, which is for the film after annealed at 220 C in air.



Fig.1 Current vs electric field at various annealing conditions.

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The transition region is narrower than the thermally oxidized film. The films were tested in acids and basics. The results are summarized in Table 1. After annealing at 600 C the films become insoluble in all acids except in HF, this also suggests the formation of Ga₂O₃. The details of the analysis of compositions after annealing will be discussed. 4) C-V and G-V curves: We already reported the C-V and G-V characteristics and their illumination effect of GaP MOS diode⁽²⁾ These curves are favorably compared with the calculated curves. In the calculation, it is assumed that the flat band voltage was shifted by existence of fixed positive charges of 1.1×10¹²cm⁻² and injected electrons compensate their positive charges upon applying positive voltag to the metal electrode. The effect of annealing on C-V and G-V curves of GaAs MOS diodes will be presented. One example is shown in Fig.3 and 4. The frequency dispersion was not improved by annealing at 400 C. The ac conductance decreases in the negative voltage region except after 600 C annealing while it increases in the positive voltage region. In the presentation, the effect of annealing under various conditions on electrical and chemical properties of the anodic oxides will be presented as well as the characteristics of MOS diodes of GaAs and GaP. The possibility will be discussed on the applications to MOS FETs and optical devices. References 1) A.G.Revesz and K.H.Zaininger; J.Am.ceram. -15 Soc.46,606(1963) 2) T.Ikoma and H.Yokomizo; to be published in IEEE ED-23 (1976) 3) H.Yoko- Fig.3 C-V curves of GaAs MOS diode.

mizo et al.; Technical Group Meeting on Solid State Devices, Inst. of Electron. and Comm. Eng. Japan SSD 75-43 (1975) 4) B.Schwartz and W.J. Sundburg; J.E.C.S.120,576(1973) 5) S.M.Spitzer et al.; J.E.C.S.<u>122</u>,397(1975)

We thank Dr.H.Iida for measurement of AES and XMA. Fig.4 G-V curves of GaAs MOS diode. -15

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Fig.2 The composition of oxide films

o - dissolved - almost dissolved Δ

		annealed in N2	
etchants	as grown	3000	0000
HC1	0	0	×
H ₂ SO ₄	0	0	×
HNO3.	0	0	×
H ₃ PO ₄	0	Δ	×
CH3COOH	0	×	×
HF	0	0	0
NH ₄ F	0	Δ	×
NaOH	0	0	×
12+KI+ CH3OH	0	×	×
KTFR Remover	ο	0	×





