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Silicon Graphoepitaxy*

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Many researchers have observed that the nucleation, growth and orientation of crystalline films on a substrate surface are strongly affected by perturbations such as scratches, pits or natural relief steps. Graphoepitaxy makes use of artificially produced surface patterns to induce orientation in an overlayer.¹⁻⁷ Uniformly oriented films of silicon have been obtained on amorphous fused silica substrates by means of graphoepitaxy. The process used initially included forming a relief structure of square-wave profile in the fused silica substrate, depositing amorphous silicon over this relief structure, and then crystallizing the amorphous silicon with an argon laser beam. Silicon films formed by this process were mosaics of crystallites. The $\langle 100 \rangle$ directions of the crystallites were perpendicular to the substrate to within $\pm 2.5^\circ$ and parallel to the grating axes to within $\pm 8.0^\circ$, as determined by x-ray pole plotting. The Hall mobilities of electrons in laser crystallized graphoepitaxial silicon films, 0.5 μm thick, ranged from 325 to 980 cm^2/Vsec , at a doping level of $5-8 \times 10^{16} \text{cm}^{-3}$. By comparison, we measured electron Hall mobilities of 320 cm^2/Vsec in commercial silicon-on-sapphire of the same thickness doped to $2 \times 10^{16} \text{cm}^{-3}$. Recently, a new process for achieving silicon graphoepitaxy has been developed which yields improved crystallographic quality. With this process we have obtained silicon films in which the $\langle 100 \rangle$ directions are perpendicular to the substrate to within $\pm 0.25^\circ$ and parallel to the grating axes to within $\pm 3.0^\circ$. Furthermore, the surface roughness and microcracks that we observed in laser crystallized films are not present with the new process. N-channel polysilicon gate field effect transistors have been fabricated in graphoepitaxial silicon films produced by this new process. Surface mobilities of 400 cm^2/Vsec have been measured. The current status of the theory of graphoepitaxial orientation will be discussed with emphasis on methods for orienting silicon over insulating substrates. We believe graphoepitaxy is a very general technique that can be used to orient a wide variety of crystalline overlayer films on a variety of substrates.

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