

## Low Temperature Growth of GaAs on Si by Migration Enhanced Molecular Beam Epitaxy

Shin Yokoyama, Dai Yui, Takashi Shiraishi and Mitsuo Kawabe

Institute of Materials Science, University of Tsukuba,

Sakura-mura, Ibaraki 305, Japan

A single domain GaAs was epitaxially grown by a migration enhanced molecular beam epitaxy, which utilizes alternately chopped Ga and As<sub>4</sub> beams, on misoriented (100) Si substrates at 250 °C. The stress in the GaAs film was evaluated from x-ray diffraction measurements and from curvature radii measured by the reflection of a laser beam, and it was confirmed that the stress was decreased by low-temperature growth. Etch pit density was  $\sim 5 \times 10^7$  cm<sup>-2</sup> for the sample grown at 250 °C.

### 1. Introduction

For the direct growth of GaAs on Si substrates, large lattice misfit between GaAs and Si, large difference in thermal expansion coefficient and being polar on nonpolar system are preventing a growth of high quality film. The problem about a antiphase domain has been resolved by utilizing misoriented substrate,<sup>1-3)</sup> and the mechanism of self-annihilation of antiphase boundary has become clear.<sup>4)</sup> However, dislocation density is still rather high ( $10^6$ - $10^8$  cm<sup>-2</sup>) even though two step growth,<sup>1)</sup> strained-layer superlattice<sup>5,6)</sup> and post or in-situ annealing<sup>7,8)</sup> have been utilized for reducing the dislocation density. Among the above problems the influence of large difference in thermal expansion coefficient can be reduced by decreasing the growth temperature. However, the crystal quality is generally degraded by decreasing the growth temperature. Recently, Horikoshi et al.<sup>9)</sup> developed so called migration-enhanced epitaxy (MEE), in which the Ga and As<sub>4</sub> beams are alternately impinging on the substrate surface, and represented a high quality of GaAs and AlGaAs films grown at 200-300°C on GaAs substrates. We applied this technique to the growth of GaAs on Si substrate for reducing the thermal stress in the films.

### 2. Experimental

The GaAs films were grown in a conventional molecular beam epitaxy (MBE) apparatus (ANELVA model 830). After thermal cleaning of misoriented Si (100) substrates tilted by 4° toward <011> at a substrate temperature above 850°C, the surface was exposed to As<sub>4</sub> flux at a substrate temperature of 250°C or 750°C. The domain direction of GaAs to Si substrate is known to be different by 90° depending on the temperature at which the sample surface is exposed to As<sub>4</sub> flux.<sup>4,10)</sup> After exposure to As<sub>4</sub> flux, the first buffer GaAs layer (25 nm in thickness) was deposited by normal MBE at a growth rate of 100 nm/h at 250°C, followed by in-situ annealing under As<sub>4</sub> pressure at 580°C for 10 min to improve the crystal quality. Thereafter the 1.5 μm-thick GaAs films were grown by the MEE technique at substrate temperatures of 200-580°C. The beam flux intensities of Ga and As<sub>4</sub> during MEE were  $J_{\text{Ga}} = 6.4 \times 10^{14}$ /cm<sup>2</sup>.s and  $J_{\text{As}_4} = 2.4 \times 10^{14}$ /cm<sup>2</sup>.s, respectively. The shutter opened durations for Ga and As cells were 1s and 3s, respectively, which gave approximately one monolayer growth per cycle. The stress in the film was evaluated from the warping of the substrate measured by a deflection of a laser beam reflecting from the sample surface. The stress was also evaluated from the lattice constant measured by the x-ray diffractometer, where (400) GaAs

diffraction peak angle was carefully measured. The crystal quality was evaluated from the full width at half of maximum (FWHM) of the (400) GaAs diffraction peak. The etch pits were observed by scanning electron microscopy (SEM) after etching in electrolyte under illumination<sup>11)</sup> and in the molten KOH.

### 3. Results and Discussion

Figure 1 shows the curvature radii of the samples grown by different procedures. The open circles are for the samples which are exposed to As<sub>4</sub> flux at a low temperature of 250°C after thermal cleaning ((A) series samples). The solid circles are for the samples which are exposed to As<sub>4</sub> flux at a high temperature of 750°C ((B) series samples). The theoretical curve is obtained by using the bi-metallic strip model.<sup>12)</sup> The curvature radii for both series of samples increase with decreasing growth temperature except for the data at 200°C, which shows that the stress in the film is increased. The disagreement with the theoretical curve may be due to the too simplified model in which the strip is used, while the measured sample is a rectangular plate. Figure 2 shows the lattice constant perpendicular to the sample surface measured by the x-ray diffraction. The theoretical curve was derived as below. The stress parallel to the GaAs/Si interface T is given by:

$$T = \epsilon E_{\text{GaAs}} / \nu_{\text{GaAs}}, \quad (1)$$

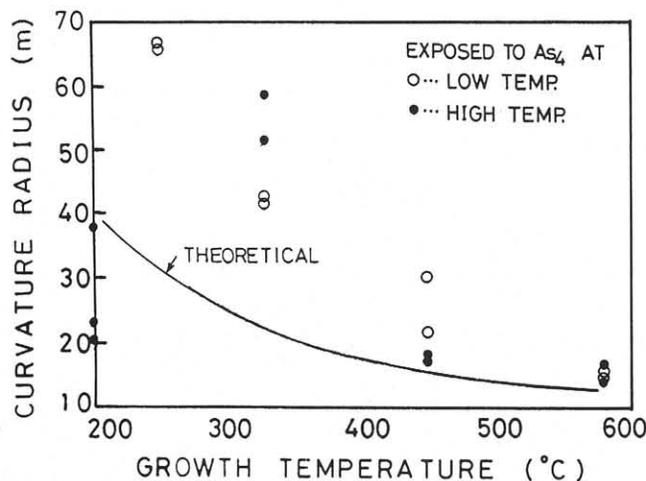


Fig. 1. Curvature radius measured by the reflection of a laser beam as a function of growth temperature.

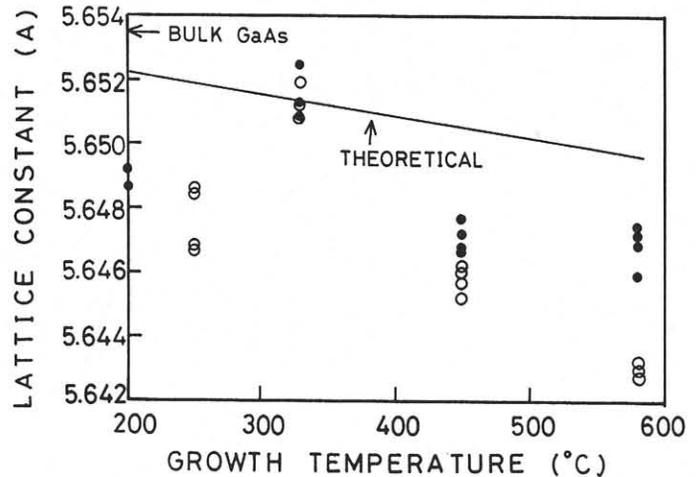


Fig. 2. Lattice constant measured by x-ray diffraction as a function of growth temperature. The meanings of ○ and ● are the same as in Fig. 1.

where  $\epsilon$  is the strain in the direction perpendicular to the surface,  $E_{\text{GaAs}}$  is Young's modulus for GaAs, and  $\nu_{\text{GaAs}}$  is Poisson's ratio for GaAs.  $\epsilon$  is calculated using the following formula:

$$\epsilon = (d - d_0) / d_0, \quad (2)$$

where  $d$  is the lattice spacing in GaAs on Si, and  $d_0$  is the lattice spacing in bulk GaAs. From the bi-metal theory the stress parallel to the interface T can be derived.<sup>12)</sup> Therefore, the lattice constant  $d$  can be calculated. We evaluated the stress in GaAs from the lattice constant and the curvature radius. The calculation from the lattice constant was carried out using the equations (1) and (2). In the calculation from the curvature, the following equation is used:<sup>13)</sup>

$$T = E_{\text{Si}} d_{\text{Si}}^2 / (6R d_{\text{GaAs}}) / (1 - \nu_{\text{Si}}), \quad (3)$$

where  $E_{\text{Si}}$  is Young's modulus for Si,  $d_{\text{Si}}$  and  $d_{\text{GaAs}}$  is the thickness of Si wafer and GaAs film, respectively, and  $\nu_{\text{Si}}$  is Poisson's ratio for Si. R is the curvature radius of the wafer. The results are shown in Figs. 3(a) and 3(b) for (A) and (B) series samples, respectively. Open and solid circles corresponds to the data obtained from the lattice constant and the curvature radius, respectively. Both series of samples have the same tendency, that is, the stress derived from the lattice constant is larger

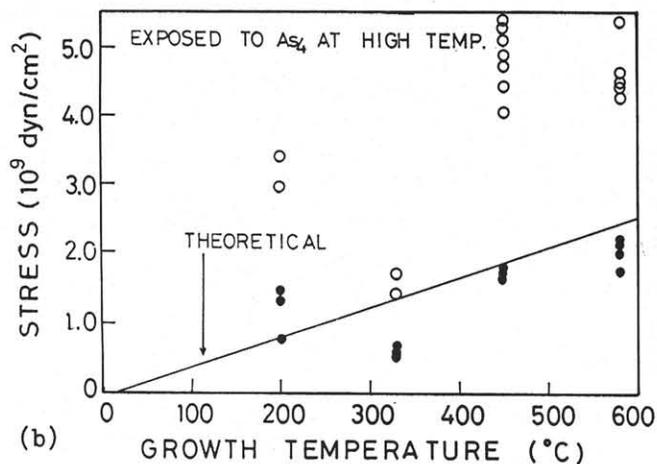
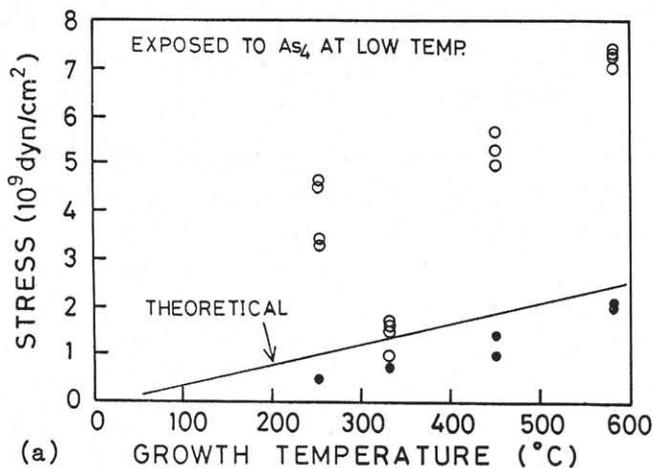


Fig. 3. Stress in the GaAs film for the samples exposed to  $As_4$  flux (a) at low and (b) at high temperature. Open and solid circles are for the results obtained from lattice constant and curvature radius, respectively.

than the theoretical curve and the stress derived from the curvature is smaller than the theoretical curve. Ishida et al. also reported on the same system (GaAs on Si) that the stress obtained from the lattice constant is larger than that obtained from the curvature by a factor of 2.<sup>14</sup>) To clarify the reason for these discrepancy, the further study will be necessary including the examination of the accuracy of the measurements, the validity of the model used in the theoretical calculation and the analysis method for the data. From the above results, it is roughly concluded that the stress is decreased with decreasing the growth temperature. Figure 4 shows the etch pit pattern of the sample in (A) series grown at 250°C. The large pits are aligned to the same direction, suggesting that the film is single domain structure. The large etch pit density is  $6.6 \times 10^3$  cm $^{-2}$ . Figures 5(a)-(d) illustrate the surface morphology after

electrochemical etching under illumination for (A) series samples. The small pits can be observed. However, the large pits as shown in Fig. 4 can not be observed in the samples grown at 330-580°C. The etch pit densities counted from Figs. 5(a)-(d) are shown in Fig. 6 together with the FWHM of the x-ray



Fig. 4. An SEM photograph of the sample grown by MEE at 250°C after electrochemical etching under illumination.

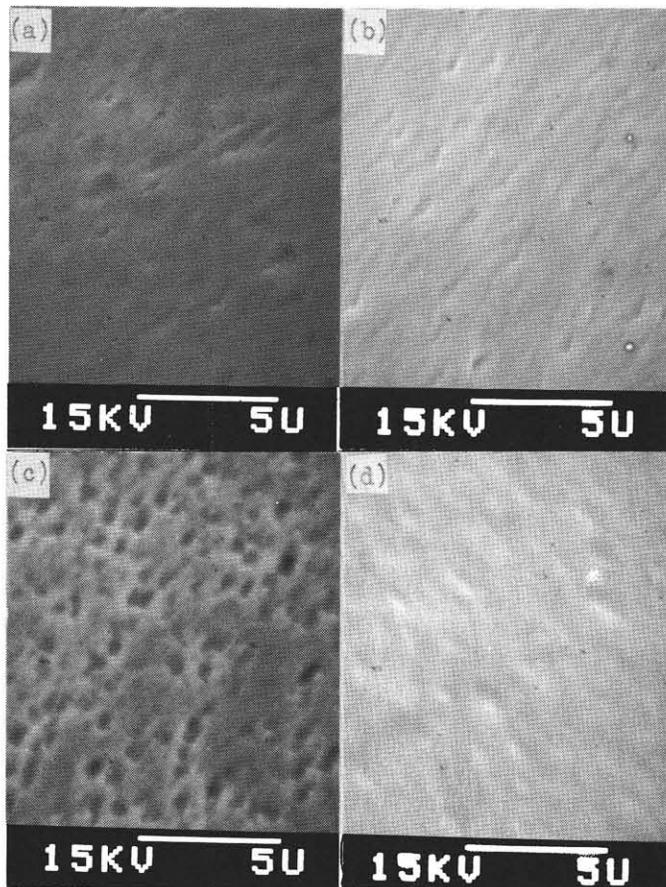


Fig. 5. SEM photographs for the samples grown at (a) 580°C, (b) 450°C, (c) 330°C, and (d) 250°C after electrochemical etching under illumination.

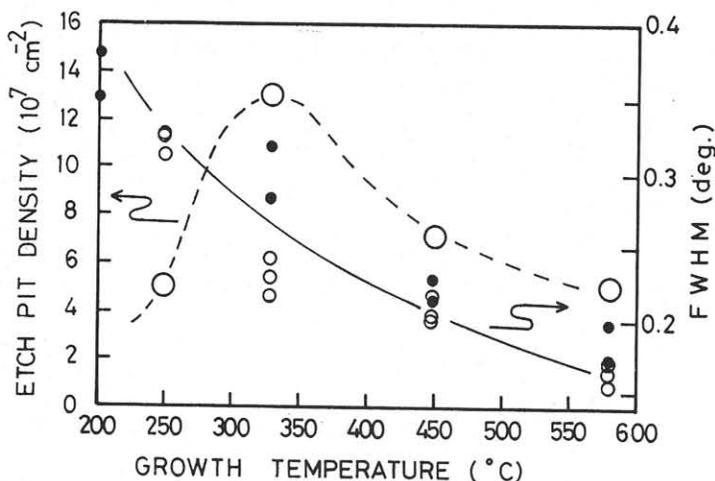


Fig. 6. Etch pit density for samples in Fig. 5 and FWHM of x-ray diffraction peak. ● and ○ are for (A) and (B) series samples, respectively.

diffraction peaks as a function of growth temperature. Although the pit density is increased with decreasing the growth temperature, it is again decreased at 250°C. The small pit density of the sample grown at 250°C is  $5 \times 10^7 \text{ cm}^{-2}$ . The FWHM is monotonously increased with decreasing the growth temperature, showing that the crystal quality is degraded at low temperatures. The lowering of the growth temperature has two affects. One is an insufficient surface migration of constituent atoms, which causes lattice defects and dislocations. Another is the reduction of the thermal stress which suppresses the generation of the dislocations. These two opposite actions compete in the actual system. At high temperatures above 450°C, the surface migration is enough large and the first effect may not be the main reason for the dislocations. At a low temperature of 250°C, the thermal stress reduction is one possible reason for the decreased etch pit density. At the middle temperatures, the stress reduction is not substantial and also the crystal quality is degraded, resulting in the high etch pit density. The similar mechanism may work for the sample grown at 330°C, which has the largest etch pit density.

In conclusion the single domain GaAs film has been grown on Si substrate at low temperatures of 250°C by MEE. The stress in the film is confirmed to be decreased by decreasing the growth temperature. The etch

pit density ( $\sim 5 \times 10^7 \text{ cm}^{-2}$ ) is relatively small even at such a low growth temperature.

#### Acknowledgement

The authors are very grateful to Mr. J. Oogi and H. Kobayashi for their help in the experiment.

#### References

- 1) M. Akiyama, Y. Kawarada and K. Kaminishi: *J. Cryst. Growth* **68** (1984) 21.
- 2) H. Morokoc, C. K. Peng, T. Henderson, W. Kopp, R. Fischer, L. P. Erickson, M. D. Longerborn and R. C. Youngmann: *IEEE Electron Dev. Lett.* **EDL-6** (1985) 381.
- 3) M. Kawabe and T. Ueda: *Jpn. J. Appl. Phys.* **25** (1986) L285.
- 4) M. Kawabe and T. Ueda: *Jpn. J. Appl. Phys.* **26** (1987) L944.
- 5) R. Fischer, H. Morokoc, D. A. Neuman, H. Zabel, C. Choi, N. Oyuka, M. Longerbone and L. P. Erickson: *J. Appl. Phys.* **60** (1986) 1640.
- 6) T. Soga, S. Hattori, S. Sakai, M. Takeyasu and M. Umeno: *J. Appl. Phys.* **57** (1985) 4578.
- 7) J. W. Lee, H. Shichijo, H. L. Tsai and R. J. Matyi: *Appl. Phys. Lett.* **59** (1987) 31.
- 8) C. Choi, N. Otsuka, G. Munns, R. Houdre, H. Morokoc, S. L. Zhang, D. Levi and M. V. Klein: *Appl. Phys. Lett.* **50** (1987) 992.
- 9) Y. Horikoshi, M. Kawashima and H. Yamaguchi: *Jpn. J. Appl. Phys.* **25** (1986) L868.
- 10) M. Kawabe, T. Ueda and H. Takasugi: *Jpn. J. Appl. Phys.* **26** (1987) L114.
- 11) A. Yamamoto and S. Yano: *J. Electrochem. Soc.* **122** (1975) 260.
- 12) S. D. Brotherton, T. G. Read, D. R. Lamb and A. F. W. Willoughby: *Solid-State Electronics* **16** (1973) 1367.
- 13) C. M. Drum and M. J. Rand: *J. Appl. Phys.* **39** (1968) 4458.
- 14) K. Ishida, M. Akiyama and S. Nishi: *Jpn. J. Appl. Phys.* **26** (1987) L530.