Threading Dislocation Reduction in InP/GaAs by Thin Strained Interlayer and Its Application to the Fabrication of 1.3 μ m Wavelength Laser on GaAs

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This paper examines the reduction of threading dislocation by a thin strained interlayer (SIL), and long-wavelength lasers fabricated on a GaAs substrate with SILs. Cross-sectional transmission electron microscope (TEM) is used to observe the dislocation blocking ability of a InGaP SIL inserted in an InP layer grown on a GaAs substrate. The characteristics of the lasers are improved due to dislocation reduction. Their threshold current is reduced to 70% on average and is also distributed more uniformly.

1. INTRODUCTION

The fabrication of III-V devices on lattice-mismatched substrates is attractive because it is the key technology in developing OEICs (opto-electronic integrated circuits). There have been several attempts to make GaAs lasers on Si¹), InP lasers on Si^{2,3}, and InP lasers on GaAs⁴). One of the difficulties in these studies is how to reduce the dislocation density and suppress the degradation of the devices.

Inserting a strained layer superlattice (SLS)⁵⁻⁹ is one of the conventional ways to reduce the dislocation density. It is assumed that dislocations are bent away by the strain field induced by the SLS at the lower interface99. Therefore, if a single strained interlayer (SIL) can provide the same strain field as an SLS does, it is expected to reduce the dislocation density as much as an SLS. In fact, dislocations are dramatically reduced by inserting a few monolayers of Si in a GaAs layer grown on a Si substrate¹⁰⁾. However, this is thought to be due to the strong covalent Si-Si bonds. In this paper, we investigate the effect of an InGaP SIL on the InP/GaAs system. Its ability to reduce threading dislocations was observed by cross-sectional transmission electron microscope (TEM). Long-wavelength lasers were fabricated on a GaAs substrate using this technique. Their high performance proved the effect of SIL on crystal improvement.

2. EXPERIMENTAL

N-type GaAs (100) substrates were used throughout this research. They were cleaned with organic solvents and chemically etched in $4H_2SO_4$: $1H_2O_2$: $1H_2O$ solution, and loaded into the metalorganic chemical vapor deposition (MOCVD) reactor and heated to 600°C in an AsH₃ flow. Then InP was grown by conventional one-step growth. The growth pressure and growth rate were 40 torr and 40 nm/min. The dopant gases used for the growth of the laser structure were H_2Se or Si_2H_6 for n-type layers and DMZn for p-type layers.

Cross-sectional TEM was used to study the threading dislocation structure. The dislocation density of the top surface was estimated by counting etch pit patterns formed by a H_3PO_4/HBr etchant. The crystal quality was evaluated by X-ray diffraction (XRD) and photoluminescence (PL) spectra at room temperature. The thickness of the SILs was measured by CAT (composition analysis by thickness fringe) - TEM¹¹ observation.

3. DISLOCATION REDUCTION BY SIL

Figure 1 shows cross-sectional TEM views of the InP/ GaAs samples with a (a) 50 Å (b) 25 Å thick In0.65Ga0.35P SIL. According to the theory of Matthews and Blacklee¹²⁾, 25 Å equals the critical thickness of InGaP at this composition on InP. The total InP thickness was 3 μ m and SILs were inserted 1 μ m above the interface. It is obvious that 50 Å is thicker than optimum, so this SIL generates more dislocations than it blocks. 25 Å seems to be a suitable thickness because this SIL reduces dislocations effectively. It generates no extra misfit dislocations though it blocks fewer dislocations than the 50 Å SIL does. The dislocation density above the SIL is 60% of that below it. This is as much as SLS can acheive. However, this does not mean the 25 Å Ino.65Gao.35P layer is the optimum SIL. More careful determination of both the optinum thickness and composition of the SIL is necessary to reduce dislocations more effectively.

4. LASER FABRICATION WITH SILS

Long-wavelength lasers emitting at 1.3 μ m on a GaAs substrate were fabricated employing SILs (A). Figure 2 is a shematic diagram of the BH structure on InP/GaAs. Three SILs were embedded in the 5 μ m n-InP buffer





layer. One 25 Å Ino.65Ga0.35P layer was placed at 1 μ m above the heterointerface, and two 30 Å Ino.89Ga0.11P layers were placed at 1.5 and 2 μ m above the interface. The two Ino.89Ga0.11P SILs were expected to block dislocations which threaded through the bottom SIL, but they were thinner than we intended them to be. Thus, they should have no effect on dislocations. After the growth of this buffer layer, thermal cycle annealing (TCA) was performed by changing the substrate temperature between 750°C and 200°C 3 times. This annealing reduced the full width at half maximum of the XRD of buffer layer from 260 to 230 arcsec, and reduced the



Figure 2 Schematic diagram of BH laser on GaAs with SILs



Figure 3 PL intensity of laser active layers

EPD at the top surface from $6x10^7$ to $3x10^7$ cm⁻².

Then (i) a 2 µm n-InP cladding layer, (ii) a 0.14 µm undoped InGaAsP ($\lambda = 1.3 \,\mu\text{m}$) active layer and (iii) a 0.4 µm p-InP cladding layer were grown. The mesa structure was formed by wet etching and its exposed region was buried with (iv) 2 μ m p-InP and (v) 1 μ m n-InP current blocking layers. The stripe width was 1.4 μ m. Finally (vi) a 2 μ m p-InP layer and (vii) a 0.3 μ m p-InGaAsP contact layer were grown. For comparison, we made another laser on GaAs with the same structure as (A) except with no SILs (B), and an identical BH laser on an n-InP substrate (C). Figure 3 shows the PL intensity from the active layer of these three lasers. The luminescence from (A) and (B) is much weaker than that from (C). Comparing the two GaAs lasers, the luminescence from (A) is slightly stronger than that from (B). These facts indicate that the SIL reduces the dislocation density of the active layer, but the density is still much higher than that on InP.

Figure 4 shows the effect of the SIL on device performance very clearly. It illustrates the distribution



Table 1 Laser characteristics derived from the following equations;

$$\eta_{d}^{-1} = \eta_{i}^{-1} \left(1 + \frac{\alpha}{\ln R^{-1}} L \right)$$

Jth = Jod + $\frac{d}{\beta \Gamma} \left(\alpha + \ln R^{-1} \frac{1}{L} \right)$

where

 $d = 0.14 \mu m$, R = 0.3, $\Gamma = 0.25$

	on GaAs		on InP
\sim	(A)	(B)	(C)
$\eta_{\rm i~(\%)}$	76	75	76
α (cm ⁻¹)	13.1	15.1	10.7
β (cm ² /A)	0.9 x10 ⁻⁶	0.6 x10 ⁻⁶	1.4 x10 ⁻⁶
J _{0 (A/cm³)}	2.9 x10 ⁸	3.4 x10 ⁸	6.7 x10 ⁷

of threshold current (Ith) of 100 laser chips each of (A) and (B), and 60 chips of (C) under pulse operation at room temperature. The cavity length is 400 µm. The average Ith of (A) is 34 mA, though that of (B) is 50 mA. This 30 % reduction in Ith was caused by the dislocation reduction due to the SIL. Moreover, the Ith of (A) are dispersed within a smaller range than that for (B). This indicates that the dislocation distribution in the active region is more uniform for (A) than (B). This fact can also be attributed to the SIL, i.e. the strain field at the SIL/InP interface not only bends dislocations but also scatters dislocations uniformly. TCA may also promote these phenomena because if dislocations are activated enough to move, the SIL can act on them again.

The external differential quantum effeiciency (ηd) and threshold current density (Jth) were investigated varying the cavity length (L). The internal differential quantum efficiency (ηi) , internal loss (α) , gain coefficient (B) and transparency current density (Jo) are summarized in Table 1. Most of the values are better for



Current (mA)

Figure 5 Light-current characteristics of lasers (cavity length = $400 \,\mu m$)

(A) than (B). This is also due to the SIL reducing dislocations. However (A) is still poor than (C) especially in β and J₀, because (A) has a much higher dislocation density as shown in Figure 3.

Figure 5 shows the light-current characteristics under cw operation at room temperature. The cavity length is 400 µm. Ith and the slope efficiency were the same as those under pulse operation. The minimum Ith of both lasers on GaAs is the same at 25 mA, and their slope efficiency is about 0.26 W/A. They operated more than 100 hours continuously at room temperature.

5. CONCLUSION

We have investigated the reduction of threading dislocations by InGaP SIL in InP/GaAs, and examined its effect on device performance. Observation of the dislocation structure by cross-sectional TEM showed the SILs ability to bend dislocations. BH lasers with a wavelength of 1.3 µm were fabricated on a GaAs substrate employing the most suitable SIL. Their characteristics such as Ith, α , β and J₀ were measured, and found to be improved by SIL insertion. This fact indicates that SIL should work effective in device fabrication on latticemismatched substrate.

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