# Elevated Polycide Source / Drain Shallow Junctions with Advanced Silicidation Processing and Al Plug / Collimated PVD-Ti/TiN/Ti / Polycide (APPOCIDE) Contact for Submicron CMOS

## H.Kotaki, Y.Takegawa, S.Kakimoto, T.Fukushima, K.Mitsuhashi, J.Takagi Y.Okamoto\* and Y.Akagi\*

Central Research Laboratories, Research and Analysis Center\*, Sharp Corporation 2613-1, Ichinomoto-cho, Tenri-shi, Nara 632, Japan

Low resistive elevated polycide source/drain shallow junction and contact processes have been developed using advanced silicidation processes called "AAS and BAS" and Al plug / collimated PVD-Ti/TiN/Ti / Ti-polycide (APPOCIDE) structure contact. Sheet resistances of n<sup>+</sup>-Ti-polycide and p<sup>+</sup>-Ti-polycide were reached at the same level as that of undoped-Ti-polycide. Contact resistivities were  $2\sim3x10^{-9}$   $\Omega$ cm<sup>2</sup> for contact on both n<sup>+</sup> and p<sup>+</sup>. These contact resistivities were two orders of magnitude lower than the conventional case of Al / TiN /Ti / n<sup>+</sup> or p<sup>+</sup>-Si structure. Furthermore, we propose unique consideration for the relative difficulty in achieving silicidation with low sheet resistance of TiSi<sub>2</sub> layer on n<sup>+</sup>-Si (or n<sup>+</sup>-poly-Si) as compared to that on p<sup>+</sup>-Si or undoped-Si.

#### **1. INTRODUCTION**

With decreasing dimensions in ULSI designs, the need for shallow junctions is increasingly important because of suppression for transistor short channel effect. However, as the junction depth and device size shrink, serious problems such as increases in sheet resistances of transistor source / drain, contact resistances and junction leakage current occur, which limit device performance. For these problems, elevated polycide S/D structures were proposed<sup>(1~3)</sup>. However, silicidation on n<sup>+</sup>-Si or -poly-Si layer with low sheet resistance was difficult as compared with undoped-Si or -poly-Si.(4,5) The actual mechanism of this obstruction is not fully understood. In order to overcome these problems, we proposed elevated polycide S/D shallow junctions formed by the advanced silicidation processing called "AAS(6) and BAS (75As+ or 11B+ doped into the silicide layer after silicidation)" processes, and then discussed the difficulty in silicidation with low sheet resistance on n+-Si (or n+-poly-Si). Furthermore, Al plug / collimated PVD-Ti/TiN/Ti / Ti-polycide (APPOCIDE) structure was demonstrated to realize a low contact resistance on n<sup>+</sup> and p<sup>+</sup>.

#### 2. EXPERIMENT

The polycide samples used in this experiment were formed by the AAS (n<sup>+</sup>), BAS (p<sup>+</sup>), ABS (n<sup>+</sup>) and BBS (p<sup>+</sup>) processes on a poly-Si layer using two-step rapid thermal annealing (2 step RTA) with halogen lamp heating in a N<sub>2</sub> atmosphere<sup>(7)</sup>. The process flows are shown in Fig.1. The ABS and BBS ( $^{75}As^+$  or  $^{11}B^+$  doped into the poly-Si layer before silicidation) processes are the conventional processes of silicidation for poly-Si layers Dosage of As<sup>+</sup> or B<sup>+</sup> instead of the Si-substrates. implantation was fixed at 5E15/cm<sup>2</sup> for all processes. The first RTA was performed in a temperature range from 575 to 625°C for 20sec in a N2 atmosphere without atmospheric exposure after Ti deposition by multi-chamber system, of which the base pressure was lower than 1x10-8 Torr. After the first RTA, the TiN and unreacted Ti were etched off by a H<sub>2</sub>SO<sub>4</sub> based solution. The second RTA condition was fixed at a temperature of 850°C for 20sec in a N2 atmosphere. The Ti-polycide film properties were estimated using a four-point probe method, scanning electron microscopy (SEM), transmission electron microscopy (TEM), secondary ion mass spectroscopy (SIMS) and Xray photoelectron spectroscopy (XPS).

The APPOCIDE contacts were formed using optimized AAS and BAS processes on  $n^+$  and  $p^+$ . After the silicidation process, CVD-SiO<sub>2</sub> deposition and contact hole definition, the multilayered metallization process was performed. Collimated-Ti (50nm), -TiN(100nm) -Ti (20nm), and high-temperature-flow-Al (500nm) were sequentially sputtered by the same multi-chamber sputtering system without atmospheric exposure. The contact resistances were estimated by the Kelvin method.

## 3. RESULTS AND DISCUSSION

Figure 2 shows the dependence of the final sheet resistances of the  $TiSi_2$  films on the first RTA temperature. The effect of the n<sup>+</sup> and p<sup>+</sup> doping into the polycide layer was studied by comparing the AAS, ABS, BAS and BBS processes with the silicidation of undoped-Si and undopedpoly-Si processes. Under the condition of first RTA temperature of 625°C, the sheet resistances of polycide layers by AAS, BAS and BBS processes were all about 2.0~2.1  $\Omega$ /square. On the other hand, the ABS process was essentially larger than the others. From TEM and SEM observations, the resistivities of TiSi<sub>2</sub> films formed by ABS and the other processes were estimated as about 26  $\mu\Omega$ cm (5.1 $\Omega$ /sq. at 50nm) and 16  $\mu\Omega$ cm (2.1 $\Omega$ /sq. at 75nm), respectively. From SIMS analysis, the tail of an oxygen peak extended throughout the silicide layer in the sample formed by the ABS process, as shown in Fig.3. So, it is considered that the oxygen in the silicide layer affects the sheet resistance of silicide layer.

Figure 4 shows final sheet resistances of the TiSi2 films formed by two kinds of ABS as a function of first RTA temperature. One method was a direct implantation of As+ ions into the poly-Si layer and subsequent annealing before silicidation. The other was implantation of As+ ions through an oxide layer of 20 nm into the poly-Si layer and the same annealing. Even in the ABS process, it was confirmed that the sheet resistance of Ti-polycide film was decreased to that of the AAS-polycide level by direct implantation, except for the problem of contamination. Figure 5 shows XPS spectra of poly-Si surface before Ti sputtering for the above-mentioned two ABS methods. HF treatment of two samples was performed just before analysis. In the sample formed by As+ implantation through a SiO2 layer, the peak assigned to Si 2P3/2 of Si-O bond was observed. By TEM micrographs, a titanium silicide layer formed by the reaction between sputtered-Ti and poly-Si was observed in the direct implantation without annealing sample. On the other hand, in the As+ implantation through SiO2 layer sample, no titanium silicide layer was observed, as shown in Fig.6. From these results (Fig.3.~6.), it is considered that the difficulty in silicidarion on n+-Si (n+-poly-Si) was caused by a knocked-on-oxygen and not an arsenic itself. If this consideration was appropriate, low sheet resistance of BBS polycide as the same level of the BAS and undoped polycide could be explained self-consistently, because of little knocking of oxygen at the implantation for small mass of B+ ion.

Figure 7 shows the SEM micrograph of the APPOCIDE contact. The TiSi2 film plays two important roles in the APPOCIDE contact. One is to act as a barrier layer against the diffusion of Al and Si. The other is to form a contact with low resistance. In the sample using collimated PVD-Ti/TiN/Ti as a barrier layer, an alloy spike was observed. On the other hand, using the APPOCIDE structure, no alloy spike was observed. It was confirmed that the barrier effect could be enhanced by the addition of a TiSi2 layer. Figure 8(a) and 8(b) show the dependence of the contact resistance on contact diameter in APPOCIDE contacts on n+ and p+ source / drain regions as compared with the conventional Al-based metal/TiN/Ti structure contacts, respectively. In the APPOCIDE structure, contact resistances two orders of magnitude lower than the conventional case were obtained for both n<sup>+</sup> and p<sup>+</sup>.

The average contact resistance of  $0.35 \,\mu\text{m}$  diameter contacts on n<sup>+</sup> was about  $2.5\Omega$  and that on p<sup>+</sup> was about  $3.0\Omega$ . From those results, the contact resistivities on n<sup>+</sup> and p<sup>+</sup>polycide were about  $2.4 \times 10^{-9}$  and  $2.9 \times 10^{-9} \,\Omega\text{cm}^2$ , respectively. Furthermore, the leakage current of elevated polycide source / drain with APPOCIDE contact for n<sup>+</sup>/p and p<sup>+</sup>/n junctions, which extended polysilicon / silicon interface area, were the same level as conventional (nonsilicidation) 0.2  $\mu$ m deep junctions with conventional contact, as shown in table 1.

### 4. CONCLUSION

Low resistive elevated polycide S/D shallow junction and contact processes have been demonstrated. Also, we suggest that the relative difficulty in achieving silicidation with low sheet resistance of the TiSi2 layer on n+-Si (or n+poly-Si) as compared to that on p+- or undoped-Si can be attributed to an knocked-on-oxygen in As+ implantation and not an arsenic itself. By using the AAS and BAS processes, low-resistive and low leakage shallow junctions were achieved. The resistivities for n+-polycide and p+polycide resulted in 16  $\mu\Omega$ cm that was the same level as that for undoped-polycide (or undoped-silicide). Al plug / collimated PVD-Ti/TiN/Ti / Ti-polycide (APPOCIDE) structure contact has low enough contact resistances. The average contact resistances of 0.35 µm diameter contact on  $n^+$  and  $p^+$  were about 2.5 $\Omega$  and 3.0 $\Omega$ , respectively. These contact resistances were two orders of magnitude lower than the conventional Al / TiN /Ti / n+- or p+-Si structure. Furthermore, the leakage current of elevated polycide source / drain with APPOCIDE contact for n+/p and p+/n junctions were the same level as conventional (non-silicidation) 0.2 µm deep junctions with conventional contacts. This technology is expected to be important for the development of future deep submicron devices.

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