

Low-Temperature Chemical-Vapor-Deposition of Silicon-Nitride Film from Hexachloro-Disilane and Hydrazine

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We have successfully deposited SiN films at temperatures as low as 350°C by CVD method using hexachloro-disilane (Si_2Cl_6) and hydrazine (N_2H_4). Atomic ratio (N/Si) of the film deposited at 400°C was 1.3. Total hydrogen content was about 25atomic%. The breakdown-field strength was 5.3MV/cm at leakage-current density of $1 \mu \text{A}/\text{cm}^2$, and the low-field resistivity was more than $10^{15} \Omega \cdot \text{cm}$. Amorphous-silicon thin-film transistors equipped with this film as the gate dielectric showed good transfer characteristics.

1. INTRODUCTION

Much attention has been paid recently to low-temperature (less than 400°C) deposition method of silicon nitride (SiN), since the SiN film deposited at low temperatures is important as a passivation layer for ULSI chips and a gate insulator for thin film transistors TFTs. Plasma-enhanced CVD (PECVD) is the representative method for low temperature deposition of the SiN film. However, there are fatal drawbacks in the PECVD arising from inherent properties of plasma. Low temperature CVD, i.e., alternative method is also attractive but its lowest deposition temperature is still as high as 400°C even by use of trisilane (Si_3H_8) and hydrogen azide (HN_3)¹. Application of higher silanes and nitrogen hydrides to reduce the deposition temperature encounters the serious limitation that they are thermally unstable even under room temperature.

We have proposed CVD method which can utilize advantage of electrostatic energy in the source molecule. It has been reported that the CVD temperature is lowered to about 600°C by using SiCl_4 and NH_3 ² due to strong dipole moments existing between Si-Cl and N-H bonds. Since higher, but still thermally stable, source molecules are expected to react more easily with each other, we investigated CVD using Si_2Cl_6 and N_2H_4 .

2. DEPOSITION CHARACTERISTICS

Figure 1 is a schematic of the apparatus used in this study. Since both Si_2Cl_6 and N_2H_4 are liquid at room temperature, they were introduced into the chamber by using N_2 carrier gas. The standard deposition conditions are given in Table 1.

Deposition rate DR and refractive index n are shown in Fig.2 as a function of reciprocal substrate temperature T. They were 6000 Å/h and 1.75 at T=450°C, respectively, and decreased with T gradually for T > 350°C

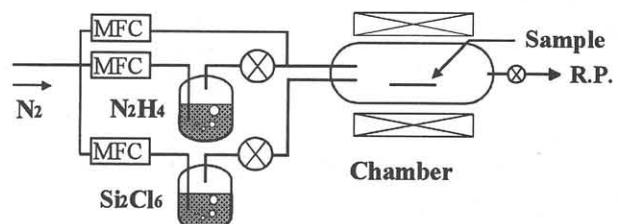


Figure 1. Schematic view of CVD system

Table 1. Standard deposition conditions

Pressure in Bubbler	760Torr
Total N_2 Gas Flow	100sccm
Chamber Pressure	160Torr
Substrate Temperature	400°C
N_2H_4 Carrier Gas Flow Rate	80sccm[0.98sccm]
Si_2Cl_6 Carrier Gas Flow Rate	20sccm[0.12sccm]

*[]: Net Gas Flow Rate

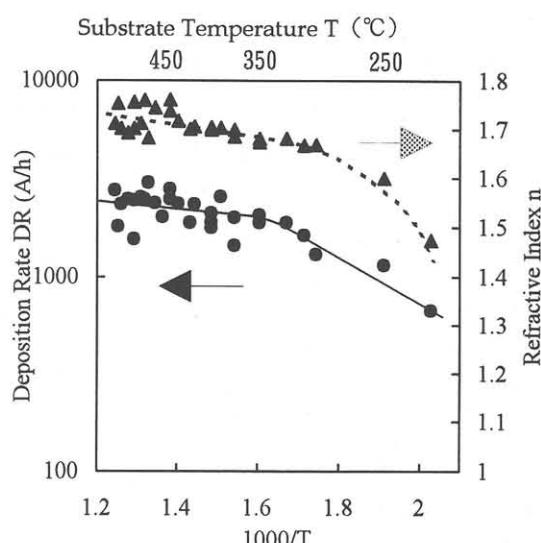


Figure 2. Deposition rate vs. substrate temperature

but rapidly for $T < 350^{\circ}\text{C}$. This result might be caused by residual NH_4Cl by-product which has the sublimation temperature of 338°C .

DR and n are shown in Fig.3 as a function of N_2H_4 carrier gas flow rate $R_{\text{N}_2\text{H}_4}$ where the Si_2Cl_6 carrier gas flow rate $R_{\text{Si}_2\text{Cl}_6}$ was kept at 20 sccm. DR was increased with $R_{\text{N}_2\text{H}_4}$ for $R_{\text{N}_2\text{H}_4} < 60$ sccm, but was saturated for $R_{\text{N}_2\text{H}_4} > 60$ sccm, since deposition kinetics became transport-limited by Si_2Cl_6 molecules. n was about 1.7 for all conditions, but it was decreased gradually for the sample formed by $R_{\text{N}_2\text{H}_4} < 60$ sccm with elongating storage time in atmosphere and finally saturated at 1.46. Results of Auger electron spectroscopy (AES) and Fourier transform infrared spectrometer (FTIR) measurements indicated that these films were Si rich at first, and that Si-N bonds were substituted gradually by Si-O bonds to form stoichiometric SiO_2 .

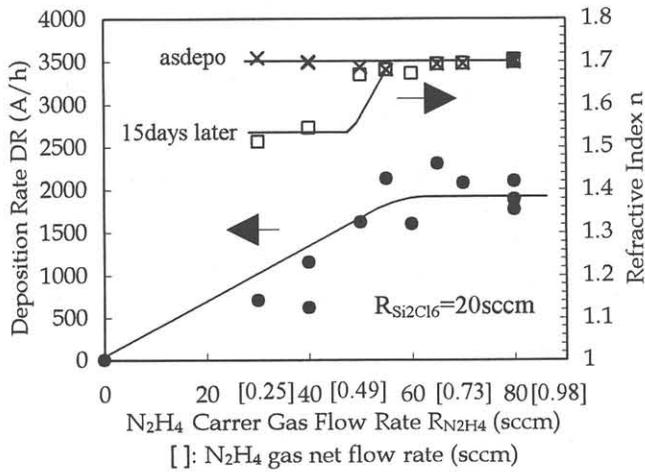
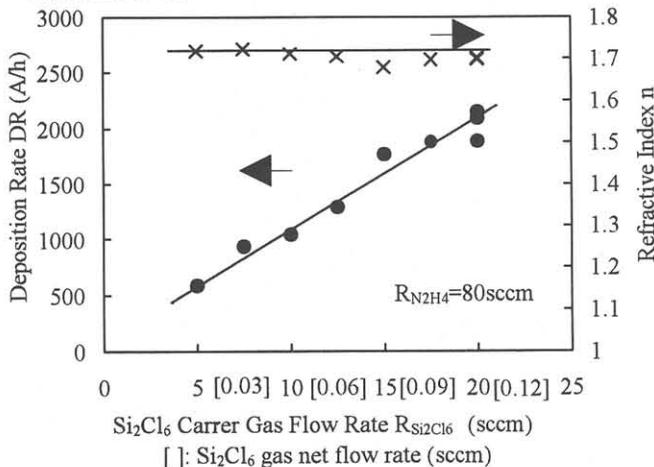


Figure 3. Deposition rate and refractive index vs. N_2H_4 flow rate

DR and n are shown in Fig.4 as a function of $R_{\text{Si}_2\text{Cl}_6}$ where $R_{\text{N}_2\text{H}_4}$ was kept at 80 sccm. Since the deposition kinetics were transport-limited by Si_2Cl_6 molecules, the deposition rate was a linear function of $R_{\text{Si}_2\text{Cl}_6}$. In these conditions, n was about 1.72.



4. ELECTRICAL PROPERTIES

Figure 7 shows logarithmic current density J as a function of square root of field strength E across the film deposited under the standard conditions. The optical dielectric constant calculated from $J-E^{1/2}$ slope in the high field region was 3.0, which is equal to square of the refractive index n ($n=1.72$). This result led us to the conclusion that the conduction mechanism in the high field region was dominated by Frenkel-poole emission.⁵ The breakdown-field strength determined from the field-strength at $1 \mu A/cm^2$ was $5.3MV/cm$. The static dielectric constant calculated from the capacitance under accumulation condition was about 5.6 for all films.

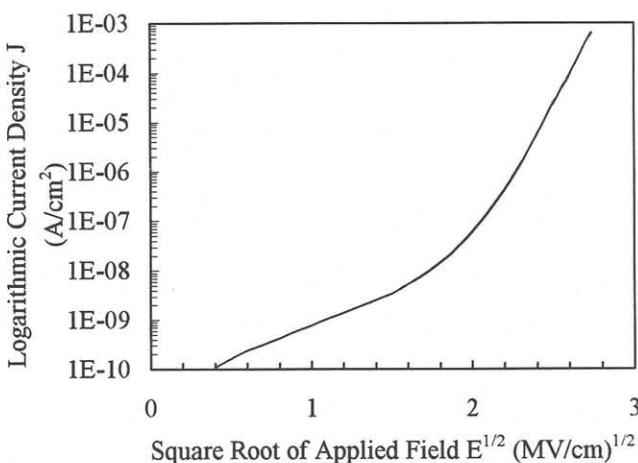


Figure 7. J-E characteristics of SiN film

5. a-Si TFT CHARACTERISTICS

The a-Si/SiN TFT fabricated in this study was of a simple inverted staggered gate structure with highly doped single-crystal silicon as the gate electrode, as shown in Fig.8 . The 200nm-thick SiN layer was deposited under the standard deposition conditions. The a-Si layer of 35 nm-thick was deposited by PECVD method at $300\text{ }^\circ\text{C}$. Al was evaporated directly onto the a-Si film for formation of electrodes.

Logarithmic drain current I_d is shown in Fig.9 as a function of gate voltage V_g for drain voltage V_d of $5V$. The threshold voltage and the mobility were $26V$ and $0.64cm^2/Vs$, respectively. The high threshold voltage will be caused by breaking vacuum between depositions of the SiN and a-Si films. The on/off current ratio was greater than 10^5 for all samples.

6. CONCLUSIONS

The SiN film has been deposited at temperatures as low as $350\text{ }^\circ\text{C}$ using Si_2Cl_6 and N_2H_4 gas mixture. The breakdown-field strength and the low-field resistivity of the film deposited at $400\text{ }^\circ\text{C}$ were $5.3MV/cm$ and more than $10^{15} \Omega \text{ cm}$, respectively. The a-Si TFT with this SiN film as gate

dielectric showed good device characteristics. The field-effect mobility and threshold voltage were $0.55cm^2/Vs$ and $26V$, respectively. Film properties appeared that this CVD gas combination is interesting as the TFT gate insulator.

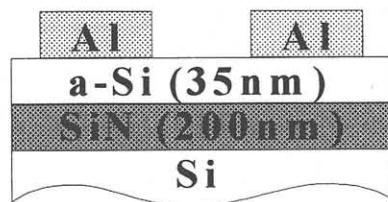


Figure 8. Cross sectional view of a-Si TFT

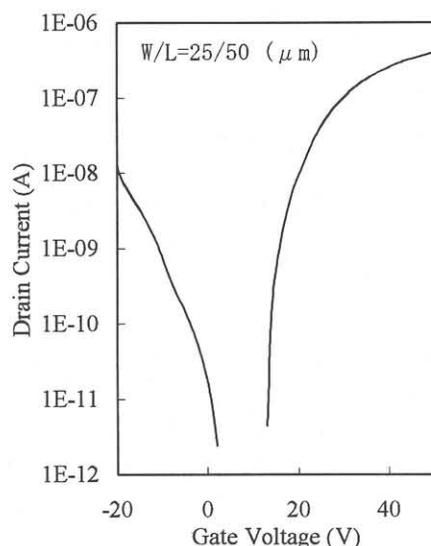


Figure 9. I_d-V_g characteristics of a-Si/SiN TFT

- 1) R. Ishihara, H. Kanoh, Y. Uchida, O. Sugiura and M. Matsumura, Mat. Res. Symp. Proc. **284** (1993) 3.
- 2) T. L. Chu, C. H. Lee, and G. A. Gruber, J. Electro-chem. Soc. **114** (1967) 717.
- 3) W. A. Lanford and M. J. Rand, J. Appl. Phys. **49** (1978) 2473.
- 4) I. Umezu and K. Maeda, Jpn. J. Appl. Phys. **30** (1991) 2547.
- 5) J. Frenkel, Phys. Rev. **54** (1938) 647.