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## High Quality Ultrathin TaO<sub>x</sub>N<sub>y</sub> Gate Dielectric Prepared by Nitridation of Ta<sub>2</sub>O<sub>5</sub>

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### 1. Introduction

Considering the technology roadmap, an equivalent oxide thickness of less than 1.5nm will be necessary to meet the requirements for sub-100nm MOSFET devices [1]. No alternative high dielectric constant materials, which are capable of meeting the above requirements for sub-100nm MOSFET devices have been reported to date. Although Ta<sub>2</sub>O<sub>5</sub> has been investigated in terms of MOS gate dielectric applications, it is difficult to obtain an equivalent oxide thickness of less than 2nm with acceptable leakage current [2,3]. It is known that nitrogen plasma annealing can significantly reduce leakage current and trap density [4]. In addition, it has been reported that the dielectric constant of Ta<sub>2</sub>O<sub>5</sub>, when deposited on a Ru layer was significantly improved by rapid thermal nitridation [5]. In this paper, we wish to report the TaO<sub>x</sub>N<sub>y</sub> thin film for gate dielectric applications in sub-100nm MOSFET devices.

### 2. Experiments

After cleaning a p-type silicon wafer using standard procedure, a 1nm-thick SiO<sub>2</sub> layer was grown by plasma oxidation in order to reduce the interface state density between Si and Ta<sub>2</sub>O<sub>5</sub>. 8-nm thick Ta<sub>2</sub>O<sub>5</sub> and TaO<sub>x</sub>N<sub>y</sub> films were deposited by MOCVD. In addition, we have prepared TaO<sub>x</sub>N<sub>y</sub> films by rapid thermal nitridation of Ta<sub>2</sub>O<sub>5</sub> films in NH<sub>3</sub>. For comparison, nitridation in ND<sub>3</sub> ambient was also performed. For some samples, an additional wet reoxidation was performed. After a 200nm-thick aluminum deposition, MOS devices were defined by photolithography and etching.

### 3. Results and Discussion

Fig. 1 shows the accumulation capacitance versus leakage current at -1.5V for as-deposited Ta<sub>2</sub>O<sub>5</sub> and TaO<sub>x</sub>N<sub>y</sub>. Compared with Ta<sub>2</sub>O<sub>5</sub>, TaO<sub>x</sub>N<sub>y</sub> exhibit better performance in terms of capacitance and leakage current. However, after rapid thermal oxidation, the capacitance of TaO<sub>x</sub>N<sub>y</sub> was almost the same as that of Ta<sub>2</sub>O<sub>5</sub>. Based on AES analysis as shown in Fig. 2, we found that a significant reduction of nitrogen concentration of TaO<sub>x</sub>N<sub>y</sub> after rapid thermal oxidation. Fig. 3 shows AFM roughness of Ta<sub>2</sub>O<sub>5</sub> and TaO<sub>x</sub>N<sub>y</sub>. Compared with Ta<sub>2</sub>O<sub>5</sub>, the roughness of TaO<sub>x</sub>N<sub>y</sub> is degraded with increasing annealing temperature. Considering capacitance, AES nitrogen profile, and roughness, we believe that the as-deposited TaO<sub>x</sub>N<sub>y</sub> is unstable under post deposition annealing step.

To solve the problems of as-deposited TaO<sub>x</sub>N<sub>y</sub>, we have prepared TaO<sub>x</sub>N<sub>y</sub> films by rapid thermal nitridation of Ta<sub>2</sub>O<sub>5</sub> films in NH<sub>3</sub>. Fig. 4 shows an XRD spectra of as-deposited Ta<sub>2</sub>O<sub>5</sub> films and processed via rapid thermal nitridation in NH<sub>3</sub> ambient. As-deposited and 700°C nitrided-Ta<sub>2</sub>O<sub>5</sub> were amorphous, while crystalline peaks were observed at 800°C nitrided Ta<sub>2</sub>O<sub>5</sub> film [6]. Fig. 5 (a) shows the C-V characteristics for as deposited Ta<sub>2</sub>O<sub>5</sub>, nitrided Ta<sub>2</sub>O<sub>5</sub>, oxidized Ta<sub>2</sub>O<sub>5</sub>, and reoxidized-nitrided Ta<sub>2</sub>O<sub>5</sub>. The accumulation capacitance of as deposited Ta<sub>2</sub>O<sub>5</sub> is approximately 11pF, indicating an equivalent oxide thickness of 2.8nm. After nitridation of Ta<sub>2</sub>O<sub>5</sub> in NH<sub>3</sub> at 700°C, the accumulation capacitance is approximately 19pF, which indicates an equivalent oxide thickness of 1.6nm. To

minimize leakage current, a wet reoxidation of the Ta<sub>2</sub>O<sub>5</sub> at 450°C for 10 minutes was performed as shown in Fig. 5(b). To obtain both a high accumulation capacitance and low leakage current, we performed nitridation, followed by a wet reoxidation. The findings show a significant improvement in the accumulation capacitance can be obtained without degradation of leakage current. To evaluate the effect of temperature on device characteristics, the nitridation and reoxidation at various temperatures were performed as shown in Fig. 6. As expected, the capacitance and leakage current increase with increasing nitridation temperature. In contrast, the capacitance and leakage current decreases with increasing reoxidation temperature. It was found that the wet reoxidation of nitrided Ta<sub>2</sub>O<sub>5</sub> at 450°C for 10min can reduce the leakage current over two order of magnitude with no detectable degradation in accumulation capacitance. Fig. 7 shows the nitrogen depth profile of the reoxidized-nitrided Ta<sub>2</sub>O<sub>5</sub> which was confirmed by Auger Electron Spectroscopy (AES). As expected, nitrogen incorporation was detected in the case of the nitrided-Ta<sub>2</sub>O<sub>5</sub>.

Recently, we have reported that the charge-trapping of ND<sub>3</sub> nitrided-SiO<sub>2</sub> was significantly less than that of NH<sub>3</sub> annealed samples [7]. To improve charge trapping characteristics, we have investigated ND<sub>3</sub> nitrided-TaO<sub>x</sub>N<sub>y</sub>. As shown in Fig. 8, we found a significant reduction of charge trapping characteristics for ND<sub>3</sub> nitrided-TaO<sub>x</sub>N<sub>y</sub>. In addition, charge-to-breakdown characteristics of ND<sub>3</sub> nitrided-TaO<sub>x</sub>N<sub>y</sub> was significantly improved as shown in Fig. 9.

### 4. Conclusion

TaO<sub>x</sub>N<sub>y</sub> have been investigated for use in gate dielectric applications of MOS devices. Nitridation of Ta<sub>2</sub>O<sub>5</sub> in ammonia ambient increases the dielectric constant and light reoxidation in a wet ambient reduces the leakage current. By optimizing the nitridation and reoxidation process, we obtained an equivalent oxide thickness as thin as 1.6nm and a leakage current of less than 10mA/cm<sup>2</sup> at -1.5V. We also confirmed nitrogen incorporation in the amorphous tantalum oxynitride (TaO<sub>x</sub>N<sub>y</sub>) by AES. Compared with NH<sub>3</sub> nitridation, nitridation of Ta<sub>2</sub>O<sub>5</sub> in ND<sub>3</sub> improves charge trapping and charge-to-breakdown. We conclude that TaO<sub>x</sub>N<sub>y</sub> thin film, formed by nitridation and wet reoxidation of Ta<sub>2</sub>O<sub>5</sub> is a promising alternative for future MOS gate dielectric applications.

### Acknowledgements

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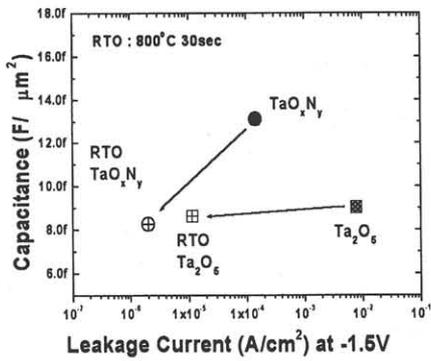


Fig. 1 Capacitance versus leakage current at  $-1.5V$  for  $Ta_2O_5$  and  $TaO_xN_y$  by MOCVD.

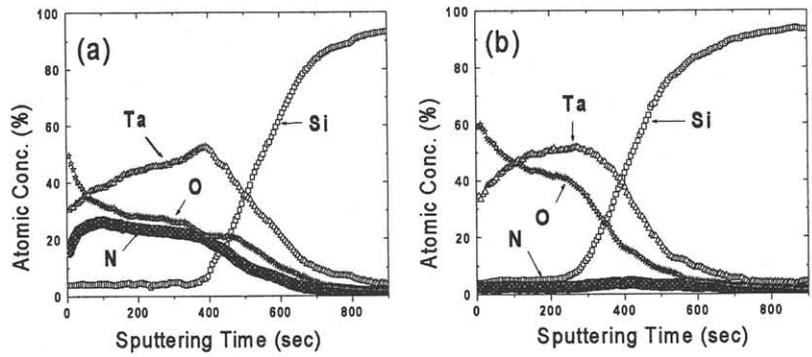


Fig. 2 AES depth profile of  $TaO_xN_y$  (a) As deposited (b) After RTO ( $800^\circ C$  30sec).

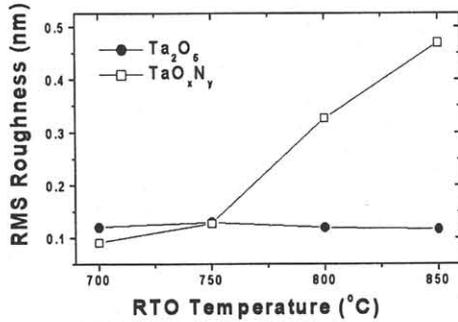


Fig. 3 AFM surface roughness versus RTO temperature.

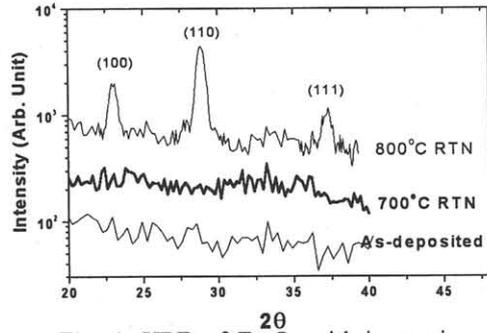


Fig. 4 XRD of  $Ta_2O_5$  with increasing annealing temperature.

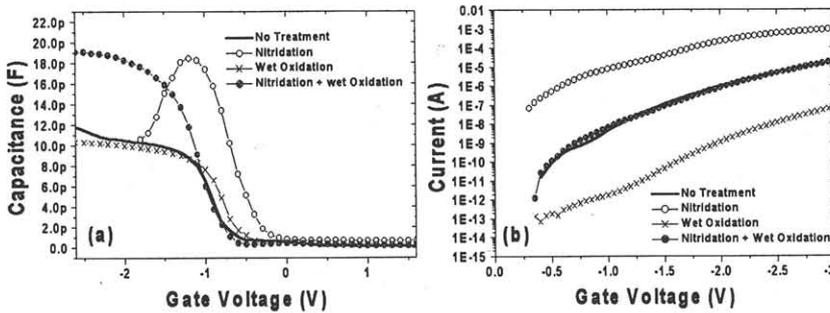


Fig. 5 (a) C-V characteristics for as deposited  $Ta_2O_5$ , nitrided- $Ta_2O_5$ , oxidized  $Ta_2O_5$ , and reoxidized-nitrided  $Ta_2O_5$ . (b) I-V characteristics.

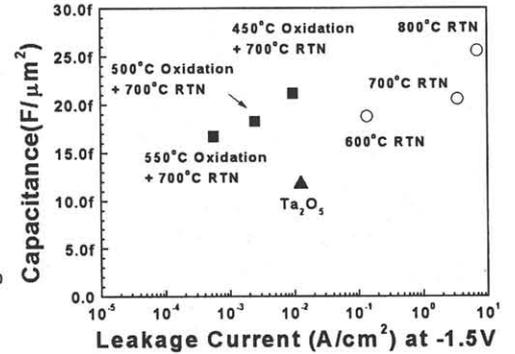


Fig. 6 Capacitance versus leakage current for various nitridation and reoxidation conditions.

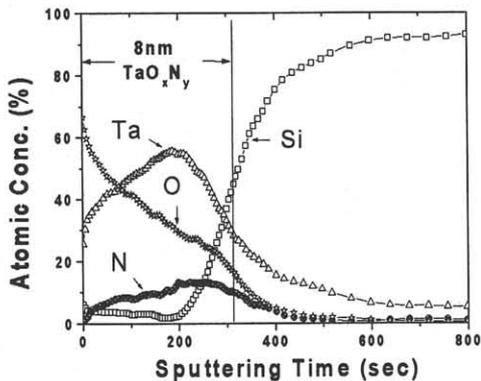


Fig. 7 Auger Electron Spectroscopy of 8nm-thick reoxidized-nitrided  $Ta_2O_5$ .

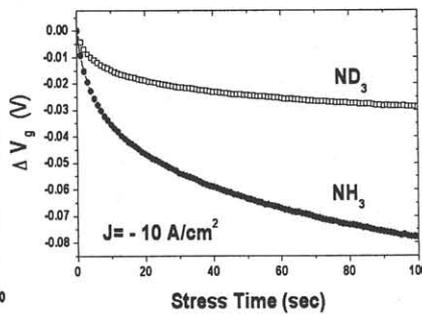


Fig. 8 Charge trapping characteristics of  $TaO_xN_y$  under constant current density of  $J_g = -10A/cm^2$ .

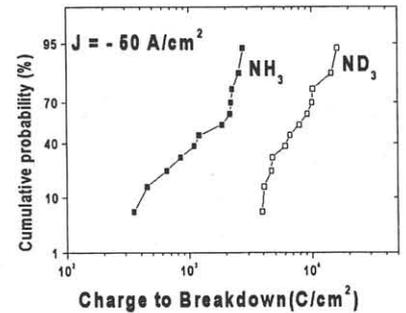


Fig. 9 Charge to breakdown characteristics under constant current stress.