F-5-2

Enhanced Quantum Effect for Sub-0.1 µm Pocket Technologies and Its Relevance for the On-Current Condition

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1. Introduction

The pocket-implant technology is promising for coming MOSFET generations [1–4], because the abrupter lateral channel/contact junctions can suppress short-channel effects (SCE) down to 25nm gate length (L_g) [2]. On the other hand, the quantum effect (QE) is also becoming remarkable with down scaling. It has been demonstrated that the inverse capacitance induced by QE causes a serious gate-voltage drop, which is usually modeled as an increase of the effective oxide thickness $\Delta t_{\rm ox}$ [5]. However, previous QE investigations have only treated the off-current condition. Here we analyze QE under the normal on-current operating condition for pocket-implanted MOSFETs.

2. Optimization of the Pocket Profile

A new analytical threshold voltage $(V_{\rm th})$ model of pocket implanted MOSFETs is based on the idea of $V_{\rm th}$ determination by the sum of carrier concentrations induced both in pocket and non-pocket regions [6]. Two parameters for the pocket profile ($N_{sub,p}$: maximum concentration; L_p : length of pocket penetration into the channel) are extracted from measured $V_{\rm th}$ - $L_{\rm g}$ characteristics. At increased drain voltage $(V_{\rm ds})$ the channel conduction due to the lateral electric field, also included in the model, becomes important and the contribution of the drain-side pocket decreases. Fig. 1 compares $V_{\rm th}$ calculated by the model and measurements for a $0.1 \,\mu\text{m}$ technology with 2.8nm oxide thickness

We use this new $V_{\rm th}$ -model to extrapolate to an opti-mized pocket profile for a 70nm technology on the basis of the results shown in Fig. 1. According to the SIA roadmap, $t_{\rm ox}$ and target $V_{\rm th}$ (long channel) are fixed to 1.5 nm and 0.2V, respectively. The extrapolation follows 3 steps: (i) The $V_{\rm th}$ - $L_{\rm g}$ characteristics are calculated with the developed $V_{\rm th}$ model, and optimal $L_{\rm p}$ and $N_{\rm sub,p}$ are chosen. (ii) The 2D pocket profile is then optimized with the chosen $L_{\rm p}$ and $N_{\rm sub,p}$ [7]. (iii) With the 2D profile QE is estimated. Steps (i)-(iii) are iterated until a self-consistent solution is obtained.

The result is depicted in Fig. 2. Fig. 2 b shows the op-timized pocket profile for the 70 nm-channel MOSFET. The optimal pocket penetration into the channel is up to the channel middle of $L_{g}=70$ nm to enhance the reverse SCE (RSCE). The $V_{\rm th}$ -change is given by $\Delta V_{\rm th}(L_{\rm g})$ $= \Delta V_{\rm th,SCE}(L_{\rm g}) + \Delta V_{\rm th,RSCE}(L_{\rm g}) + \Delta V_{\rm th,QE}(L_{\rm g}).$ We focus here on $\Delta V_{\rm th,QE}$, which has not been considered previously. The fact that $\triangle V_{\mathrm{th,QE}}(L_{\mathrm{g}})$ amounts to about 20% of $\Delta V_{\text{th,RSCE}}$ as shown in Fig. 3, allows to realize the required $V_{\text{th}}-L_{\text{g}}$ characteristics with reduced $N_{\text{sub,p}}$. Our optimized profile shows a factor 2 lower pocket concentration than necessary without the QE contribution.

However, these results are obtained under off-current condition. An important question is whether this effect is preserved also under on-current condition.

3. Quantum Effect at On-Current Condition

To include QE in the on-current condition we have to solve the Schrödinger, the Poisson, the current-density and the continuity equation at least in 2D. However, this is very computer-time consuming and it is also difficult to achieve stable solutions. Therefore, we solve the Schrödinger equation after obtaining a solution for the other three equations. This is done iteratively. Fig. 4a shows the results along the channel at $V_{\rm ds}$ =50mV and $V_{\rm gs}$ = $V_{\rm th}$ +0.5V. For comparison, results with an analytical equation [8] are depicted in the same figure

$$t_{\rm ox}^{\rm eff} = t_{\rm ox} + \alpha \left(Q_{\rm b} + \frac{11}{32} Q_{\rm i} \right)^{-\frac{1}{3}} \tag{1}$$

where $Q_{\rm b}$ and $Q_{\rm i}$ are the depletion and the inversion charge, respectively. α is determined by the effective mass. The analytical solution of eq. (1) is derived with a triangular potential approximation parametrized to the triangular potential approximation perpendicular to the channel and occupation of only the lowest energy level. Analytical and self-consistent calculation results are similar. This means the approximations are good enough. However, with increasing $V_{\rm ds}$, the triangular shape of the potential distribution disappears especially as the position moves from source to drain as demonstrated in Fig. 4 b. Fig. 5 compares energy-level results with the self-consistent simulation for $V_{\rm ds}$ =50mV and $V_{\rm ds}$ =1.2V. Without QE the ground state coincides with the potential bottom. Therefore we define the strength of QE by the energy difference between the potential bottom and the ground state. This difference is plotted in Fig. 6a along the channel. The difference has a peak at the channel middle for V_{ds} =50mV due to the pocket implantation. The large reduction of the difference at the drain edge for V_{ds} =1.2V means that the quantization is getting lost. In fact, the confinement of carriers is weakened at the drain edge as shown in Fig. 6 b. This means that the oncurrent quantization is smaller than expected from the usual off-current calculation.

To examine the influence of the change of the QE strength along the channel on the I-V characteristics, we perform a Monte Carlo simulation with FALCON considering QE by modifying the potential bending to reproduce the carrier distribution (Fig. 6b) and the surfaceroughness-scattering rate. These modifications are cali-brated so that the bulk mobility preserves the mobility universality. Fig. 7 shows the results for $L_g=70$ nm. The reduction of I_{ds} including QE is partly due to the mobil-ity reduction, which is not well verified yet. Nevertheless, it is obvious that the inclusion of the position dependent QE and the approximation of a homogeneous QE along the channel determined at the source side lead to equivalent results. The enhancement of $\Delta V_{\rm th,QE}$ for $V_{\rm ds}$ =1.2V is mainly due to a shift of the position determing $V_{\rm th}$ to the source side to a higher pocket concentration.

4. Conclusion

We have demonstrated that the pocket implantation causes an enhanced QE with reduced L_{g} under the offcurrent condition. Under the on-current condition QE becomes very position dependent. However, this dependence has a small influence on the on-current.

References

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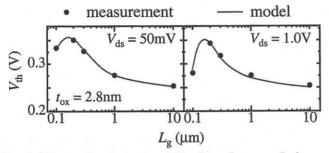


Fig. 1: Measured and simulated $V_{\rm th}$ dependence on $L_{\rm g}$ by using the developed $V_{\rm th}$ model for pocket implanted MOSFETs.

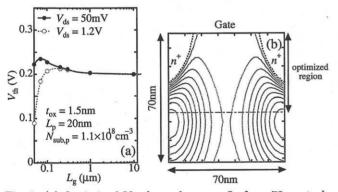


Fig. 2: (a) Optimized $V_{\rm th}$ dependence on $L_{\rm g}$ for a 70 nm technology with $t_{\rm ox}=1.5$ nm. (b) Pocket profile contours are optimized by approximating a Gaussian function. Contour lines are plotted from 6×10^{17} cm⁻³ to 4.2×10^{18} cm⁻³.

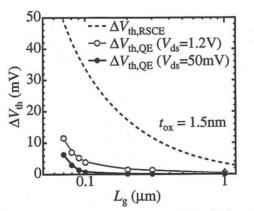
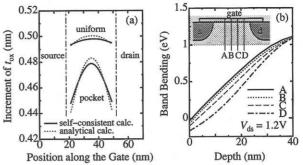
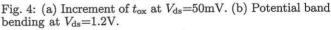


Fig. 3: Simulated increments $\Delta V_{\rm th}(L_{\rm g}) = V_{\rm th}(L_{\rm$





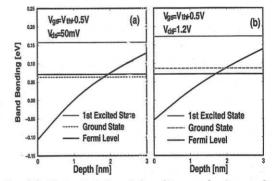


Fig. 5: (a) Potential band bending and eigen-values at $V_{\rm ds}$ =50mV, and (b) at $V_{\rm ds}$ =1.2V. These results are obtained at mid-channel position.

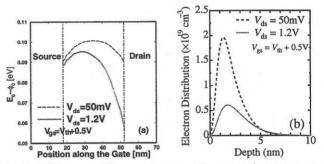


Fig. 6: (a) Energy difference between the ground-state energy of the longitudinal mode and the potential at the channel surface. (b) Electron distribution from surface to bulk at the drain edge.

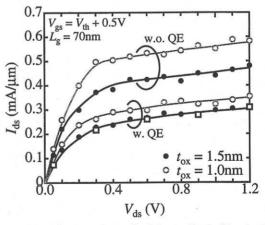


Fig. 7: $I_{\rm ds}$ - $V_{\rm ds}$ Characteristics by Monte Carlo Simulation for $L_{\rm g}$ =70nm. The results with squares consider the position dependent QE, whereas those with circles are for homogeneous QE along the channel.