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High-Performance Low-k Dielectric Using Advanced EB-Cure Process

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1. Introduction

The integration of the multilevel interconnection by damascene Cu with low-k dielectrics is the key technology for logic devices. Fundamental requirements include: high mechanical strength of low-k material to avoid the crack of film and scratch by CMP process, and lower thermal budget in order to control the grain growth and the diffusion of Cu. In general, the coated low-k film has poor mechanical properties. For the current formation of low-k spin-on dielectrics, 30-60 minutes is needs to be cured at 400°C, after the film coating and pre-baking. In this paper, we will present the impact of the EB cure process for 0.10µm-nodes multilevel interconnect technology.

2. Experimental

LKDTM material (JSR Corporation), ladder-type MSX (poly-methylsiloxane) low-k dielectric (k = 2.9), was used in this work. Samples were coated and pre-baked at 80°C and 200°C in succession for 1 minute, followed by EB cure process.

The EB cure cnamber has a dry pump and a gas supply unit, a heating single-wafer stage that controls up to 500°C and an EB unit including 19 EB tubes (Miniature Electron Tube Min-EB UEB-04: USHIO INC.).

The EB cure process was carried out as following conditions; 25keV of accelerating voltage, 400°C of the wafer temperature and 10 Torr (in N_2) of pressure in process chamber. Total dose and the dose rate are variable appropriate for the purpose. For these conditions, the thickness and refractive-index uniformity of EB-cured LKDTM film were very reasonable. In the following chapter, we will discuss the film properties for each EB cure condition (total dose and dose rate), comparing with thermal cure condition of 400°C for 30 minutes using the only hot-plate (HP).

3. Results and discussion

Figure 1 shows the modulus and dielectric constant for EB cured film as a function of dose. The modulus E, which is measured using the nanoindentation technique, has been

1.9 times bigger at 500μ C/cm² and 2.8 times bigger at 1000μ C/cm², as compared with the value of thermal cured film (*E*=8.1GPa). The improvement in mechanical strength induced 1/1000 or less velocity of a crack propagation in EB cured film, as compared with thermal cured film. Figure 2 shows the modulus and dielectric constant for EB cured film as a function of cure time, whicn is determined by dose rate. In Fig. 2, total dose is fixed at 500μ C/cm², with respect to lower thermal budget. As can be seen in Fig.2, EB cure needs shorter process time as 5 minutes to obtain the same k value of thermal cured film. Table 1 summarizes the properties of EB cured film against conventional HP cured one. It is remarkable that EB cure process is drastically effective in improving the mechanical strength of LKDTM film and reducing thermal budget.

Concerning the structure of EB cured LKD^{TM} film, both peak split around 1130cm⁻¹ for Si-O bond streching and peak intensity at 1270cm⁻¹ for Si-CH₃ bond decrease with increasing dose, as shown in Figure 3, which indicates FT-IR spectra for EB cured and thermal cured film. It means that Si-CH₃ bonds are broken up by EB and Si-O random-network are reconstructed. On the other hand the result of RBS analysis evidenced that carbon content in film don't change by the EB irradiation. In addition, the result of FT-IR analysis exhibited an existence of the polymer-like clusters with C-C bond, namely EB cured film has a hybrid structure between a SiO₂-based siloxane and carbon-based polymer. We believe that the properties of EB cured LKD^{TM} film is derived from this hybrid structure.

As for the integration with EB cured LKDTM film, we confirmed the restraint of gas penetration during ashing process, and equality in dry etching properties between EB cured film and conventional HP cured one. Figure 4 shows trench etch profile of EB cured and thermal HP cured film. Both films exhibit same characteristic. Further investgations are in progress.

4. Conclusion

We developed the EB cure process for low-k dielectrics. For JSR LKDTM film, the mechanical strength becomes 1.9 times stronger than conventional HP cured film, and cure time is shortened from 30 minutes in 5 minutes. These results show that the EB cure technique for the coated low-k film will be very useful in the LSI processes. Further the EB cure technology has possibility to apply to the other materials (ex. SOG, Photo resist etc.) for the LSI fabrication processes.

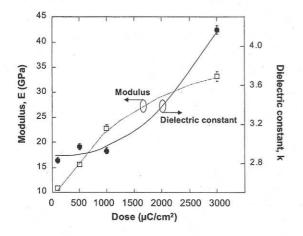


Figure 1 The modulus and dielectric constant for EB cured film as a function of dose. Modulus: square and broken line. Dielectric constant: solid circle and line.

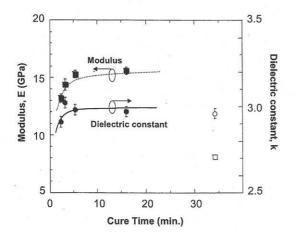


Figure 2 The modulus and dielectric constant for EB cured film as a function of cure time. Total dose is $500 \ \mu C/cm^2$. Modulus: square and line. Dielectric constant: solid circle and line.

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Table 1 Film Characteristic Comparison

EB Cure	Thermal Cure
2.0	30
2.91	2.90
1.392	1.382
1.34	1.25
13.2	8.1
2.2	1.4
	2.0 2.91 1.392 1.34 13.2

Dose : 500 μ C/cm²

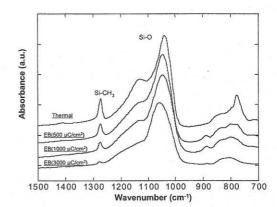


Figure 3 FT-IR spectra for EB cured and thermal cured film.

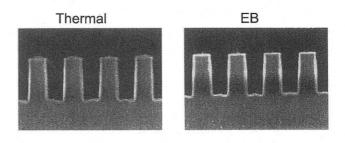


Figure 4 Trench etch profile of EB cured and thermal cured film. L/S=0.1625 $\mu m.$