Thermal Stability of Plasma-treated Ohmic Contact to n-GaN

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1. Introduction
Nitride-based electronic devices, such as heterostructure field effect transistors (HFETs) and heterostructure bipolar transistors (HBTs), are potentially very useful for high power and high temperature applications. To use these devices for such applications, good ohmic contacts with low contact resistance and good thermal stability are very important. In general, ohmic contacts to n-GaN with low contact resistance are not easily obtainable because of its wide bandgap. Surface treatment with plasma prior to metal deposition has been shown to be effective in improving the contact resistance. [1-6]. However, the thermal stability of the plasma-treated ohmic contacts has not been studied. In this work, the thermal stability of plasma-treated ohmic contacts was investigated by high temperature aging tests. We found that no obvious degradation of contact resistance was observed after aging at 600 °C for 2 hrs in N₂ and air ambient.

2. Experiment
The 2μm thick n-GaN films for this study were grown by metalorganic chemical vapor deposition (MOCVD) on c-plane sapphire substrates. The electron concentration and the mobility obtained by Hall measurement were 8.7x10¹⁶ cm⁻³ and 514 cm²/Vs, respectively. After layer growth, mesa patterns for transmission line measurement (TLM) were defined by photolithography. Prior to contact metal deposition, the samples were treated by Cl₂/Ar plasma using an inductive coupled plasma (ICP) system. Contact metal, Ti/Al/Ti/Au (200/1500/450/550 Å), was then deposited and lifted-off to form the contact pads. The samples were annealed at 750 °C for 30s in N₂ ambient. Aging tests were carried out at temperatures ranging from 400 to 600 °C for 2 hrs in N₂ and air ambient. The TLM measurement was performed for the determination of the contact resistance.

Table I. Etch conditions and TLM data

<table>
<thead>
<tr>
<th>No.</th>
<th>1</th>
<th>2</th>
<th>3</th>
<th>4</th>
<th>5</th>
<th>6</th>
</tr>
</thead>
<tbody>
<tr>
<td>ICP power (w)</td>
<td>X</td>
<td>300</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Bias power (w)</td>
<td>X</td>
<td>5</td>
<td></td>
<td></td>
<td></td>
<td></td>
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<tr>
<td>Pressure (morr)</td>
<td>X</td>
<td>15</td>
<td></td>
<td></td>
<td></td>
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<tr>
<td>Cl₂ flow (sccm)</td>
<td>X</td>
<td>50</td>
<td></td>
<td></td>
<td></td>
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</tr>
<tr>
<td>Ar flow (sccm)</td>
<td>X</td>
<td>30</td>
<td>10</td>
<td>15</td>
<td>20</td>
<td>30</td>
</tr>
<tr>
<td>Time (min.)</td>
<td>X</td>
<td>1</td>
<td>2</td>
<td>2</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>Rₑ (Ω · mm)</td>
<td>0.638</td>
<td>0.614</td>
<td>0.48</td>
<td>0.45</td>
<td>0.21</td>
<td>0.28</td>
</tr>
<tr>
<td>ρₑ (Ω · cm²)</td>
<td>621.0</td>
<td>656.3</td>
<td>692.2</td>
<td>696.3</td>
<td>668.3</td>
<td>671.5</td>
</tr>
<tr>
<td>ρₑ (μΩ · cm²)</td>
<td>6.6</td>
<td>5.7</td>
<td>3.4</td>
<td>2.8</td>
<td>0.68</td>
<td>1.2</td>
</tr>
</tbody>
</table>

3. Result and discussion
The conditions of the plasma treatment and the TLM results are shown in Table I. It should be noticed that a very low bias power of 5W was used in the plasma treatment to ensure a very slow etching rate for GaN. For the conditions used in here, only about 100-200 Å was etched during the plasma treatment. After annealing at 750 °C for 30s in a N₂ ambient, all samples showed good ohmic behavior. The contact resistances for samples with treatment time of 0, 1 and 2 min. (sample No. 1, 2, and 6), are 0.638, 0.614, and 0.28 Ω-mm, respectively. Hence, samples with a longer treatment time, 2 min., have much better improvement in contact resistance. Besides, we found that Ar flow rate could have a significant influence on contact resistance. Sample No. 5, which was treated with a flow rate ratio of Cl₂/Ar=50/20 for 2 min., in particular, showed the best electrical performance. Its contact resistance and specific contact resistivity are 0.21 Ω-mm and 6.8x10⁻⁷ Ω-cm², respectively.
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Clz/Ar=50/20, 2 min.
• No treatment
• Cl2/Ar=50/20, 1 min.
N2 ambient
Fig.1 The aging test results of contact resistance

Specific contact resistivity (Ω·cm²)

N2 ambient
• No treatment
• Cl2/Ar=50/20, 2 min.
• Cl2/Ar=50/20, 1 min.

0 50 100 150 200 250 300 350 400 450 500 550 600
Temperature (°C)

1E-5
1E-4
0.0
0.2
0.4
0.6
0.8
1.0
1.2
Contact resistance (Ω·mm)

0 50 100 150 200 250 300 350 400 450 500 550 600
Temperature (°C)

1E-6
1E-5
1E-4
Specific contact resistivity (Ω·cm²)

Fig. 2 The aging test results of specific contact resistivity

Fig. 1 and 2 show the aging test results of the contact resistance and specific contact resistivity with temperatures ranging from 400 to 600 °C in N₂ ambient. As a whole, there is no obvious degradation observed after aging at 600 °C for 2 hrs. It is generally believed that the improvement of contact resistance after plasma treatment is mainly due to the formation of nitrogen vacancies on the wafer surface [1-6]. Although the effect of high temperature aging on the recovery of the plasma-induced damages is largely unknown, the results obtained here indicate that this annealing effect does not lead to electrical degradation of plasma-treated ohmic contacts. For samples that were aged in air ambient, similar results were obtained as well, despite the metal surface seems to be oxidized to some degree. In addition, the electrical performance of plasma-treated ohmic contacts was measured at high temperatures. At 300 °C, sample No. 5 still has a very low contact resistance (0.412 Ω·mm) and specific contact resistivity (9.3x10⁻⁷ Ω·cm²), which are lower than those measured at room temperature of samples without plasma treatment (sample No. 1).

4. Conclusion

With appropriate plasma treatment, ohmic contacts to n-GaN have lower contact resistance than those without plasma treatment. No obvious degradation of contact resistance was observed after aging tests in N₂ and air ambient, showing the thermal stability of plasma-treated ohmic contacts would not be influenced by the recovery of plasma-induced damages on the wafer surface. Moreover, plasma-treated ohmic contacts have lower contact resistance even at high temperatures. This is a great benefit to the nitride-based HFETs used for high power and high temperature applications.

Acknowledgments

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Reference