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Fluoride Resonant Tunneling Diodes Using Cd-rich $\text{Ca}_x\text{Cd}_{1-x}\text{F}_2/\text{CaF}_2$ Heterostructures on Si Substrates

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1. Introduction

Si-based resonant tunneling diodes (RTDs) are very attractive because compact circuit composed of small number of devices can be constructed in Si integrated circuits. Recently, Si-based RTDs with good characteristics at room temperature have been reported using heterostructure such as Si/SiGe^[1], $\text{Al}_2\text{O}_3/\text{Si}$ ^[2] and SiO_2/Si ^[3]. We have investigated RTDs using fluoride heterostructure, CaF_2 (barrier) / CdF_2 (well), on Si(111) substrates^[4] as a candidate of Si-based RTDs. This structure has large conduction band discontinuity, and extremely large peak to valley current ratio (PVR) has been reported^[5]. The co-integration of the fluoride RTDs with Si-MOSFETs was also reported^[6]. However, some critical problems in durability for fabrication process of such integrated-type devices were found.

An origin of these problems is instability of CdF_2 for high temperature or wet treatment. From the viewpoint of growth temperature, the optimum growth temperature of CdF_2 is very low (around at room temperature) compared to that of CaF_2 (around at 750°C). Thus the layered structure such as $\text{CaF}_2/\text{CdF}_2/\text{CaF}_2$ should be grown at very low temperature after the first CdF_2 layer has been grown. Furthermore, thermal budget in the post growth process is very limited. These limitation leads to degradation of heterostructure quality and device characteristics.

We have investigated basic growth characteristics of $\text{Ca}_x\text{Cd}_{1-x}\text{F}_2$ alloy, in which possibility of higher growth temperature than that for pure CdF_2 was indicated^[7]. In this work, use of $\text{Ca}_x\text{Cd}_{1-x}\text{F}_2$ alloy was studied to overcome these problems. Energy band structure of this alloy system is not clear at present, however, it is predicted that low composition alloy ($x = 0.2$ or less) should have low conduction band edge (E_c) level enough to use for quantum well layer under an assumption of the Vegard's law. The growth characteristics of the Cd-rich $\text{Ca}_x\text{Cd}_{1-x}\text{F}_2$ alloy was investigated, and RTDs were fabricated to show the feasibility of application to quantum well layers.

2. Growth characteristics of Cd-rich $\text{Ca}_x\text{Cd}_{1-x}\text{F}_2$ alloy films

The 100 nm of Cd-rich $\text{Ca}_x\text{Cd}_{1-x}\text{F}_2$ alloy including pure CdF_2 layers were grown at 200°C on the 1.2 nm-thick CaF_2 buffer layers grown on Si(111) substrate at 750°C by molecular beam epitaxy (MBE). The growth temperature of 200°C was predicted to be too high for pure CdF_2 to obtain good properties from previous studies. These layers were characterized by atomic force microscopy (AFM) and double-crystal X-ray diffraction (XRD).

Figure 1 shows surface morphologies of pure CdF_2 ($x = 0$) layer and alloy ($0.1 \leq x \leq 0.3$) layers. The alloy layers exhibited fairly smooth surface, while pure CdF_2 formed rough island structure.

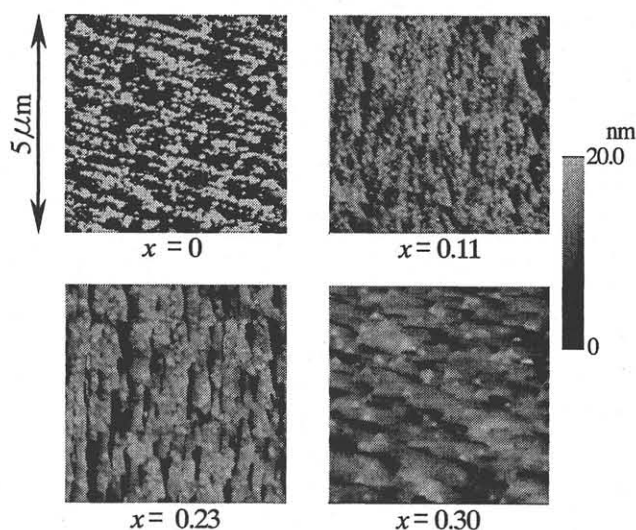


Fig.1 Composition dependence of surface morphologies of $\text{Ca}_x\text{Cd}_{1-x}\text{F}_2$ alloy layer ($0 \leq x \leq 0.3$) grown at 200°C . $5\mu\text{m} \times 5\mu\text{m}$ AFM images.

Figure 2 shows results of XRD measurements for the samples of similar structure. The single peak position shifts to lower angle with increasing composition, x , which indicates formation of uniform alloy layers.

Fig.3 shows the RMS values obtained from the AFM observations and FWHM values of rocking curves obtained from the XRD measurements depending on composition, x . Both values decreased drastically by slight addition of CaF_2

($x = 0.1$) to pure CdF_2 for the growth at 200°C .

These results show that Cd-rich $\text{Ca}_x\text{Cd}_{1-x}\text{F}_2$ alloy layers can be grown with good properties at 200°C , which would be too high temperature for pure CdF_2 .

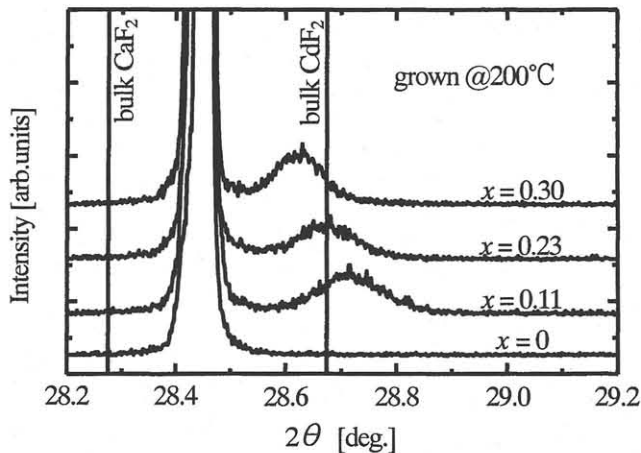


Fig.2 XRD spectra ($2\theta/\theta$ scan) depending on alloy composition, x .

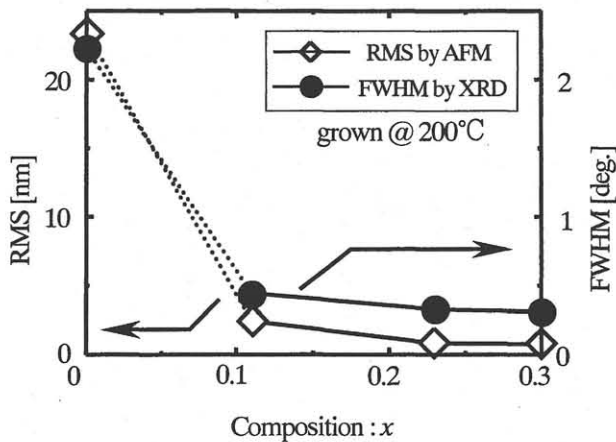


Fig.3 Composition dependence of RMS value of surface roughness and FWHM value of θ -scan rocking curves.

3. Fluoride RTD using Cd-rich alloy well

RTDs in which the Cd-rich alloy were employed for well layers were fabricated and compared with conventional pure CdF_2 -well layer, as shown in Figure 4. Here the first 1.2-nm-thick CaF_2 buffer layer was grown at 750°C commonly, and the following heterostructure with pure CdF_2 -well was grown at room temperature, whereas that with Cd-rich alloy-well was grown at 200°C . I - V characteristics of these RTDs shows that the lager PVR was obtained for the alloy-well structure. The normal operation of the alloy-well RTDs indicates that the E_c level of the alloy is low enough. And the better performance is considered to be due to the higher growth temperature for the fluoride heterostructure.

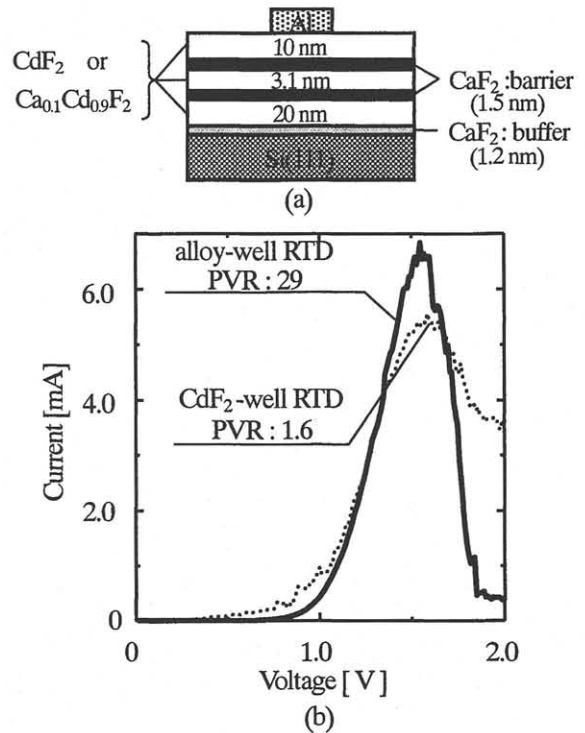


Fig.4 (a)RTD structures with $\text{CdF}_2/\text{CaF}_2$ grown at room temperature or $\text{Ca}_{0.1}\text{Cd}_{0.9}\text{F}_2/\text{CaF}_2$ grown at 200°C . (b) I - V characteristics of these RTDs.

4. Conclusion

Cd-rich $\text{Ca}_x\text{Cd}_{1-x}\text{F}_2$ alloy ($x \sim 0.1$) was introduced to well layers of fluoride RTDs on Si substrates, which was substituted for conventional pure CdF_2 . Higher temperature (200°C) growth of the fluoride heterostructure was achieved by this method. The alloy-well RTDs showed larger PVR than that of pure- CdF_2 -well RTDs. This indicates that the Cd-rich alloy has low E_c level, and that heterostructure with better quality was obtained due to the higher temperature growth.

Reference

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