

Origin of Enhanced Thermal Noise for 100nm-MOSFETs

S. Hosokawa, Y. Shiraga, H. Ueno, M. Miura-Mattausch, H. J. Mattausch¹,
T. Ohguro², S. Kumashiro², M. Taguchi², H. Masuda², and S. Miyamoto²

Graduate School of Advanced Sciences of Matter, ¹Research Center for Nanodevices and Systems,
Hiroshima University, 1-3-1, Kagamiyama, Higashi-Hiroshima, 739-8526, Japan
Phone: +81-824-24-7637 Fax: +81-824-22-7195 E-mail: sts@hiroshima-u.ac.jp

²Semiconductor Technology Academic Research Center, 3-17-2, Shin-Yokohama, Kanagawa, 222-0033, Japan

1. Introduction

The thermal noise (TN) increases in importance as RF application of MOSFETs is becoming realistic. However, to achieve accurate measurement is still a serious task, and thus to realize its accurate modeling is an urgent task for accurate simulation of RF-circuits. Our aim is to determine the origin of observed TN enhancement, and to develop a reliable model based on this origin, which can be even used for testing and supplementing measurements.

The thermal noise is independent of applied frequency. Thus the Nyquist theorem describes the spectral intensity of the TN current (S_{id}) of a MOSFET at temperature T [1, 2] by

$$S_{id} = 4kT \int g_{ds}(y) dy = 4kT g_{ds0} \gamma \quad (1)$$

where k , $g_{ds}(y)$, g_{ds0} , γ are Boltzmann's constant, position dependent channel conductance, channel conductance at $V_{ds}=0$, and noise coefficient, respectively. Origin of TN is attributed to the carrier fluctuation in a time interval. The Nyquist theorem predicts that γ varies from 1 to 2/3 as a function of V_{ds} for long-channel transistors as shown in Fig. 1 schematically. This prediction has been proved experimentally for long-channel transistors. For short-channel transistors, Knoblinger et al. measured much larger γ than 2/3, even more than 3 in the saturation region [3], and explained this result by hot electrons [4]. Jamal Deen et al. have measured smaller values than Knoblinger, and modeled S_{id} only with the effective channel length (L_{eff}) considering the channel length modulation as shown in Fig. 2 [5]. Their result shows a monotonically increasing γ characteristics as a function of V_{ds} for the gate length (L_g) of 0.18 μ m, similar to the Knoblinger result. Recently, Scholten et al. measured that γ for $L_g=0.18\mu$ m is about 1 in the saturation region [6]. They explained the enhanced γ by the velocity saturation. However, detailed description of the model is not given.

2. Theoretical Investigation

We followed the Nyquist theory given in Eq. (1). For the g_{ds} description we applied HiSIM, a circuit simulation model based on the drift-diffusion approximation [7], which makes the g_{ds} description valid for any bias conditions. The final TN description is verified by calculated γ , which should give an universal relationship without varying from technology to technology. Fig. 3 shows the calculation result including both previously proposed explanation, the velocity saturation [6] and the channel-length modulation but with a constant mobility [5] by symbols.

Calculated γ characteristics are nearly the same for all gate lengths, and expected γ increase is not obtained. From this fact a conclusion is derived that the previous explanations are not sufficient and the origin of the enhanced TN is missing in the modeling.

We have extended the TN description including the channel-position dependence of all physical quantities such as mobility and carrier concentration. The final equation is a function of surface potentials at source & drain sides and their derivatives. Calculated γ with this model is also depicted in Fig. 3 and shows the expected behavior. Thus the reason of the γ increase for short-channel MOSFETs is attributed to the position dependence of physical device quantities, which can be reduced to the surface potential distribution along the channel. The basis of the Nyquist theorem is the carrier fluctuation by scattering, and this is enhanced by the potential increase along the channel. HiSIM distinguishes potential values at source & drain sides, which allows to include the potential gradient in a consistent way, and thus to model the enhanced TN of short-channel MOSFETs correctly.

3. Calculation Results

Calculated TN characteristics are compared with the Scholten measurements in Fig. 4. Required model parameters for the calculation were extracted from measured current-voltage characteristics with a normal parameter extraction. Without any fitting parameter good agreement can be achieved for any channel length and any bias conditions. This concludes that the TN characteristics are determined only by the carrier transport in the channel, and the origin of the carrier transport is the potential difference along the channel. The difference between TN and other device characteristics such as the drain current is that the gradient itself, namely g_{ds} , determines the TN characteristics. Fig. 5 shows the γ characteristics as a function of V_{ds} . By reducing L_g , γ increases, but not so drastic as reported previously [3]. It is also seen that γ for short L_g length reduces in the liner region, and starts to increase as V_{ds} enters the saturation condition, causing steep potential increase in the channel. The minimum of γ becomes larger than 2/3 as reduction of L_g is further continued.

4. Conclusion

We found that the enhancement of TN for short-channel MOSFETs is caused by the potential gradient in the channel. A model developed on the basis of this concept reproduces measured noise characteristics without any fitting parameter. This provides the possibility to even predict noise characteristics for accurate circuit simulation.

References

- [1] H. Nyquist, Phys. Rev., **32**, p.110 (1928).
- [2] A. van der Ziel, "Noise in Solid State Devices and Circuits", New York Wiley (1986).
- [3] G. Knoblinger, P. Klein, M. Tiebout, IEEE J.Solid-State Circuits, **36**, No. 5, p.831 (2001).
- [4] R. P. Jindal, IEEE Trans. Elec. Dev. **ED-33**, No. 9, p.1395 (1986).
- [5] M. Jamal Deen *et al.*, Proc. of MSM, p.694 (2002).
- [6] A. J. Scholten *et al.*, Tech. Dig. of IEDM, p.129 (2002).
- [7] <http://www.starco.or.jp/kaihatu/pdgr/hisim/index.html>

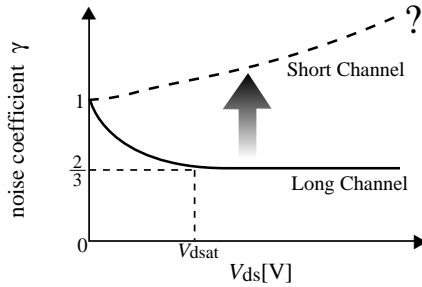


Figure 1: Schematic of noise coefficient γ as a function of V_{ds} for a long channel MOSFET. The dashed line shows that given in [4].

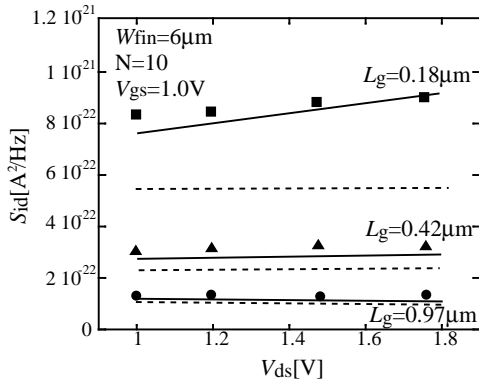


Figure 2: Comparison of measured thermal noise current (symbols) with calculated results as a function of V_{ds} given in [5]: with the channel length modulation (solid lines) and without the channel length modulation (dashed lines).

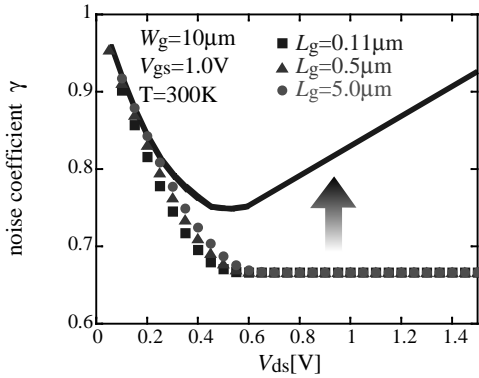


Figure 3: Calculated noise coefficient γ with a constant mobility μ (symbols) and that considering the channel-position dependence of μ and other device parameters (solid line) for a short channel.

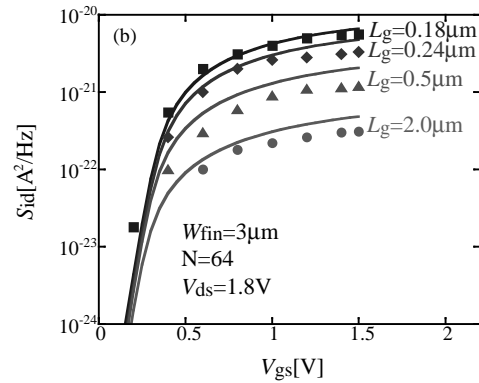
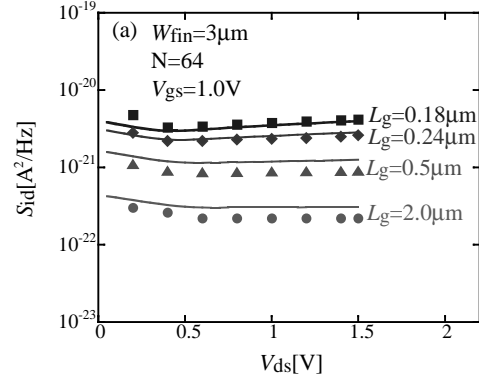


Figure 4: Calculated thermal noise current characteristics with the developed model (solid lines) compared with measurements (symbols) given in [6], (a) as a function of V_{ds} and (b) as a function V_{gs} .

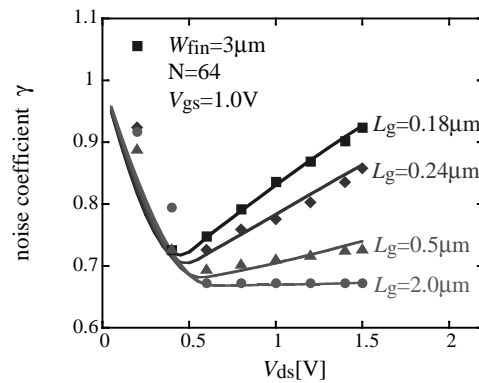


Figure 5: Calculated noise coefficient γ with the developed model as a function of V_{ds} (solid lines) compared with the transformed Scholten measurements into γ , where the g_{ds0} values are selected so that γ is unity at $V_{ds}=0$.