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The novel method to improve electrical characteristics of *p*-type GaN by using Ni catalysis

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Abstract

GaN and related compounds have attracted much attention as prospective candidates for wide band-gap optical devices such as short-wavelength light-emitting diodes or laser diodes [1-3]. Although the performance of these devices has been developed owing to a lot of breakthroughs in crystal growth and carrier control [4-5], several problems remain to be solved. In this paper, we used different conditions of Ni films and annealing temperature to research the electric property of *p*-type GaN, and further, control current spreading to improve LED performance.

Samples used in this study were all grown by metalorganic chemical vapor deposition (MOCVD). We first prepared a low-temperature GaN, an undoped GaN, and a *p*-type GaN epitaxial layer sequential onto the *c*-face (0001) sapphire substrates. The thickness of the GaN buffer layer was 30 nm. After growth of buffer layer, the 2 μ m undoped GaN film was grown on buffer layer. Then we grow the 1 μ m *p*-type GaN epilayers.

This as-grown wafer was cut into 5 mm \times 5 mm pieces. Ni films with the thickness of approximately 2 nm, 10 nm, 20 nm, 50 nm were entirely deposited on the surface of the sample for activation by annealing at temperatures ranging from 200°C to 800°C in N₂ ambient gas for 10 min. After removing the Ni film and native oxides on the

surfaces, all samples were cleaned in 50% HCl for 30 min at room-temperature, rinsed in DI water for 1 min, and dried by a N₂ blower.

Figure 1 shows the hole concentration as a function of the annealing temperature. It can be seen that the hole concentration of $4.35 \times 10^{17} \text{ cm}^{-3}$ was achieved by annealing at 400°C by this method (Ni=10nm). The highest hole concentration of $3.25 \times 10^{18} \text{ cm}^{-3}$ with the mobility of 0.922 cm²/Vs was obtained at the annealing temperature of 600°C (Ni = 2 nm). This hole concentration of *p*-GaN with the Ni film is one order of magnitude higher than that of the sample without that at this annealing temperature. The Typical I-V curves of *p*-type GaN samples with and without Ni films annealed at 600°C in N₂ ambient for 10 min were shown in Fig. 2. It was found that annealing in this thick Ni film and such high temperature range of 700°C to 800°C didn't yield a better I-V curve than as-grown sample. Nevertheless, annealing temperature at 600°C with thin Ni film (2 nm) can obtained better I-V curve. The results shown in Fig. 2 indicated that the proper deposition thin Ni film and low annealing temperature are the critical conditions to improve the electrical properties of *p*-type GaN.

Figure 3 shows the I-V curve dropped abruptly from ohmic to Schottky state when samples annealed at 600°C after etching 0.3 μ m, 0.6 μ m, via ICP system. The results

revealed Ni-compound materials are the key factor in electrical characteristic. However, we must be careful with NiH to disturb electronic distribution of the p-type GaN.

In summary, the optimum result of concentration and I-V characteristic are deposited 2 nm Ni film, and then, annealed at 600°C in N₂ for 10 min in our study. We observe that the Ni-compound materials were to form in the surface of p-type GaN, such as, the formation of NiO reduces specific resistance between contact and p-type GaN, NiH considerably perturb the electronic distribution of the surface of p-type GaN. In the future, we will continue to study the mechanism of Ni catalytic, Ni-compound materials and control current spreading to improve the light intensity of GaN-based LED.

Acknowledgements

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References

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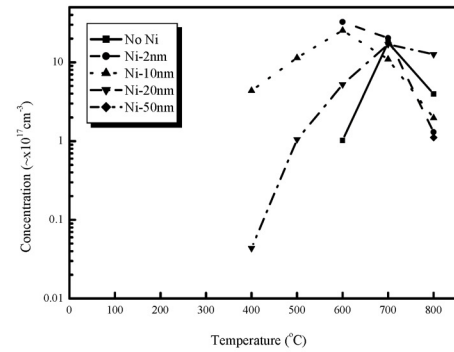


Fig. 1 The hole concentration as a function of the annealing temperature with Ni films and without Ni films.

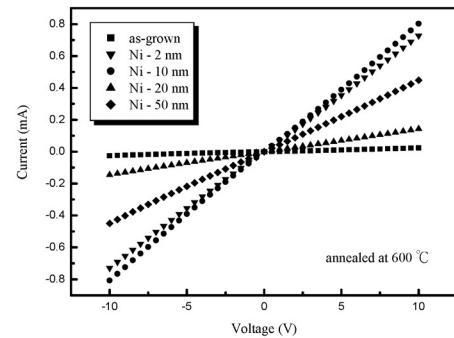


Fig. 2 Typical I-V curves of p-type GaN samples with and without Ni film annealed at 600°C in N₂ ambient for 10 min.

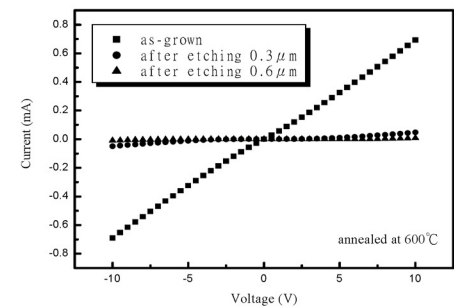


Fig. 3 Typical I-V curves of p-type GaN samples with Ni film (10 nm)