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Recent Advances on GaN Power Devices

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Introduction

In recent years, saving of energy consumption and reduction of CO₂ emission are recognized as global targets. Power electronics is a key technology for the subjects and, especially, development of power devices is the most important issue. In the present time, silicon-based power devices are widely used. However, the limit of the Si-based devices becomes apparent. So that, wide band-gap semiconductors, such as SiC and GaN, have been recently studied as novel power devices which breakthrough the Si limit.

Though GaN has some advantages over SiC such as wide band-gap, high electron mobility, hetero-junction, many of developed electronic devices based on GaN have been for high frequency devices. However, taking advantage of the impressive result of a lateral insulated-gate AlGaIn/GaN HEMTs¹⁾, studies of GaN power devices has been increasing. In this presentation, recent progress in the development of high-voltage GaN power devices is reviewed.

Schottky rectifiers

Until recently, all the GaN Schottky diodes reported are either lateral or quasi-vertical in structure²⁾ because of the lack of an electrically conducting GaN substrate. Though many dislocation of $\sim 10^9$ cm⁻² is in the GaN epi-layer on sapphire substrates, high breakdown voltage as high as 9.7kV been reported³⁾ with the lateral structure. In the lateral structure, direction of electric field is normal to the direction of the dislocation, which results in high breakdown voltage.

GaN substrates made with HVPE are recently supplied commercially. Using the GaN substrates, a pn diode has been fabricated on an n⁺ GaN substrate. Breakdown voltage over 900V has been obtained⁴⁾. The GaN substrate has still about 10^6 cm⁻² dislocations. This result suggests that the dislocations don't seriously affect the breakdown voltage, but need to be done to ensure long-term reliability.

AlGaIn/GaN HEMT power devices

First demonstration of an AlGaIn/GaN HEMT for a switching application was the report by Zhang et al¹⁾. The breakdown voltage of 1.3kV and on-resistance of 1.7mΩcm² have been reported using lateral insulated-gate structure. The insulated-gate structure is effective to obtain high breakdown voltage because of the reduction of gate leak current and electric field crowding at gate edge. However, the threshold voltage V_{th} shifts to negative direction because of insertion of the low series capacitance. Figure1 shows I_d - V_g characteristics of the Schottky gate HEMT (SG-HEMT) and insulated gate HEMT (IG-HEMT). In this case, 50nm SiO₂ film shifts V_{th} from -4V to -25V⁵⁾. To suppress the shift of V_{th} , high-k materials such as HfO₂, TiO₂ and HfAlO, have been studied as the insulator. Breakdown voltage of 2kV has been reported using TiO₂/SiN insulator⁶⁾.

The other method for high breakdown voltage is field plate structure. The field plate structure shown in fig.2 suppresses the electric field crowding at the gate edge and current-collapse. However, the effect of the field plate is sensitive to the design⁷⁾.

Recent trend

Until now, almost all developed GaN transistors are normally-on in the operation because the technology of GaN power devices was based on that of high frequency devices. Normally-off operation is more desirable for power switching applications. So that, studies on normally-off operation have been projected. To realize the normally-off operation based on HEMT structure, various methods have been proposed, for example, a recess gate structure, a p-type gate structure, F ion treatment, use of non-polar surface. However, though the expected V_{th} as a normally-off operation is above 5V, many of their V_{th} are near 0V.

Clear normally-off operation has been reported using conventional MOSFET structure⁸⁾. The V_{th} was 3.3V and the maximum channel mobility

was $167\text{cm}^2/\text{Vs}$, which is higher than that of SiC MOSFET.

As another structure, vertical device with normally-off operation using GaN substrate has been demonstrated. The device structure is shown in Fig.3. A vertical device has some advantage compared with a lateral device, such as easy wiring, small device size and collapse free. Development of large size GaN substrate is a key for the vertical devices.

Summary

History of GaN power devices is shorter than that of SiC power devices. Nevertheless, performance of the GaN power device has been progressing rapidly. To overtake SiC devices, total progress of technologies for GaN power devices, such as GaN substrates, processing and device design, is needed.

- 1) N.-Q. Zhang, B. Moran, S.P. DenBaars, U.K. Mishra, X.W. Wang and T.P.Ma, Phys. Status Solii a **188**, 213(2001).
- 2) A.P. Zhang, J.W.Dang, F. Ren, H. Cho, K.P. Lee, S.J. Pearton, J.I. Chui, T. Nee, and C. Chuo, IEEE Trans. Electron. Dev. **48**, 407(2001).
- 3) A.P. Zhang, J.W. Johnson, F. Ren, A.Y. Polyakov, N.B. Smirnov, A.V. Govorkov, J.M. Redwing, K.P. Lee, and S.J. Pearton, Appl. Phys. Lett. **78**, 823(2001).
- 4) Y. Yoshizumi, S. Hashimoto, T. Tanabe, and M. Yoneyama, 23a-ZE-6, Extended Abstracts (The 53rd Spring Meeting, 2006); The Japan Society of Applied Physics and Related Societies.(in Japanese)
- 5) M. Sugimoto, M. Kodama, N. Soejima, E. Hayashi, T. Uesugi, and T. Kachi, Proceedings of the 17th International Symposium on Power Semiconductor Devices & IC's, 307(2005).
- 6) S. Yagi, M. Shimizu, Y. Yamamoto, and G. Piao, International Semiconductor Device Research Symposium WP7-02-02.
- 7) W. Saito, Y. Takada, M. Kuraguchi, K. Tsuda, I. Omura, and T. Ogura, Extended Abstracts of the 2003 International Conference on Solid State Devices and Materials, 924(2003).
- 8) W. Huang, T. Khan, and T.P. Chow, Proceedings of the 18th International Symposium on Power Semiconductor Devices & IC's, 309(2006).

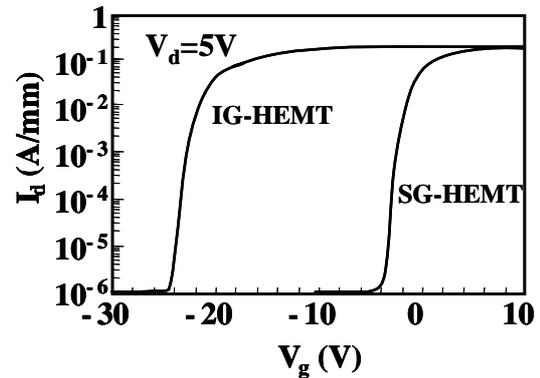


Fig.1 Comparison of V_{th} for SG-HEMT and IG-HEMT.

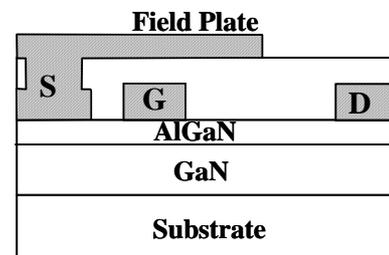


Fig.2 Field plate structure for high breakdown voltage.

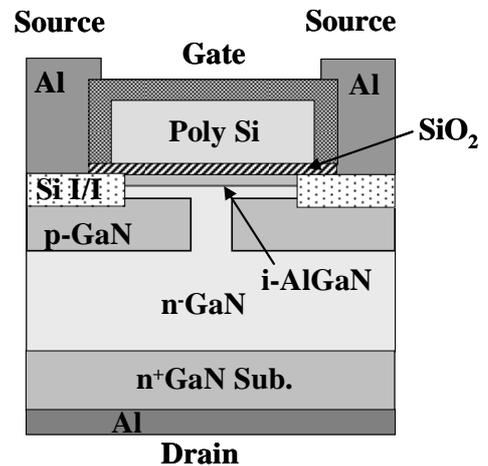


Fig.3 Vertical device using GaN substrate.