# Influence of High Dielectric Constant in Gate Insulator on Remote Coulomb Scattering due to Gate Impurities in Si MOS Inversion Layer

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# 1. Introduction

It has been reported that inversion-layer mobility ( $\mu_{eff}$ ) is significantly degraded in gate oxides thinner than critical thickness [1]. Although reliable experimental results [2,3] suggest that this mobility lowering can be associated with remote Coulomb scattering (RCS) due to ionized impurities in poly-Si gate, the mechanism of RCS has not been fully understood yet. Recent calculation [4] suggested that the dependence of remote Coulomb scattering potential  $\phi$  on dielectric constant  $\epsilon$  in stacked-gate structure can have a maximum with increasing  $\epsilon$  (Fig.1(a)).

In this paper, we experimentally examine  $\mu_{eff}$  of MOS-FEETs with SiON gate insulator, in order to clarify the influence of high dielectric constant  $\varepsilon$  in gate insulator on remote Coulomb scattering due to gate impurities. We discuss how is the trade-off between the " $\varepsilon$  effect" and "r effect" under EOT=const. (Fig.(b)).

## 2. Experimental

Since SiON gate insulator has large amount of trap charges and interface states, characterization of mobility in SiON has not been sufficient as compared to that in pure SiO<sub>2</sub>, and hence it is not easy to extract the RCS component ( $\mu_{RCS}$ ). In this study, we fabricated MOSFETs with different poly-Si gate impurity concentration ( $N_{poly}$ ) values of  $5x10^{15}$  cm<sup>-2</sup> (high dose) and  $2x10^{15}$  cm<sup>-2</sup> (low dose) (Fig.2). Since  $\mu_{RCS}$  has strong  $N_{poly}$  dependence [3], the comparison of  $\mu_{eff}$  between different  $N_{poly}$  samples enables us to detect the behavior of RCS due to gate impurities, even in SiON case.

The devices used in this study were conventional n-MOSFETs with gate insulators of  $SiO_2$  (1.5-2.1nm) and SiON (1.8-2.4nm). The thickness of gate insulators was determined with high resolution TEM.

## 3. Results and discussion

## 3.1. Influence of dielectric constant on RCS

In order to examine the influence of dielectric constant on RCS ( $\epsilon$  effect), inversion-layer mobility in the SiON sample was compared to that in the SiO<sub>2</sub> sample with almost the same insulator thickness (Fig.3). Although  $\mu_{eff}$  in SiON is degraded compared to that in SiO<sub>2</sub>, there is no N<sub>poly</sub> dependence of  $\mu_{eff}$  at 300K (Fig.4). The  $\mu_{eff}$  degradation in SiON is due to trap charges ( $\mu_{trap}$ ) and/or interface sates ( $\mu_{it}$ ). Since  $\mu_{RCS}$  has stronger temperature dependence than  $\mu_{trap}/\mu_{it}$  (Fig.5) [5], the influence of  $\mu_{RCS}$  becomes significant at low temperatures, compared to other components. The SiON sample shows strong N<sub>poly</sub> dependence at 100K, while the SiO<sub>2</sub> sample still has little N<sub>poly</sub> dependence (Fig.6). This is the direct experimental evidence that the increase in dielectric constant can enhance RCS due to gate impurities ( $\epsilon$  effect), though it is not so critical at 300K in our SiON samples.

# 3.2. Influence of physical thickness on RCS

Next, the influence of physical thickness on RCS (r effect) under constant EOT is discussed.  $\mu_{eff}$  in samples with the same EOT of 1.5nm (Fig.7) was compared at 300K (Fig.8). Although  $\mu_{eff}$  in SiO<sub>2</sub> has clear N<sub>poly</sub> dependence, similar as previously reported [3],  $\mu_{eff}$  in SiON is almost independent of N<sub>poly</sub>, even at the same EOT. The same behaviors were observed in SiON samples with higher EOT. The ratio of  $\mu_{eff}$  at high N<sub>poly</sub> to that at low N<sub>poly</sub> is shown as a function of EOT (Fig.9(a)) and physical thickness (Fig.9(b)). Although the  $\epsilon$  effect can influence RCS, it is concluded that RCS is mainly dominated by the r effect from a practical viewpoint.

The relationship between remote Coulomb scattering potential  $\phi$  and dielectric constant  $\varepsilon$  was calculated (Fig.10). When the distance r is constant,  $\phi$  has a maximum at/around the dielectric constant of substrate, which leads to the enhancement of RCS as shown in Fig.6. On the other hand, under constant EOT,  $\phi$  decreases monotonously, which corresponds to suppression of RCS (Figs.8, 9). From these results, RCS due to gate impurities on inversion-layer mobility will become less influential in MOSFETs with high-k materials, in which sufficient physical thickness is required to reduce gate leakage current.

## 3. Conclusions

The influence of high dielectric constant in gate insulator on remote Coulomb scattering due to gate impurities was examined. Experimental evidence that the increase in dielectric constant can enhance RCS was obtained. However, since the physical thickness also increase with increasing  $\varepsilon$  for a practical use, RCS will be rather suppressed. Our new findings will contribute to an accurate modeling of mobility limited by remote Coulomb scattering.

#### References

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Fig.1 (a) Schematic diagram for dielectric constant dependence of RCS potential. Dashed circle indicates the strength of scattering potential. (b) Trade-off between  $\varepsilon$  effect and r effect. " $\varepsilon$  effect" means that the increase in  $\varepsilon$  leads to stronger  $\phi$ . "r effect" means that the distance between surface carriers and ionized impurities in the gate becomes long with increasing  $\varepsilon$  under constant EOT.

C<sub>GC</sub> (pF)



14 12 10 8 6 4 -  $T_{sio2}=2.1$ nm 2 -  $T_{sioN}=2.2$ nm 0 -  $T_{siON}=2.2$ nm -  $T_{siON}=2.2$ 

Fig.3 Gate-to-channel capacitance characteristic. Dielectric constant in SiON is higher than that in SiO<sub>2</sub>.



of ionized impurities in gate

Fig.5 Schematic diagram for temperature dependence of each Coulomb scattering component [5].



Fig.8 Dependence of  $\mu_{eff}$  on  $E_{eff}$  at 300K at EOT=1.5nm.





Fig.9 Ratio of mobility in at high  $N_{poly}$  to low  $N_{poly}$  as function of (a) EOT and (b) thickness.  $N_S=3x10^{11}$  cm<sup>-2</sup>, (•) SiO<sub>2</sub>, (•) SiON.



Fig.4 Dependence of inversion-layer mobility ( $\mu_{eff}$ ) on effective field ( $E_{eff}$ ) at 300K.



Fig.7 Gate-to-channel capacitance characteristic at EOT=1.5nm.



Fi.10 Relationship between scattering potential and dielectric constant.