

## G-2-3

# First Operation of AlGa<sub>N</sub> Channel High Electron Mobility Transistors with Sufficiently Low Resistive Source/Drain Contact formed by Si Ion Implantation

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## 1. Introduction

AlGa<sub>N</sub>/Ga<sub>N</sub> high electron mobility transistors (HEMTs) are strong candidates for future high-power and high-frequency applications[1,2]. This is because Ga<sub>N</sub>-based wide bandgap semiconductors have not only superior material properties compared to silicon or GaAs, such as high breakdown field and a high electron saturation velocity, but also a high-density two-dimensional electron gas induced in the AlGa<sub>N</sub>/Ga<sub>N</sub> hetero-structures due to the polarization effects even if AlGa<sub>N</sub> barrier layer is consisted of unintentionally-doped layer. The trend of such devices is now progressing for much higher-power applications. In order to operate in much higher-power region, increasing the breakdown voltage is simple strategy and applying the materials with higher breakdown field ( $E_c$ ) to the channel layer is the one of the effective method. Higher aluminum mole fractional AlGa<sub>N</sub> is the available material in order to increase the breakdown voltage without decrease of the drain current density, because the  $E_c$  of AlN which has about twice large of energy bandgap comparing to that of Ga<sub>N</sub> is about four times larger than that of Ga<sub>N</sub>, and the electron saturation velocity of AlN is almost as same as that of Ga<sub>N</sub>. For realizing of such hetero-structures, however, we may use much higher aluminum mole fractional AlGa<sub>N</sub> as the barrier layer which will increase the contact resistance significantly, so that a breakthrough technology is necessary for the AlGa<sub>N</sub> channel HEMTs.

Recently we have reported that Si ion implantation technique was very effective to realize superior ohmic contact for the HEMTs with Ga<sub>N</sub> channel[3]. In this paper, we fabricated the HEMTs with AlGa<sub>N</sub> channel, where the Si ion implantation technique was used to form sufficiently low resistive contact, and demonstrated the transistor operation for the first time.

## 2. Experimental

Figure 1 shows the cross sectional structure of the fabricated HEMTs with AlGa<sub>N</sub> channel layer. The AlGa<sub>N</sub>/AlGa<sub>N</sub> epitaxial layers were grown on a sapphire substrate with a thin low-temperature Ga<sub>N</sub> (LT-GaN) buffer layer by the metalorganic chemical vapor deposition

technique. The aluminum mole fraction in barrier layer and channel layer were 40% and 20%, respectively. Both barrier layer and channel layer were unintentionally doped.

The fabrication process started with selective Si ions implantation into the source/drain region. <sup>28</sup>Si ions were implanted with energy of 50 keV at dose concentration of  $1 \times 10^{15} \text{ cm}^{-2}$  at room temperature, and subsequently implanted Si ions were activated by rapid thermal annealing at 1150 °C for 5 min in an environment of flowing nitrogen. During the Si ions implantation and activation annealing, the wafer was capped by 30nm thick of SiN layer deposited by plasma-enhanced chemical vapor deposition. After removing the SiN cap layer in a HF solution, the Ti/Al source/drain ohmic contacts were formed, and device isolation was performed by Zn ion implantation[4]. Lastly Ni/Au Schottky gate contact was formed. All electrodes were defined by the conventional photolithography and liftoff technique, and the all metals were deposited by an electron beam evaporation system.

The current-voltage (I-V) and capacitance-voltage (C-V) characterization were carried out at room temperature using an Agilent 4156C parameter analyzer and a HP4284 LCR meter.

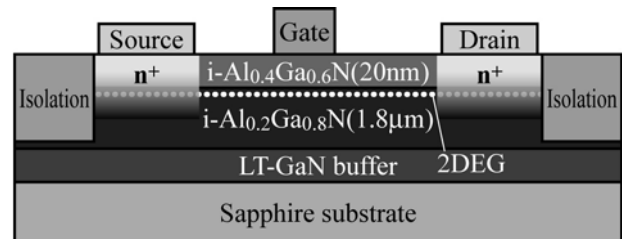


Fig.1 Schematic structure of HEMTs with AlGa<sub>N</sub> channel layer

## 3. Results and discussion

### 3.1. Formation of two-dimensional electron gas

Figure 2 shows the depth profile of carrier concentration calculated from C-V characteristics of Schottky diode fabricated on same wafer.

The carrier concentration rapidly increased around 20

nm of depth corresponding to the AlGa<sub>N</sub> barrier layer thickness. This result indicated that the two-dimensional electron gas was formed at the AlGa<sub>N</sub>/AlGa<sub>N</sub> hetero interface. The sheet carrier concentration calculated from the integration of this carrier concentration was  $6.9 \times 10^{12} \text{ cm}^{-2}$ .

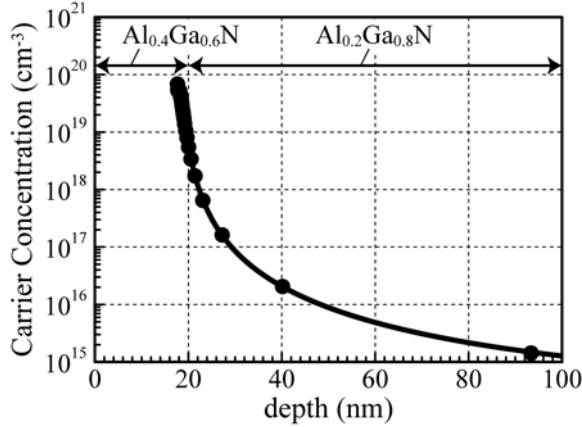


Fig.2 Depth profile of the carrier concentration calculated from the C-V characteristics.

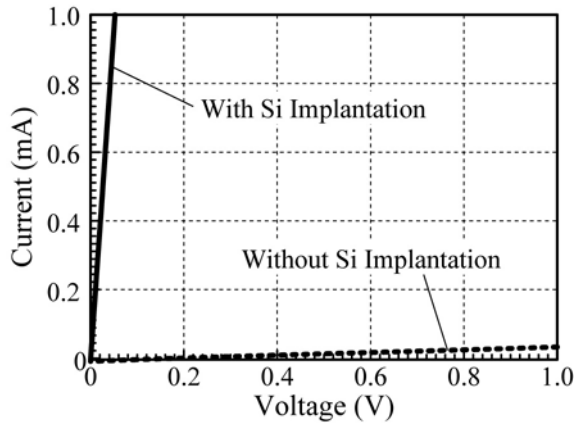


Fig.3 I-V curves of the CTLM pattern with and without Si ion implantation.

### 3.2. Si ion implantation for Ohmic contact

Figure 3 shows the I-V curves of the circular transfer length method (CTLM) pattern spaced  $4 \mu\text{m}$  with and without Si ion implantation. In the sample without Si ion implantation, the contact resistance was so high that we could not calculate it from CTLM, because the AlGa<sub>N</sub> barrier layer was not only unintentionally doped but also the aluminum mole fraction was higher comparing to the conventional barrier layer of AlGa<sub>N</sub>/Ga<sub>N</sub> structure. On the other hand, in the sample with Si ion implantation, we could realize the low ohmic contact resistance of  $1.2 \times 10^{-3} \Omega \text{ cm}^2$  led from the CTLM. As a result Si ion implantation technique was very effective to obtain sufficiently low contact resistance to operate the transistor with this AlGa<sub>N</sub>/AlGa<sub>N</sub> hetero-structure.

### 3.3. Characteristics of AlGa<sub>N</sub>/AlGa<sub>N</sub> HEMT

Figure 4 shows the drain current-drain voltage curves of the AlGa<sub>N</sub> channel HEMT applied the Si ion implantation technique for source/drain ohmic contact. The gate length and the gate width of the measured sample were  $1 \mu\text{m}$  and  $50 \mu\text{m}$ , respectively. We could confirm the transistor operation in the HEMTs with AlGa<sub>N</sub> channel layer and obtained the maximum drain current density of  $0.13 \text{ A/mm}$  at the gate voltage of  $2 \text{ V}$ .

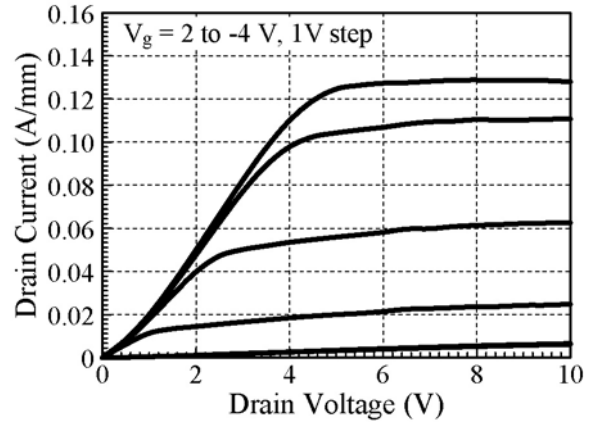


Fig.4 Drain current-drain voltage curves of the sample applied Si ion implantation for source/drain ohmic contact.

## 4. Conclusions

We realized low resistive contact by applying the Si ion implantation technique for the source/drain formation and demonstrated a transistor operation in the HEMTs with AlGa<sub>N</sub> channel layer for the first time. This remarkable result is very promising for increasing the breakdown voltage without the decrease of drain current, and thus much higher-power operation of HEMTs should be realized.

## References

- [1] S. Keller, Y-F. Wu, G. Parish, N. Ziang, J. J. Xu, B. P. Keller, S. P. DenBaars, and U. K. Mishra, IEEE Trans. Electron Devices **48** (2001) 552.
- [2] Y. Kamo, T. Kunii H. Takeuchi, Y. Yamamoto, M. Totsuka, T. Shiga, H. Minami, T. Kitano, S. Miyakuni, T. Oku, A. Inoue, T. Nanjo, Y. Tsuyama, R. Shirahana, K. Iyomasa, K. Yamanaka, T. Ishikawa, T. Takagi, K. Marumoto, and Y. Matsuda, 2005 IEEE MTT-S Int. Microwave Symp. Dig. (2005) WE1E-5.
- [3] M. Suita, T. Nanjo, T. Oishi, Y. Abe, and Y. Tokuda, phys. Status Solidi C **3**(2006)2364.
- [4] T. Oishi, N. Miura, M. Suita, T. Nanjo, Y. Abe, T. Ozeki, H. Ishikawa, T. Egawa and T. Jimbo: J. Appl. Phys. **94** (2003) 1662.