

Low resistivity V/Al/Mo/Au ohmic contacts on AlGaIn/GaN annealed at low temperatures

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1. Introduction

AlGaIn/GaN heterostructure field effect transistors (HFETs) are very attractive for high-power and high-frequency electronic devices. This is due to its unique material properties, including wide band gap, large saturation velocity, and polarization effects leading to high sheet charge density. In order to make the best use of these advantages, it is important to develop ohmic contact technology which provides low contact resistances.

Several metallization schemes, mostly Ti/Al-based, have been reported to form ohmic contacts on AlGaIn/GaN heterostructures [1-6]. Due to the low melting temperature of Al, ohmic contacts are easy to be oxidized at high temperatures. Therefore it is desirable to get low-resistivity ohmic contacts with smooth surface morphology by low-temperature annealing. Schweitz et al. reported that V/Al-based metals are effective for AlGaIn/GaN ohmic contact formation [5]. They pointed out the potential of low-temperature annealing below 700°C, but they reported minimum contact resistivities in the order of $10^{-6}\Omega\text{cm}^2$ after annealing at 700-750°C [5]. In this paper, we present experimental results of V/Al/Mo/Au ohmic contact, and report a minimum contact resistivity of low- $10^{-6}\Omega\text{cm}^2$ after low-temperature annealing at 600-650°C.

2. Experiments

AlGaIn/GaN heterostructures used in this study were grown by metalorganic chemical vapor deposition (MOCVD) on a sapphire substrate. The structure consists of a 1 μm unintentionally doped GaN and a 25nm unintentionally doped $\text{Al}_{0.25}\text{Ga}_{0.75}\text{N}$. Ohmic metals were successively deposited by electron-beam evaporation. Contact resistances were determined by circular transfer length measurement (C-TLM). The metallization schemes studied consist of V(15nm)/Al(60nm)/Mo(35nm)/Au(60nm). For comparison, we have also investigated other ohmic metallization systems, such as Ti/Al/Mo/Au, Hf/Al/Mo/Au, and Zr/Al/Mo/Au. These ohmic contacts were annealed at temperatures ranging from 550 to 900°C by rapid thermal annealing for 30s in a N_2 ambient. The effects of the thickness of V were also investigated for the V/Al/Mo/Au metal system. Figure 2 shows the top-view of C-TLM patterns. The distance between electrodes was varied from 2 to 20 μm .

3. Results and discussion

Figure 3 shows the specific contact resistivity (ρ_c) as a function of annealing temperature for ohmic contacts with V/Al/Mo/Au and related metal systems. It is clearly seen that the optimum annealing temperature differs significantly depending on the ohmic metal system employed. Table I lists the minimum ρ_c and the optimum annealing temperature for each ohmic system. Note that V/Al/Mo/Au exhibited a lowest ohmic contact resistivity of $1.6\times 10^{-6}\Omega\text{cm}^2$ at 550°C. These results suggest that V/Al/Mo/Au works as a low-resistivity ohmic contact metal after annealing at temperatures below 650°C.

Figure 4 shows ohmic contact resistance of V/Al/Mo/Au with various V thicknesses ranging from 0 to 22.5nm as a function of annealing temperature. The optimum annealing temperature moves toward higher temperatures as the thickness of V is increased. A minimum ohmic contact resistivity of $1.6\times 10^{-6}\Omega\text{cm}^2$ was achieved at 550°C for a V thickness of 15nm. These results suggest that the second ohmic layer of Al reacts with the AlGaIn layer to form N vacancies [6], and the first ohmic layer of V prevents Al from reacting excessively with the AlGaIn layer.

Table II shows surface roughness (R_a) for V/Al/Mo/Au and Ti/Al/Mo/Au after annealing. V/Al/Mo/Au showed R_a of 87Å at 550°C, while Ti/Al/Mo/Au exhibited $R_a=213\text{Å}$ after annealing at 800°C. The results indicate that V/Al/Mo/Au ohmic metal provides flatter surface morphology after annealing, which is important for securing good uniformity and reproducibility.

4. Conclusion

We investigated V/Al/Mo/Au ohmic contacts on AlGaIn/GaN HFETs. A V/Al/Mo/Au ohmic contact exhibited a low resistivity of $1.6\times 10^{-6}\Omega\text{cm}^2$ at a low annealing temperature of 550°C. It was found that the surface of V/Al/Mo/Au after annealing was very smooth ($R_a=87\text{Å}$).

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References

- [1] D. Selvanathan, F. M. Mohammed, A. Tesfayesus and I. Adesida, J. Vac. Sci. Technol. **B22**, 2409 (2004).
- [2] A. N. Bright, P. J. Thomas, M. Weyland, D. M. Tricker, C. J. Humphreys and R. Davies, J. Appl. Phys. **89**, 3143 (2001).
- [3] M. A. Miller and S. E. Mohny, Appl. Phys. Lett. **91**, 012103 (2007).
- [4] F. M. Mohammed, L. Wang and I. Adesida, Appl. Phys. Lett. **88**, 212107 (2006).
- [5] K. O. Schweitz, P. K. Wang, S. E. Mohny and D. Gotthold, Appl. Phys. Lett. **80**, 1954 (2002).
- [6] Z. F. Fan, S. N. Mohammad, W. Kim, O. Aktas, A. E. Botchkarev, and H. Morkoc, Appl. Phys. Lett. **68**, 1672 (1996).

Au
Mo
Al
V
Al _{0.25} Ga _{0.75} N (25nm)
GaN (1 μ m)
Buffer layer
Sapphire substrate

Fig. 1 Ohmic structure on Al_{0.25}Ga_{0.75}N/GaN.

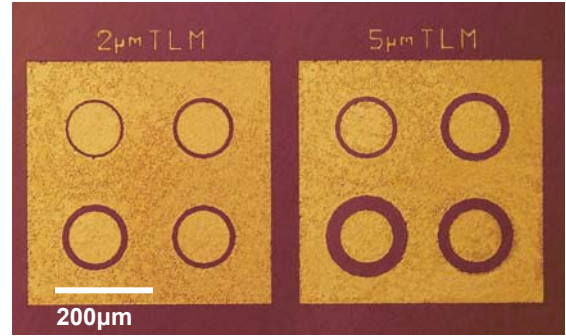


Fig. 2 Top-view of circular TLM patterns.

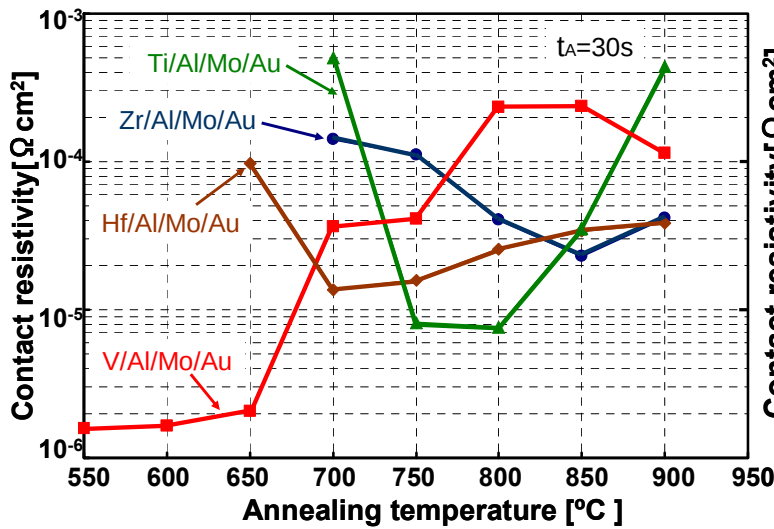


Fig. 3 Contact resistivity of X/Al/Mo/Au (X=Ti, Hf, Zr, and V) as a function of annealing temperature.

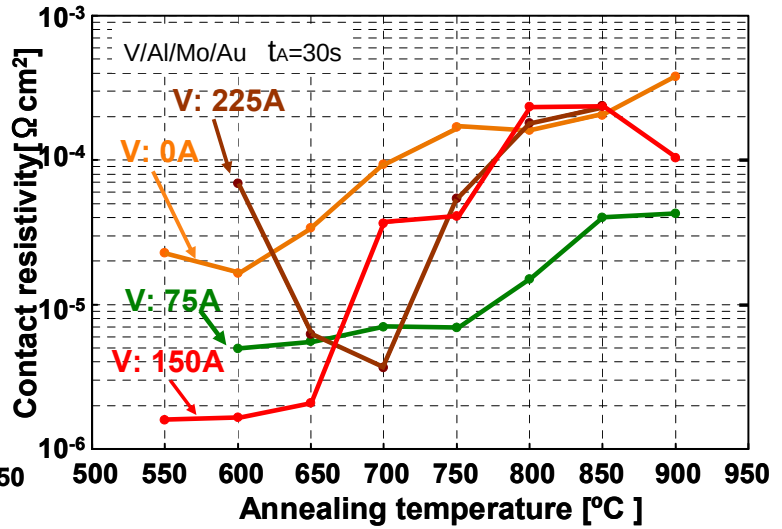


Fig. 4 Contact resistivity of V/Al/Mo/Au with V thickness from 0 to 300Å as a function of annealing temperature.

Table I Summary of contact resistance of V/Al/Mo/Au and related metal systems.

	Annealing temperature (°C)	Contact resistivity (Ωcm ²)
V/Al/Mo/Au	550	1.6×10^{-6}
Ti/Al/Mo/Au	800	7.7×10^{-6}
Hf/Al/Mo/Au	700	1.4×10^{-5}
Zr/Al/Mo/Au	850	3.3×10^{-5}

Table II Surface roughness Ra of V/Al/Mo/Au and Ti/Al/Mo/Au.

	Annealing temperature (°C)	Surface roughness: Ra (Å)
V/Al/Mo/Au	550	87
Ti/Al/Mo/Au	800	213