

Effects of Substrate Defects on the Gate Leakage Current of AlGaIn/GaN Heterojunction FETs Fabricated on Na Flux Bulk GaN

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1. Introduction

AlGaIn/GaN heterojunction FETs (HEMTs) have attracted great interest in applications to high-voltage, high-power, and high-frequency electronic devices due to their outstanding material properties including high breakdown field, high saturation drift velocity, and stability at high temperatures. AlGaIn/GaN heterostructures have been commonly grown on sapphire, SiC, and Si substrates. However, the large lattice mismatch between GaN and the substrate material gives rise to high dislocation density in the epitaxial layer typically on the orders of 10^8 to 10^{10} cm⁻², which is considered to be the origin of significant Schottky gate leakage in AlGaIn/GaN HEMTs.

A free-standing GaN has emerged as a leading candidate to accommodate those issues related to lattice mismatch. A number of authors have reported that a free standing GaN substrate grown by hydride vapor phase epitaxy (HVPE) is effective to obtain ideal Schottky characteristics and to reduce reverse leakage current of Schottky diodes fabricated on n-GaN and AlGaIn/GaN heterostructures[1,2]. Although additional defect generation is suppressed using GaN substrates, as-grown HVPE substrates still have a relatively high dislocation density on the order of 10^6 cm⁻². A melt-grown method, such as Na-flux LPE method[3,4], has recently attracted significant attention to further reduce the defect density of a bulk GaN substrate.

In this work, we have fabricated an array of AlGaIn/GaN HEMTs on a whole 30mm-diameter bulk GaN substrate grown by Na-flux method and investigated the effects of crystal defects on the gate leakage of Schottky-barrier gate AlGaIn/GaN HEMTs.

2. Experiments

AlGaIn/GaN heterostructures were grown by metalorganic chemical vapor deposition (MOCVD) on a free-standing GaN substrate grown by Na-flux method. A 30mm-diameter GaN substrate with a dislocation density on the order of 10^4 cm⁻² was used in this experiment. The AlGaIn/GaN epitaxial layers consist of an undoped GaN

channel layer and a 25 nm undoped Al_{0.25}Ga_{0.75}N barrier layer. The ohmic metallization consists of Ti/Al/Mo/Au and was annealed at 850 °C for 30 s in N₂ ambient. The Schottky metals used were Ni/Au. AlGaIn/GaN HEMTs with a gate length of 3 μm and a gate width of 70 μm were fabricated. DC characteristics including Id-Vds, Id-Vgs, and Ig-Vgs were measured for 1000 FETs and electrical characteristics were compared with the defect profiles estimated by X-ray topography.

3. Results and discussion

There are two groups showing low and high gate leakage currents. Figure 1 shows the typical DC characteristics of the fabricated device for the low gate leakage one, where V_{gs} was varied from 2 V with a -1 V step. The maximum drain current (I_{dsmax}) was 400 mA/mm and the transconductance (g_m) was 100 mS/mm with a pinch-off voltage of -3.5 V. The relation between I_{dsmax} and gate-source leakage current (I_{gs leak}) defined at V_{gs}=-20 V is shown in Fig. 2. As is shown in the figure, no correlation was found between I_{dsmax} and I_{gs leak}, that is, I_{dsmax} is independent of I_{gs leak}. The I-V characteristics between gate and source are displayed in Fig. 3 for 1000 pieces of devices. As clearly seen, almost all the devices (983 pieces) showed I_{gs leak} of less than 10⁻⁷ A/mm, while 17 devices exhibited a high leakage current of >10⁻⁶ A/mm. In order to make the origin of gate current clear, the surface of the wafer was carefully observed with a microscope. An example of the photo is shown in Fig. 4. The defect near the gate electrode was found for the device showing relatively high leakage current, while the defect was less or not found for the device with low leakage current. Figure 5 displays the X-ray topograph image of the GaN substrate (before epitaxial growth) together with closed circles showing the device position exhibiting high gate leakage current. Although the defects are not clear by the image, a relatively good correlation was observed between the defects and the device area with a high gate leakage.

4. Conclusions

Gate leakage characteristics were estimated for Al-GaN/GaN HEMTs fabricated on a bulk GaN substrate grown by Na-flux method. Clear correlation was observed between the substrate defects and the gate leakage for 1000 pieces of fabricated HEMTs.

Acknowledgements

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References

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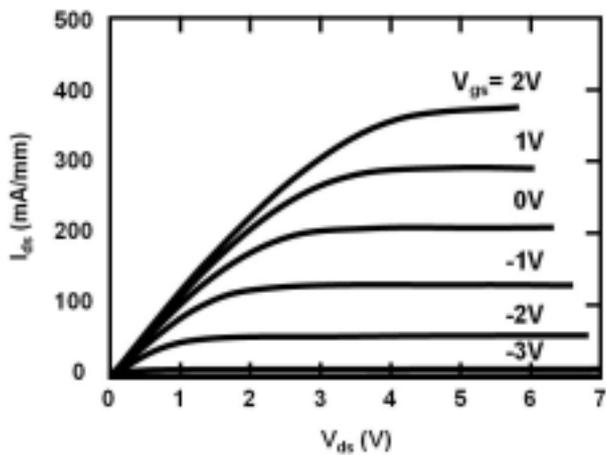


Fig. 1 DC characteristics of the fabricated HEMT (low gate leakage current device).

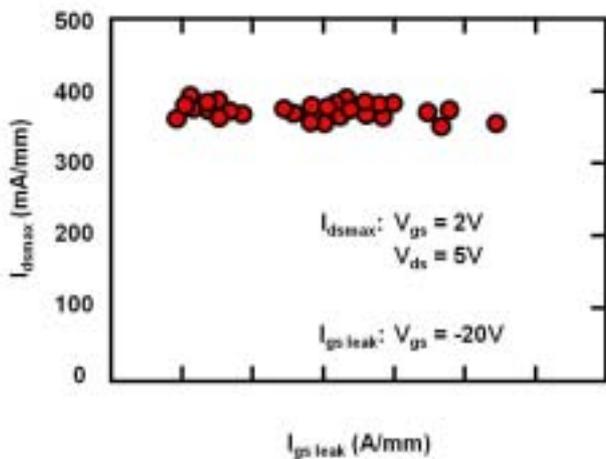


Fig. 2 Maximum drain current (I_{dsmax}) as a function of gate-to-source leakage current ($I_{gs leak}$).

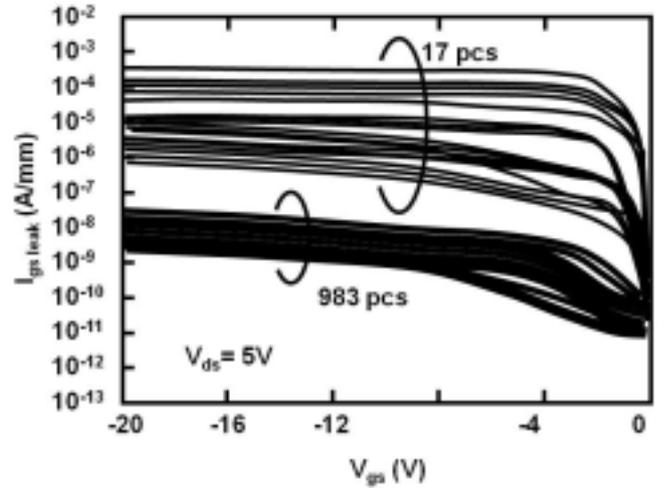


Fig. 3 I-V characteristics between gate and source for the fabricated 1000 pieces of HEMTs.



Fig. 4 Photo of the fabricated HEMT, where the defect is found (see the circled black dot).

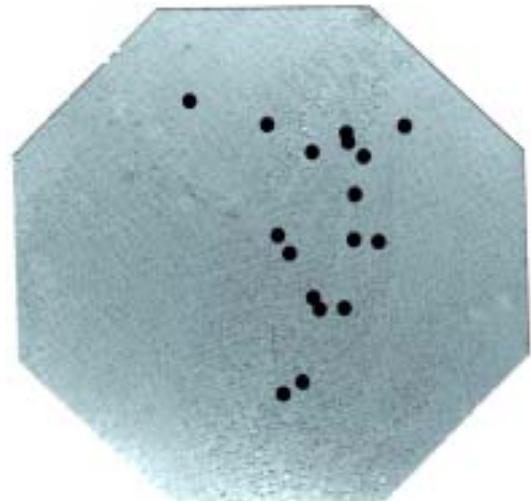


Fig. 5 Top view of X-ray topograph image for GaN substrate (before epitaxial growth). Black dots show the device areas with high gate leakage current.