

# Nanoscale epitaxial lateral overgrowth of GaN-based light-emitting diodes on a SiO<sub>2</sub> nanorod-array patterned sapphire template

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## 1. Introduction

The wide band gap GaN-based semiconductors have attracted much attention for practical application, such as short-haul optical communication, traffic and signal lights, backlights for liquid-crystal displays, and daily lightings. Typically, GaN-based light emitting diodes (LEDs) was grown on sapphire and SiC substrate by heteroepitaxial techniques, such as metal-organic chemical vapor deposition (MOCVD) [1]. However, the low thermal conductivity and insulating properties make sapphire less perfect as a substrate for the growth of GaN epilayers. However, the GaN-based epilayers still contain a high threading dislocations density (TDD) (around  $10^9 - 10^{10} \text{ cm}^{-2}$ ) due to the lack of a suitable substrate, leading to damage on the performance of devices then led to the low internal quantum efficiency. On the other hand, the light extraction efficiency of LEDs is limited by critical angle loss is attribution to the refraction index between GaN film and surrounding air have large difference. Therefore, the patterned sapphire substrates (PSS) technique not only decreases the TDD but also improves the light extraction efficiency (LEE). However, experimental and theoretical studies reveal a further reduction TDD method if the lateral overgrowth approach is reduced the nanoscale. In this work, we investigated high efficiency InGaN/GaN LEDs grown on sapphire substrate with different depth SiO<sub>2</sub> nanorod-array (NRA).

## 2. Experiment and Discussion

The preparation of the sapphire with different depth SiO<sub>2</sub> NRA started with the deposition of 100 nm, 200 nm and 300 nm SiO<sub>2</sub> on the sapphire by plasma-enhanced chemical vapor deposition (PECVD), respectively. Then, 10nm-thick Ni layer was deposited by an e-gun evaporator. The sample was annealed at 850°C for 60s in nitrogen ambient to form self-assembled Ni nano-cluster on the SiO<sub>2</sub> layer. The Ni nano-clusters acted as etching masks and subsequently, the-reactive ion etching and inductive couple plasma dry etching were performed to form SiO<sub>2</sub> NRA. After etching, the SiO<sub>2</sub> NRA was dipped into hot HNO<sub>3</sub> to remove Ni cluster. The growth of a conventional GaN LED structure, which consists of six periods of InGaN/GaN MQWs and a 100nm-thick p-GaN layer, was deposited by

MOCVD. A indium-tin-oxide (ITO) layer was deposited on the surface on the purpose of ohmic contact and current spreading. Lastly, the completely epitaxial structure underwent a standard four-mask LED fabrication process with a chip size 300μm×300μm as shown in Fig. 1. The different depth of the SiO<sub>2</sub> NRA on the surface of sapphire were fabrication by different SiO<sub>2</sub> thickness and RIE time, and could be used to effectively scatter or redirect the guiding light inside the GaN LED to escape. Moreover the epitaxial crystal quality was improved.

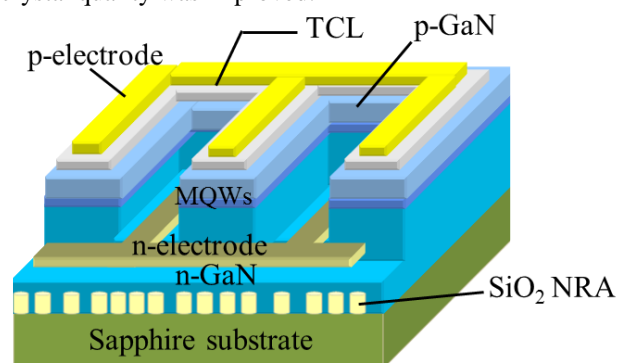


Fig. 1 The schematic of InGaN/GaN LED structure

Fig. 2(a) shows image indicating that the SiO<sub>2</sub> nanorods were approximately 180nm~200nm in diameter. The spacing between nanorods was about 130nm~150nm. Fig. 2(a) also shows that the exposed sapphire surface was flat enough for epitaxy. As the deposition process began, localized and hexagonal islandlike GaN nuclei were first form the sapphire surface to initiate GaN overgrowth, as shown in fig. 2(b). Fig. 2 (c)(d)(e) shows the cross-section SEM image of GaN epitaxial layer, where air voids were observed between the SiO<sub>2</sub> nanorods. The existing of the voids between nanorods observed from the micrographs suggested that not all the exposed surface enjoyed the same growth rate. Hence, only the regions with the higher growth rates, which might be originate from larger exposed surface, could play the role of a seed layer, facilitating the lateral coalescence of GaN.

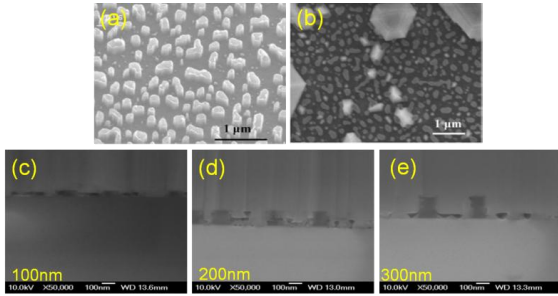


Fig. 2 SEM image of (a) the fabrication  $\text{SiO}_2$  NRA (b) GaN nuclei on the sapphire with  $\text{SiO}_2$  NRA as growth seeds (c) the GaN epilayer on the sapphire with 100nm  $\text{SiO}_2$  NRA (d) 200nm  $\text{SiO}_2$  NRA (e) 300nm  $\text{SiO}_2$  NRA

Fig. 3 (a) shows XRD full-width at half- maximums (FWHMs) of the GaN films grown on flat sapphire and sapphire with 100nm, 200nm, 300nm  $\text{SiO}_2$  NRA. The FWHMs of the  $\omega$  scan rocking curve for (102) planes infer the densities of edge dislocations [2]. The FWHMs of (102) planes were 360, 323, 275 and 327 arcsec for GaN films grown on flat sapphire and sapphire with 100nm, 200nm, 300nm  $\text{SiO}_2$  NRA, respectively. The room temperature photoluminescence spectrum was also used to analyze GaN crystal quality. The peak intensity of GaN divided by peak intensity of yellow band (GaN peak/yellow peak), as shown in the Fig. 3 (b). The intensity of yellow band is related to defect density. The ratios of the GaN films grown on flat sapphire and sapphire with 100nm, 200nm, 300nm  $\text{SiO}_2$  NRA were 85, 91, 183 and 113, respectively. The significant decreases in XRD FWHM and increases in GaN peak/yellow peak indicate a reduction in threading dislocation density. The improvement in crystal quality is attributed to the strain relaxation of the partially relieved GaN layer in combination with the subsequent regrowth. When the initial GaN epitaxial layer was grown on sapphire, a compressive strain was built up inside the material due the mismatched lattice constants and thermal expansion coefficients between GaN and sapphire. These mismatched factors introduced threading dislocations. The nanoscale epitaxial lateral overgrowth (ELO) process relaxed the compressive strain. It acted as a buffer layer to partially filter out the problems of mismatched lattice constant and thermal expansion coefficient for MOCVD growth and resulted in improved crystalline quality.

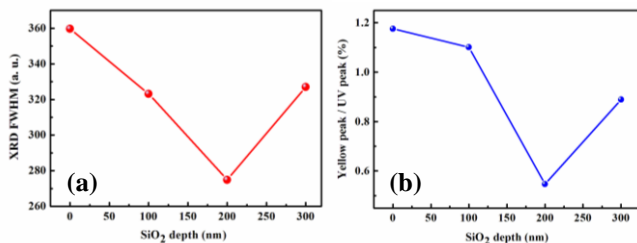


Fig. 3(a) XRD full-width at half- maximums (FWHMs) ; (b) The peak intensity of GaN divided by peak intensity of yellow band of the GaN films grown as a function of the  $\text{SiO}_2$  NRA depth

Fig.4 (a) shows the measured L-I curve for LED grown on sapphire with and without  $\text{SiO}_2$  NRA. The enhancement in light output power of LED grown on sapphire

with 100nm, 200nm and 300nm  $\text{SiO}_2$  NRA at injection current of 20mA were 49.6%, 77.6%, and 54.3% , respectively, as compare to the LED grown on sapphire without  $\text{SiO}_2$  NRA. We believe the enhancement in light output power is due to the improvement of IQE and the enhanced extraction efficiency. The NRA-assisted NELO effectively suppress the threading dislocation density of GaN LED, which increased the IQE. Moreover the  $\text{SiO}_2$  NRA in GaN epilayers contributed to light extraction due to light scattering at the interface of different refractive indices. Ueda *et al* [3] reported that the output power linearly increased with the surface coverage ratio of nanosilica spheres. Therefore, the extraction efficiency was enhanced by the  $\text{SiO}_2$  NRA. To examine the leakage current of GaN LED devices under reverse bias voltage, as shown in the Fig.4 (b). The leakage current of GaN grown on sapphire with  $\text{SiO}_2$  NRA and that grown on planar sapphire were 40nA and 120nA at reverse bias voltage of -6V, respectively. The much smaller leakage current might indicate the better crystalline quality for GaN-based LED regrowth on sapphire with  $\text{SiO}_2$  NRA.

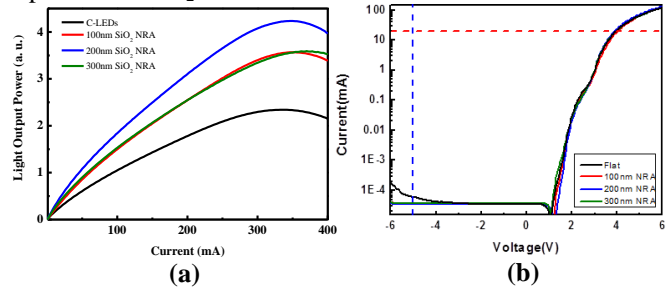


Fig. 4 (a) L-I curves ;(b) I-V of GaN-based LED grown on sapphire with and without  $\text{SiO}_2$  NRA

### 3. Conclusions

In summary, the  $\text{SiO}_2$  nanorod array was fabricated by self-assembled Ni cluster, followed by ICP-RIE ion etching. The work introduced the  $\text{SiO}_2$  nanorod-array assisted NELO method suitable for the MOCVD growth of the high-brightness blue LEDs. The LED grown on sapphire with  $\text{SiO}_2$  nanorod-array demonstrated an enhanced IQE and light extraction efficiency when compared to a conventional LED grown on flat sapphire. The TDD reduction in GaN-based epilayers was realized by the  $\text{SiO}_2$  nanorod-array assisted NELO method, where three potential TD reduction mechanisms were identified.

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