

Improved Current Collapse Phenomenon in AlGaIn/GaN-on-Si HFETs Using Sacrificial GaO_x Process

Jae-Gil Lee¹, Sang-Woo Han¹, Bong-Ryeol Park¹, Kwang-Seok Seo², Hyungtak Kim¹ and Ho-Young Cha^{1,*}

¹ School of Electronic and Electrical Engineering, Hongik Univ.
94 Wausan-ro, Mapo-gu, Seoul, Republic of Korea, 121-791
Phone: +82-2-320-3062 *E-mail: hcha@hongik.ac.kr

² Department of Electrical and Computer Engineering, Seoul National Univ.
1 Gwanak-ro, Gwanak-gu, Seoul, Republic of Korea, 151-742

Abstract

We have developed a novel passivation process employing a sacrificial GaO_x process to suppress the current collapse phenomenon in AlGaIn/GaN HFETs. Even with a conventional prepassivation process, surface damage during high temperature ohmic annealing cannot be avoided completely, which in turn induces surface trapping states. In this proposed process, the damaged surface after high temperature ohmic annealing was removed by a sacrificial GaO_x process prior to SiO₂ surface passivation. As a result, surface damage induced trapping effects were successfully suppressed.

1. Introduction

AlGaIn/GaN heterostructure field effect transistors (HFETs) are very suitable for high frequency power and power switching applications. One of the important issues in RF and switching characteristics of AlGaIn/GaN HFETs is the current collapse phenomenon associated with surface trapping effects. Thus, surface passivation using various types of dielectric materials, such as SiN_x, SiO₂, Al₂O₃, AlN, etc., has been intensively studied in many research groups [1-4]. It was reported that not only poor surface/interface conditions between AlGaIn/GaN and passivation layer but also surface damage associated with high temperature ohmic annealing was responsible for trapping phenomenon. Several studies reported that appropriate plasma treatments reduced the interface states [5, 6] and a prepassivation process was effective to protect the surface during ohmic annealing [1]. In this study, we proposed a new process employing a sacrificial GaO_x layer and SiO₂ passivation to suppress the current collapse phenomenon in AlGaIn/GaN HFETs.

2. Experiments

AlGaIn/GaN HFETs were fabricated using three different processes, as shown in Fig. 1. Process (1) employed a conventional prepassivation process using SiO₂. In processes (2) and (3), a SiN_x prepassivation process was utilized but the protecting prepassivation layer was etched away after high temperature ohmic annealing and followed by SiO₂ passivation. In process (3), the O₂ plasma treatment

was performed after etching the SiN_x prepassivation layer in order to form a GaO_x sacrificial layer at the surface that was etched back prior to SiO₂ passivation.

The epitaxial structure consisted of a 2-nm undoped GaN layer, a 30-nm Al_{0.25}Ga_{0.75}N barrier, and a 100-nm GaN layer and 3.9-μm carbon-doped GaN buffer layer on a p-type Si (111) substrate. The process began with mesa isolation using a Cl₂/BCl₂-based ICP-RIE process. A 30 nm SiO₂ film was deposited as a prepassivation layer for process (1) whereas a 10 nm SiN_x film was deposited as a surface protection layer during ohmic annealing for processes (2) and (3). A Si/Ti/Al/Mo/Au (= 5/20/60/35/50 nm) was used for ohmic contacts, which were annealed at 830°C in N₂ ambient. After ohmic annealing, the SiN_x film was etched away using BOE in processes (2) and (3). A 30 nm SiO₂ film was then deposited at 250°C using ICPCVD as a passivation layer for process (2) [2]. In process (3), an additional process step was performed prior to SiO₂ deposition; O₂ plasma treatment was carried out to form a GaO_x sacrificial layer that was etched back using diluted HCl solution. It is suggested that this sacrificial layer process not only remove the damaged surface but also improve the interface quality between AlGaIn/GaN and SiO₂. The gate region was patterned and etched using BOE, and an ad-

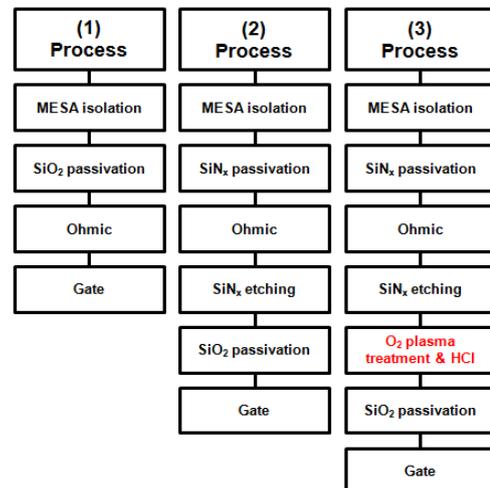


Fig. 1 Three different processes compared in this study.

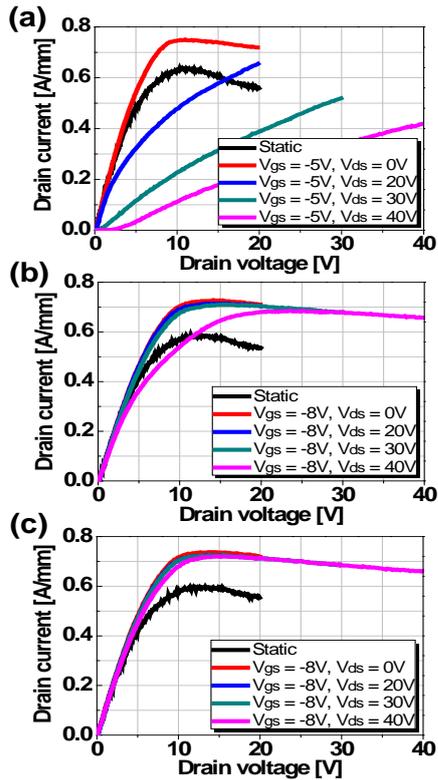


Fig. 2 Static and pulsed current-voltage characteristics of AlGaIn/GaN HFETs fabricated using (a) process (1), (b) process (2), and (c) process (3). The pulsed I-V characteristics were measured using a pulse width of 0.5 μ s with a period of 1 ms. The quiescent bias conditions were varied from $V_{ds} = 0$ V to $V_{ds} = 40$ V with fixing the gate bias under complete pinch-off conditions. The source-to-gate distance, gate length, and gate-to-drain distance were 3, 2, and 12 μ m, respectively.

ditional lithography process defined the field plated gate. A Ni/Au stack was used for gate metallization. As a stabilization annealing process, a rapid thermal annealing was carried out at 400°C for 5 min in N_2 ambient.

3. Results and discussion

Figure 2 shows the static and pulsed current-voltage characteristics of AlGaIn/GaN HFETs fabricated using processes (1), (2), and (3). A significant current collapse phenomenon was observed for process (1) where the high temperature ohmic annealing was carried out with the SiO_2 protection layer. Lin *et al.* reported that Ga out-diffusion into SiO_2 produced defects and damaged the lattice structure of SiO_2 during high temperature annealing [7]. This annealing induced surface damage is responsible for the significant current collapse phenomenon. It is known that Ga atoms tend to more easily diffuse into SiO_2 compared to SiN_x [8]. Therefore, it is suggested that a SiN_x film is a better choice as a protection layer for GaN surface during high temperature annealing. In processes (2) and (3), the surface was protected by a SiN_x film during ohmic annealing and the following SiO_2 passivation layer was not exposed to high temperature annealing. As a result, the cur-

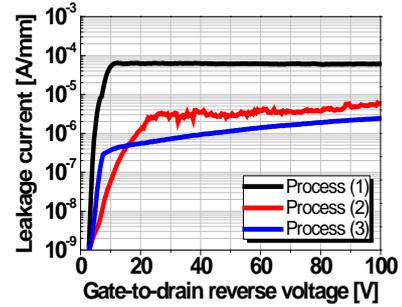


Fig. 3 Leakage characteristics in three different processes.

rent collapse phenomenon was significantly suppressed in processes (2) and (3). Slightly better pulsed characteristics were observed for process (3) compared to process (2), especially under high quiescent drain bias conditions. It is suggested that the sacrificial GaO_x process prior to SiO_2 passivation in process (3) removed the damaged surface associated with high temperature ohmic annealing, resulting in high quality surface.

As shown in Fig. 3, the leakage current dramatically decreased in processes (2) and (3) in comparison with process (1). It is suggested that the SiN_x film is suitable to protect the surface during ohmic annealing and the following SiO_2 passivation in conjunction with a sacrificial GaO_x process is effective to suppress the current collapse phenomenon and reduce the leakage current.

4. Conclusions

We have developed a new process using SiN_x protection and sacrificial GaO_x . The SiN_x protection layer prevents Ga-out diffusion from GaN surface and the sacrificial GaO_x process improves the interface quality between SiO_2 and AlGaIn/GaN. It is suggested that the proposed process can effectively suppress the current collapse phenomenon and reduce the leakage current in AlGaIn/GaN HFETs.

Acknowledgements

This work was supported by the National Research Foundation of Korea (NRF) under Grant 2012042153, Nano Material Technology Development Program 2012035274 through the NRF funded by the Ministry of Education, Science, and Technology, and the ‘‘Energy Efficient Power Semiconductor Technology for Next Generation Data Center’’ IT R&D project (No. 10038766) of Korea Ministry of Knowledge Economy (MKE/KEIT),

References

- [1] J.-C. Her *et al.*, Jpn. J. Appl. Phys. **49** (2010) 041002.
- [2] B.-R. Park *et al.*, IEEE Electron Device Lett., vol. 34, no. 3, pp.354-356 (2013).
- [3] S. Huang *et al.*, IEEE Electron Device Lett., vol. 33, no. 4, pp.516-518 (2013).
- [4] D. H. Kim *et al.*, Electron. Lett., vol. 43, no. 2, pp.129-130 (2007).
- [5] M. Tajima *et al.*, Jpn. J. Appl. Phys. **48** (2009) 020203.
- [6] D. J. Meyer *et al.*, CS MANTECH Conference (2007).
- [7] L. M. Lin *et al.*, J. Electron. chem. Soc., **154** (3), G58-G62 (2007).
- [8] J. Dahl *et al.*, Appl. Phys. Lett., **99** (2011) 102105.