

Engineering of Energy Band Structure with Epitaxial $\text{Ge}_{1-x-y}\text{Si}_x\text{Sn}_y/\text{n-Ge}$ Hetero Junctions for Solar Cell Applications

Shunsuke Asaba¹, Takashi Yamaha¹, Masashi Kurosawa^{1,2},

Mitsuo Sakashita¹, Noriyuki Taoka¹, Osamu Nakatsuka¹, Shigeaki Zaima¹

¹ Graduate School of Eng., Nagoya Univ., Furo-cho, Chikusa-ku, Nagoya 464-8603, Japan

² Research fellow of the Japan Society for the Promotion of Science

Phone: +81-52-789-3819, FAX: +81-52-789-2760, E-mail: nakatuka@alice.xtal.nagoya-u.ac.jp

Abstract

We prepared $\text{Ge}_{1-x-y}\text{Si}_x\text{Sn}_y/\text{Ge}$ hetero junction structures and investigated its electrical and optoelectrical properties for high efficiency solar cell applications. We demonstrated the modulation of the energy band gap of a $\text{Ge}_{1-x-y}\text{Si}_x\text{Sn}_y$ layer which is epitaxially grown with its lattice constant well matches to Ge.

1. Introduction

There are many efforts to improve on current generation efficiency of solar cells. The maximum value of the theoretical efficiency is expected to be less than 30% for Si based single junction solar modules commonly used [1]. On the other hand, multi junction solar cells are able to enhance the efficiency because of appropriate energy band gap adjustment for the solar spectrum. The efficiency of 36% has been achieved by using $\text{InGaP}(1.9\text{eV})/\text{InGaAs}(1.4\text{eV})/\text{Ge}(0.7\text{eV})$ three junction cell [2]. Furthermore, a four junction cell with an absorption interlayer whose bandgap is 1.0 eV promises the efficiency over 40% [3].

In this study, we focused $\text{Ge}_{1-x-y}\text{Si}_x\text{Sn}_y$ ternary alloy to realize multi junction cell with group-IV semiconductor materials. $\text{Ge}_{1-x-y}\text{Si}_x\text{Sn}_y$ is an attractive material because both its lattice constant and band gap can be tuned independently with adjusting Si and Sn compositions. The energy band gap can be controlled from 0.66 eV of Ge to 1.1 eV as well as Si with a lattice constant matching to Ge [4]. Beeler *et al.* reports the photo response of a $\text{Ge}_{1-x-y}\text{Si}_x\text{Sn}_y$ layer with a Sn content less than 4% [5]. In this study, we focus the target Si and Sn contents of 24% and 7.0%, respectively, in order to obtain the band gap of 1.0 eV with the lattice constant matching to Ge substrate. We demonstrated the energy band gap modulation from electrical and photo response properties.

2. Sample preparation

A chemically cleaned n-type Ge(001) substrate was loaded into the ultra high vacuum chamber and the clean surface was confirmed with RHEED pattern after thermal annealing. A 200 nm-thick undoped Ge or $\text{Ge}_{0.69}\text{Si}_{0.24}\text{Sn}_{0.07}$ layer was epitaxially grown at 150°C with molecular beam epitaxy (MBE) method. Then, a mesa-shaped structure was formed with wet etching. A 300 nm-thick SiO_2 layer was deposited with coating of spin-on-glass and annealing at 450°C. Finally, contact holes are formed and an Al grid

electrode was deposited to take incident light (Fig. 1).

3. Results and discussion

Figure 2 shows a result of x-ray two dimensional reciprocal space mapping (XRD-2DRSM) for an as-grown $\text{Ge}_{0.69}\text{Si}_{0.24}\text{Sn}_{0.07}$ layer on Ge(001). We can confirm pseudomorphic growth with no strain relaxation of $\text{Ge}_{0.69}\text{Si}_{0.24}\text{Sn}_{0.07}$ on Ge. Thickness fringe is clearly observed, indicating that an abrupt interface is formed.

I-V characteristics of the undoped Ge/n-Ge and undoped $\text{Ge}_{0.69}\text{Si}_{0.24}\text{Sn}_{0.07}/\text{n-Ge}$ diodes were measured at various temperatures from 100K to 360K (Fig. 3). Both samples exhibit good rectifying property of pn junction despite epitaxial layers are grown with undoped condition. As is often with undoped Ge grown with low temperature MBE, epitaxial Ge and $\text{Ge}_{1-x-y}\text{Si}_x\text{Sn}_y$ layers exhibit p-type conduction due to unintentional hole generation. We can observe leakage current limited by tunneling phenomenon at $\text{Ge}_{1-x-y}\text{Si}_x\text{Sn}_y/\text{Ge}$ hetero interface below 167K.

Figure 4 shows the Arrhenius plot of the reverse current above 310K for both samples to discuss the energy bandgap of epitaxial layers. In ideal situation, reverse current is expressed by $I_R \propto \exp[-E_g/mk_B T]$, where m varies from 1 to 2 depending on diffusion and generation carrier transport mechanisms, respectively. The activation energy, E_g/m of 0.69 eV estimated for the Ge/n-Ge diode agrees very well with the theoretical bandgap of bulk Ge. On the other hand, The energy bandgap for the $\text{Ge}_{0.69}\text{Si}_{0.24}\text{Sn}_{0.07}/\text{n-Ge}$ sample is estimated to be 0.79 eV. Beeler *et al.* reports the activation energy of is less than 0.578 eV for $\text{Ge}_{1-x-y}\text{Si}_x\text{Sn}_y/\text{Ge}$ hetero junction [5]. The larger activation energy, E_g/m in this study indicates the increasing in the energy bandgap of $\text{Ge}_{1-x-y}\text{Si}_x\text{Sn}_y$ with larger Si and Sn content, and/or the decreasing in the m value due to the improvement on the crystallinity of the epitaxial layer.

Figure 5 shows the external quantum efficiencies (E.Q.E) estimated with the photo response measurement. Excess current with incident light was observed for both samples at the energy larger than 0.66 eV. The origin of this absorption for the Ge/n-Ge samples can be accounted by Ge layer and Ge substrate. In addition, the value of E.Q.E. rises at the energy above 0.9 eV even though epitaxial $\text{Ge}_{0.69}\text{Si}_{0.24}\text{Sn}_{0.07}$ layer is very thin. This result evidently means that $\text{Ge}_{0.69}\text{Si}_{0.24}\text{Sn}_{0.07}$ has an energy bandgap as large as 0.9 eV.

4. Conclusions

We have demonstrated increasing the energy bandgap of $\text{Ge}_{1-x-y}\text{Si}_x\text{Sn}_y$ layer pseudomorphically grown on Ge(001) substrate. The photo response measurement of $\text{Ge}_{1-x-y}\text{Si}_x\text{Sn}_y/\text{Ge}$ hetero junctions reveals that the energy bandgap above 0.9 eV is achieved by introducing Si of 24% and Sn of 7.0%.

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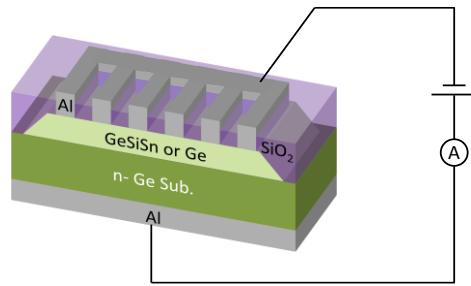


Fig1. Schematic diagram of the solar cell diode sample prepared in this study.

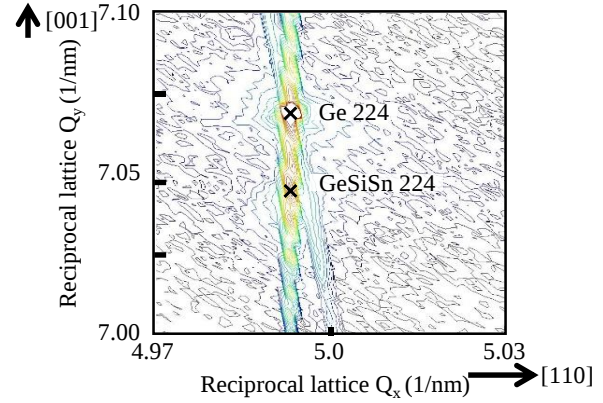


Fig2. XRD-2DRSM of as-grown $\text{Ge}_{0.69}\text{Si}_{0.24}\text{Sn}_{0.07}$

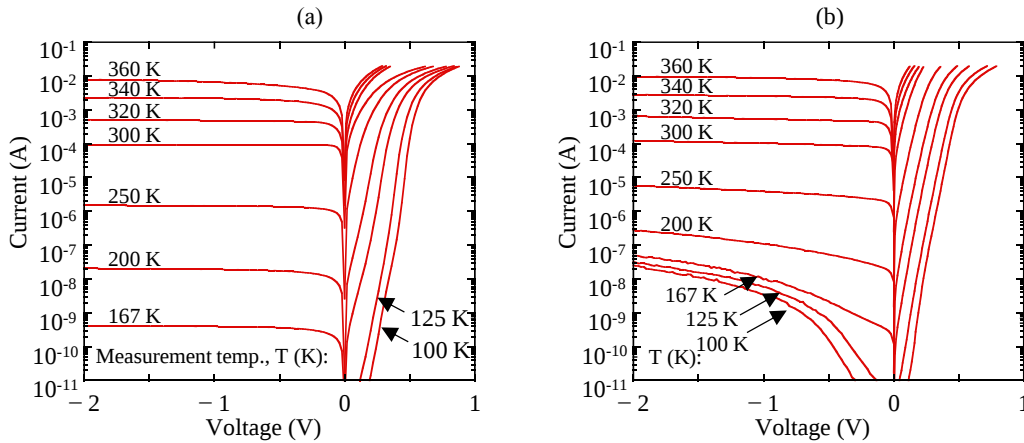


Fig.3 Forward and reverse I-V characteristics of solar cell diodes for various measurement temperatures. (a) Epitaxial-Ge/n-Ge and (b) Epitaxial- $\text{Ge}_{0.69}\text{Si}_{0.24}\text{Sn}_{0.07}/\text{n-Ge}$.

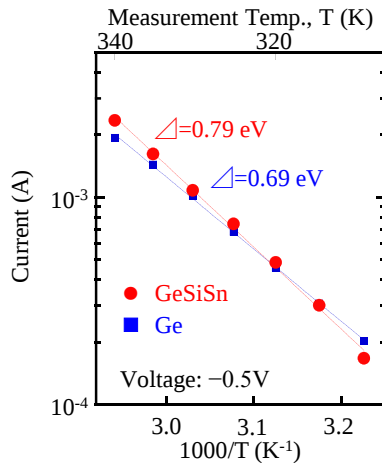


Fig.4 Arrhenius plot of the reverse current at 0.5 V reverse bias for Ge/n-Ge and $\text{Ge}_{0.69}\text{Si}_{0.24}\text{Sn}_{0.07}/\text{n-Ge}$ samples.

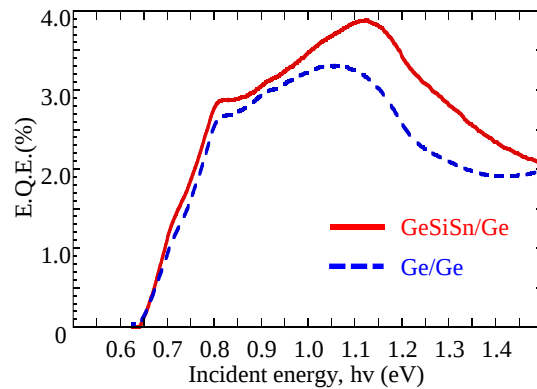


Fig.5 External quantum efficiency of undoped $\text{Ge}_{1-x-y}\text{Si}_x\text{Sn}_y/\text{Ge}$ and undoped Ge/n-Ge diodes at room temperature