The Metal Grating Design of Plasmonic Hybrid III-V/Si Evanescent Lasers

Min-Hsiang Hsu1, Chien-Chung Lin1*, and Hao-Chung Kuo2

1 Institute of Photonic System,
National Chiao Tung University, Tainan 711, Taiwan
2 Department of Photonics and Institute of Electro-Optical Engineering,
National Chiao Tung University, Hsinchu 30010, Taiwan
*E-mail address: chienchunglin@faculty.nctu.edu.tw

Abstract

The plasmonic hybrid III-V/Silicon evanescent lasers employing metal gratings have been theoretically demonstrated. The optimized threshold gain is of the order 0.6 μm⁻¹, comparable to current nanolasers and showing the possibility to be realized.

1. Introduction

Since electrically-pumped hybrid III-V/silicon evanescent laser was reported in 2006, such device designs have become popular [1]. Several improvements in hybrid laser cavity designs [2], passive waveguide structure [3], epitaxial materials [4], and bonding process [5] have been demonstrated to achieve better performance than the prototype. Despite the impressive device performance of those demonstrations, the dimensions of the silicon waveguide in above devices still fall behind what the photonic roadmap predicts [6]. If this roadmap needs to be followed and the passive waveguide is shrunk down to hundreds of nanometer scale, below the cut-off dimensions, the waveguide will not be able to support the mode anymore, and leading to design failure [6]. Therefore, we propose a plasmonic hybrid III-V/Si evanescent laser, as shown in the Fig. 1(a), which combines the merits of optical gain of III-V chip and small dimension of plasmonic waveguide, to achieve a feasible Si-based laser design in a nano-scale platform. In this paper, the numerical study of such design will be performed, and general device dimensions and performance will be evaluated through simulations.

![Fig. 1. The schematics of the proposed hybrid III-V/Silicon evanescent lasers.](image)

2. Simulation Model

A 3-dimensional (3-D) eigenmode calculation at wavelength 1.55 μm was established by the finite-element method (FEM) using COMSOL differential equation solver. The III-V structure in our proposed device, as shown in Fig. 1(b) and 1(c), consists of 1-μm InP substrate, 0.25-μm Alₐ₀.₁₃₁In₀·₅₂₅Ga₀·₄₇₃As separated confinement heterostructure (SCH), 0.125-μm Al₀.₃₃₅In₀.₆₆₅Ga₀.₂₉₂As quantum well (QW) and 0.12-μm InP contact layer, respectively. The height of the embedded silicon waveguide, H, can be treated as one of the variables, and the width of the waveguide is fixed at 300nm; the thickness of Al layer located at bottom of the silicon ridge waveguide is 0.05 μm. The dimensions of metal gratings are h in depth, w in width and 0.1 μm in length and their periods are p (h, w, and p will be used as design variables in the latter contents.).

In order to address the real application and determine the optimization criterion of our proposed design, the threshold gain in a general laser device can be calculated by the formula:

\[ g_{th} = \frac{(n_{eff,QW}/n_{eff})}{4\pi n_{eff}(\lambda + \ln(1/R^2)/(2\lambda))} \]

where \( g_{th} \) is threshold gain, \( n_{eff} \) is effective index of real part and imaginary part, respectively, \( \lambda \) is free wavelength 1.55 μm, \( R \) is reflectivity, \( l \) is waveguide length assumed to be 30 μm, \( \Gamma_{QW} \) is power confinement in quantum wells and \( n_{QW} \) is the refractive index equal to 3.6594 [7]. Note that we also assume the reflection loss is resulted from dielectric-air Fresnel reflection instead of from mirrors or any other reflectors. Therefore,

\[ R = (n_{eff} - 1)/(n_{eff} + 1) \]

Low threshold gain indicates low loss for the system, but mode distribution in waveguide cavity and III-V gain medium should be considered simultaneously. Owing to multiple device structure parameters needed to be determined, the waveguide height (H), grating depth (h), width (w) and period (p) are chosen to be optimized.

3. Results & Discussion

First, the embedded ridge height (H) and Al grating depth (h) are set as variables to test their influences on the optical behaviors, and, in the beginning, grating width (w), height (h) and period (p) are 0.1, 0.1 and 0.3 μm, respectively. When we increase the height of buried silicon waveguide from 0.3 to 0.9 μm, transformation in optical field distribution is expected. In the short waveguide height, only surface plasmonic polariton (SPP) mode on the bottom of Al layer is possible (like in 0.3 μm case of Fig. 2(b)). However, if the waveguide ridge is too high, as in Fig. 2(d), multiple SPP modes can be seen in the waveguide. When the design falls in between these two situations, it will be
the best for the lasers device (as in Fig. 2(c)). Therefore, the ridge height is fixed at 0.6 µm to ensure there is only one circular guided mode supported in the waveguide. Then, we simultaneously change the width and height of metal gratings, and calculate their field distributions and threshold gains, as shown in the Fig. 3. In the Fig. 3(a), the device with the metal grating width 0.3 µm exhibits the lowest threshold gain. At the metal grating with width 0.3 µm, the device with 0.1 µm grating depth is suitable for device because not only the device has less threshold gain but also equal energy distribution in QW and waveguide can be established such that the light can transmit in the III-V material and silicon structure simultaneously, as energy distributions shown in the Fig. 3(b).

The calculated threshold gain of the device. The corresponding energy density distribution with the metal grating separation is large as shown in the Figure 4(a). From the cross-section view of field distribution, the distribution of energy oscillates back and forth between the waveguide and QW regions. When the field is concentrated in the upper QW part, the threshold tends to be the lowest. On the other hand, when field maximized in the silicon waveguide region, the threshold gain increases sharply. The balance between the requirement of light transmission and lasing operation is an important issue for the engineer to decide, and a suitable combination of the two extremes (energy in QW or silicon waveguide) can generate the best working device such that the energy can be transmitted inside the waveguide and replenished by the gain of QW at the same time. In our simulation, we believe the case of period 0.73 µm (Fig. 4(f)) should be most suitable for the generic design because of its balanced field distribution between QW and silicon plasmonic waveguide, corresponding to the threshold gain 0.62 µm⁻¹.

Finally, the grating period is varied systematically to examine the field distribution and the threshold gain. The power distribution undulates corresponding to the metal grating period, leading the threshold gain to oscillate as well. The threshold gain becomes stable to 0.5 µm⁻¹ (=5000 cm⁻¹) when the metal grating separation is large as shown in the Figure 4(a). From the cross-section view of field distribution, the distribution of energy oscillates back and forth between the waveguide and QW regions. When the field is concentrated in the upper QW part, the threshold tends to be the lowest. On the other hand, when field maximized in the silicon waveguide region, the threshold gain increases sharply. The balance between the requirement of light transmission and lasing operation is an important issue for the engineer to decide, and a suitable combination of the two extremes (energy in QW or silicon waveguide) can generate the best working device such that the energy can be transmitted inside the waveguide and replenished by the gain of QW at the same time. In our simulation, we believe the case of period 0.73 µm (Fig. 4(f)) should be most suitable for the generic design because of its balanced field distribution between QW and silicon plasmonic waveguide, corresponding to the threshold gain 0.62 µm⁻¹.

Fig. 2. (a) The corresponding schematic to the simulated energy density distribution results with (b) 0.3 µm-, (c) 0.6 µm- and (d) 0.9 µm-high waveguide.

Fig. 3. (a) The calculated threshold gain of the device. The inset just zooms in the range of threshold gain from 0 to 8 µm⁻¹. The corresponding energy density distribution with (b) 0.1 µm-, (c) 0.2 µm-, (d) 0.3 µm-, and (e) 0.55 µm-deep gratings, which are labeled in the (a) via red circles.

Fig. 4. (a) The calculated threshold gain of the device. The corresponding energy density distribution with the grating period at (b) 0.29 µm, (c) 0.5 µm, (d) 0.73 µm, (e) 0.87 µm, (f) 0.93 µm and (g) 1.21 µm, which are labeled in the (a) via red circles.

4. Conclusions

We propose a method to fabricate nano-scaled hybrid III-V/Silicon laser device by incorporating the plasmonic effect from the embedded metal grating. The calculated gain threshold 0.62 µm⁻¹ of the optimized device shows great potential to realize this design in practice.

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