Comparative Study of Silicon Nanowire Transistors with Triangular-Shaped Cross Sections

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Abstract

Nanowire transistors with triangular cross sections (TNWT) are proposed and studied. TNWT’s working mechanism and the influence of physical parameters are investigated with TCAD tools. It is found that TNWT’s conducting area expands from the channel center to the triangle’s vertices with higher gate bias. TNWT has larger cross section than its counterpart with inscribed circle nanowire, thus exhibiting higher drain current and cut-off frequency. Moreover, we also find that TNWT with moderate angle is less affected by SCE, and shows lower subthreshold slope.

1. Introduction

As device dimension keeps diminishing, numerous novel structures, such as silicon on insulator (SOI), Schottky barrier (SB) MOSFETs, and nanowire transistors (NWT), have been proposed to maintain large drain current and minimize short channel effect (SCE). Among these devices, NWTs with gate-all-round (GAA) structure are reported to have excellent gate control and high \( I_{on}/I_{off} \) ratio. Recently, NWTs with triangular-shaped cross sections (TNWT) have attracted much attention, due to the manufacture characteristic of self-stopping on \([111] \) planes with anisotropic wet etch. Another feature of TNWT is that the triangular cross section can be further reduced by oxidation thinning method without long-time high temperature procedure. Several experimental studies have been conducted on TNWT fabrication. In this paper, we focus on analyzing the working mechanism of TNWT, and the influence of device parameters on TNWT’s performance is also investigated by comparison.

2. Device structure and simulation parameters

The schematic structures of NWT and TNWT are shown in Figs. 1(a) and 1(b), respectively. For both NWTs and TNWTs, the gate length, \( L_g \), varies from 10 to 16 nm, and the source/drain (S/D) length, \( L_{sd} \), varies from 10 to 20 nm. According to the experiment, the channel is along \(<110> \) direction, and n-type channels are mainly focused in this program. The channel doping, \( N_{ch} \), is \( 10^{19} \) cm\(^{-3} \), and the S/D concentration, \( N_{sd} \), is \( 10^{20} \) cm\(^{-3} \). The cross sections of NWT and TNWT are illustrated in Figs. 1(c) and 1(d), respectively. For TNWTs, the height of the triangle, \( H \), varies from 3 to 8 nm. The angle between \([100] \) and \([111] \) plane, \( \theta = \arctan \frac{3^{1/2}}{2} \approx 54.74^\circ \). For NWTs, the cross section is the inscribed circle of TNWT’s triangle, and the nanowire diameter, \( D = \left( \frac{3}{2} - 1 \right) \times H = 0.75H \). The drain voltage, \( V_D \), is 0.5 V, and the equivalent gate oxide thickness, \( T_{ox} \), is 1 nm. The default gate work function, \( GWF \), is 4.2 eV, and work function engineering is applied to get proper voltage bias.

3. Results and discussion

The transfer curves of NWT and TNWT are displayed in figure 2. TNWT’s drain current is higher than that of NWT, because of a relatively larger cross section. From the log plot, we can see that TNWT has higher \( I_{on}/I_{off} \) ratio. Figure 3 shows the electron current density distributions along (a) NWT’s and (b) TNWT’s channel, respectively. NWT’s conducting area has a certain distance to channel surface, which is the result of density-gradient potential quantum correction model. TNWT transports carriers mostly in the center of the channel. Figure 4 explains this phenomenon by plotting the current density distributions perpendicular to channel with (a) \( V_D = V_{off} + V_{th} \), and (b) \( V_D = V_{off} + 1 \) V, respectively. With larger gate bias, TNWT’s conducting area expands to the vertices of the triangle, because of the higher electric field at corners.

From Fig. 5 to Fig. 7, the effect of \( D & H \) on device performance is discussed. OFF-state current is fixed at \( 1 \) nA/\( \mu m \), and normalized drain current is calculated for comparison. In figure 5, \( I_{on} \) of NWT and TNWT with \( L_g = 16 \) nm increase as \( D & H \) increase, because conducting area has higher growth rate than device perimeter. \( I_{on} \) of \( L_g = 10 \) nm has a turning point, due to the increase of subthreshold slope (SS) induced by SCE. Figure 6 shows the relation between gate work function difference, \( \Delta GWF \), and physical parameters. When \( D & H \) increase, NWT and TNWT require larger work function to keep \( I_{off} \) fixed at \( 1 \) nA/\( \mu m \). TNWT with \( L_g = 16 \) nm exhibits lower \( \Delta GWF \) than NWT. The SS & DIBL versus \( H \) curves for TNWT is plotted in figure 7. When \( H \) increases, the drivability of TNWT is reduced, which leads to the increment of SS and DIBL. TNWT with \( L_g = 16 \) nm keeps SS < 70 mV/dec and DIBL < 20 mV, which indicates excellent gate control over channel.

The influence of \( \theta \) on TNWT’s performance is studied in Figs. 8 and 9. Figure 8 shows the relation between \( I_{on} \) and \( \theta \). \( I_{on} \) of TNWT with \( L_g = 10 \) nm and \( H = 4 \) nm increases as \( \theta \) increases, while there is a turning point for \( H = 6 \) nm. This is because \( L_g \) with \( H = 6 \) nm is influenced by the decrease of conducting area when \( \theta > 45^\circ \), and short \( L_g \) with \( H = 4 \) nm mainly benefits from less SCE. In figure 9, it is seen that, for all structures, \( \Delta GWF \) decreases as \( \theta \) increases, because gate modulation ability is enhanced by large \( \theta \). The relation between SS and \( L_g \) is displayed in figure 10. TNWT with \( \theta = 75^\circ \) has lowest SS, which exhibits better gate control property. TNWT with \( \theta = 54.74^\circ \) shows nearly the same SS as NWT when \( L_g > 10 \) nm. In figure 11, we further investigate the relation between cut-off frequency, \( F_c \), and dimension parameters. We can see that TNWT has higher \( F_c \) than NWT, because larger cross section provides higher transconductance and intrinsic gain. \( F_c \) of NWT and TNWT with \( L_g = 10 \) nm decrease when \( D & H > 5 \) nm, because short \( L_g \) loses control of channel due to SCE.

4. Conclusion

In this paper, the characteristics of TNWT have been simulated and investigated. It is found that TNWT’s current mostly concentrates in the center of the channel, and expands to the corners of the triangle with high gate voltage. TNWT exhibits higher drive current and cut-off frequency, due to its larger cross section. It is also observed that TNWT with \( L_g = 16 \) nm has lower \( \Delta GWF \) than NWT, and smaller SS & DIBL than \( L_g = 10 \) nm. As the expanding analysis, we find that TNWT with moderate \( \theta \) can make tradeoff between larger conducting area and less SCE.

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