

Highly Reduced Current Collapse in AlGa_{0.2}N/GaN HEMTs by Combined Application of Oxygen Plasma Treatment and Field-plate Structures

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Abstract

Using the oxygen plasma treatment alone, we have dramatically cut down the current collapse in AlGa_{0.2}N/GaN high-electron mobility transistors (HEMTs) as evidenced by more than an order of magnitude reduction in normalized dynamic R_{on} (NDR) with respect to that of untreated reference devices. By incorporating the field-plate (FP) structure in these oxygen plasma treated devices, we were able to further reduce the NDR by almost 45 %, demonstrating for the first time the additive effect of oxygen plasma treatment and field-plate structures in mitigating current collapse.

1. Introduction

With so many desirable intrinsic properties, gallium nitride (GaN) has become one of the leading material candidates for realizing devices are capable of handling unprecedented power levels and frequencies well beyond those possible with the conventional silicon-based devices [1]. Nevertheless, the widespread adoption of GaN-based devices is still limited due to several issues, foremost of which is the well-known current collapse. Although still under debate, it is widely believed that current collapse is predominantly due to charging of traps on the AlGa_{0.2}N surface [2]. Accordingly, current collapse is usually addressed by schemes involving the device surface such as passivation [3], plasma surface treatment [4], or adding a cap layer [5]. Aiming for a collapse-free operation, our group has been combining some of these different schemes. Results from one of our previous works suggest that employing two different solutions to current collapse does not necessarily guarantee linear (additive) effect. For instance, we have found out that GaN cap layer has no further effect on the reduction of current collapse in the devices subjected to oxygen (O₂) plasma treatment [6].

Meanwhile, field-plate (FP) technology is a well-known approach for achieving not only high breakdown voltages but also reduced current collapse by reducing the electric field strength at the drain edge of the gate [7]. Furthermore, we have found out that the partial depletion of 2DEG electrons can be instantly recovered by field effect of a positively biased FP electrode [8]. In this work, we demonstrate for the first time highly-suppressed current collapse in AlGa_{0.2}N/GaN HEMTs by combined application of O₂ plasma treatment and field-plate structures.

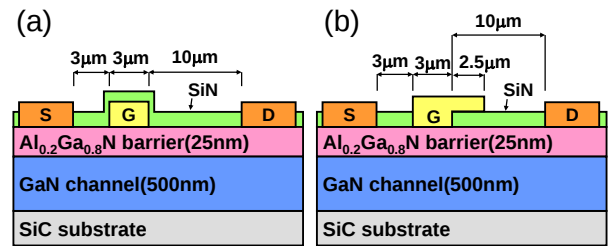


Fig. 1. Simplified schematic cross-section of (a) device A (O₂ plasma-treated device without FP) and (b) device B (O₂ plasma-treated device with FP).

2. Experimental

Three different devices were fabricated and investigated in this work, namely, device A (O₂ plasma-treated device without FP), device B (O₂ plasma-treated device and with FP), and device C (without O₂ plasma treatment and without FP). The device fabrication started with the formation of mesa isolation structures by inductively coupled plasma reactive ion etching (ICP-RIE), using a gas mixture of BCl₃/Cl₂. To form the drain and source ohmic electrodes, a Ti/Al/Mo/Au (15/60/35/50 nm) metal stack was then deposited by electron beam evaporation and annealed under N₂ atmosphere at 880 °C for 30 s. For devices A and B, prior to SiN passivation, the AlGa_{0.2}N surface was subjected to O₂ plasma treatment using a plasma power of 100 W and exposure time of 1 min. The gate window was then formed by removing portions of the SiN film by dry etching, followed by Schottky gate (device A) or Schottky gate and FP formation (device B) with Ni/Au (50/150 nm) bilayer. The reference device C was fabricated using exactly the same procedure as that of device A, minus the O₂ plasma treatment. The simplified schematic cross-section images of devices A and B are shown in **Figs. 1 (a)** and **(b)**, respectively.

After confirming that the devices have essentially identical DC characteristics, we then performed current collapse evaluation by carrying out stress- and time-dependent dynamic R_{on} measurements [8]. A train of gate pulses alternating between -5 V (OFF-state) and +1 V (ON-state) was applied to the gate terminal. At every pulse on-time duration (t_{on}), the corresponding $V_{ds}(t_{on})$ and $I_{ds}(t_{on})$ were recorded and used to compute the resulting dynamic R_{on} . We used the normalized dynamic R_{on} (NDR), which is defined as the ratio of dynamic R_{on} to static R_{on} , to represent the degree of current collapse.

3. Results and Discussion

Figures 2 (a) and (b) show the dependence of NDR on t_{on} for devices A and C respectively. Compared with the reference device C, device A exhibited lower corresponding NDR, suggesting the effectiveness of O₂ plasma treatment alone in reducing current collapse. As proposed by Del Alamo and co-workers [9], NDR can be expressed as a sum of pure exponential terms in the form:

$$NDR = 1 + \sum_{i=1}^n \alpha_i \exp\left(-\frac{t}{\tau_i}\right) \quad (1),$$

where, α_i represents the magnitude of the trapping process with time constant τ_i . Fitting the experimentally measured NDR with the above relationship, we were able to obtain the best fit curves (solid curves) and the corresponding exponential terms (dashed curves) given in Fig. 2. Each dashed curve represents a trap level with associated $(E_C - E_t)$ energy value. By performing a detailed analysis using Shockley-Read-Hall (SRH) statistics [10], we came up with the conclusion that O₂ plasma treatment leads not only to significant reduction in surface trap densities, as suggested by its corresponding smaller α_i values, but also to elimination of the two deepest trap levels traps 5 and 6.

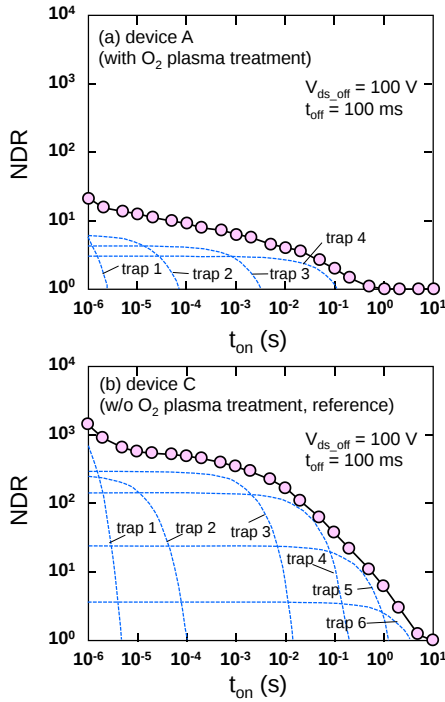


Fig. 2. Measured (solid data points) and calculated (solid curves) normalized dynamic Ron (NDR) dependence on t_{on} of (a) device A (O₂ plasma-treated device without FP) and (b) reference device C. Dashed curves are extracted exponential terms each representing a trap level.

To investigate if further reduction of current collapse is possible with the application of FP structures, we then performed dynamic R_{on} measurements under different V_{ds_off} for devices A and B. As can be seen in Fig. 3, device B exhibited smaller NDR values, which seems to be further reduced with increasing V_{ds_off} . At $V_{ds_off} = 250$ V, a 45 % reduction in

NDR was achieved. We believe that the effectiveness of the combined approaches is due to the fact that each method offers non-overlapping solution to current collapse. While O₂ plasma treatment reduces and/or eliminates surface trap densities responsible for current collapse, the FP structure not only reduces the peak electric field that leads to surface charging at the drain edge of the gate but also instantly recovers by field effect of a positively biased FP electrode the partial depletion of 2DEG.

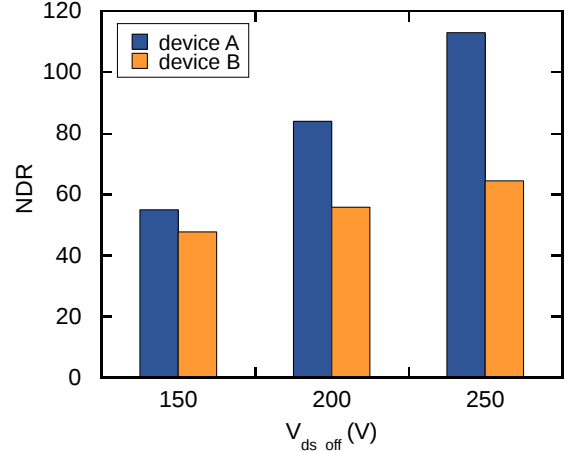


Fig. 3. V_{ds_off} dependence of NDR for device A (O₂ plasma-treated device without FP) and device B (O₂ plasma-treated device with FP).

4. Conclusions

In summary, by oxygen plasma treatment alone, we have dramatically reduced the current collapse in Al-GaN/GaN high-electron mobility transistors (HEMTs) as evidenced by more than an order of magnitude reduction in normalized dynamic R_{on} (NDR) with respect to that of untreated reference devices. By incorporating the field-plate (FP) structure in these O₂ plasma treated devices, we were able to further reduce the NDR by almost 45%, thereby demonstrating for the first time the additive effect of O₂ plasma treatment and field-plate structures in mitigating current collapse. We believe that the efficacy of the combined approaches is due to the fact that each method offers non-overlapping solution to current collapse.

References

- [1] L. F. Eastman and U. K. Mishra, *IEEE Spectrum* (2002) 28.
- [2] R. Vetury et al., *IEEE Trans. Electron Devices* 48, 560 (2001).
- [3] T. Hashizume et al., *J. Vac. Sci. Technol. B* 21, 1828 (2003).
- [4] Y. Sakaida et al., *Dig. CSMANTECH*, Denver, CO, USA, 2014, p. 197.
- [5] T. Kikkawa, *Jpn. J. Appl. Phys.* 44 (2005) 4896.
- [6] J. T. Asubar et al., submitted to *Appl. Phys. Exp.*
- [7] Y. Ando et al., *IEEE Electron Device Lett.* 24 (5), 289 (2003)
- [8] M. T. Hasan et al., *IEEE Electron Device Lett.* 34, 1379 (2013).
- [9] J. Joh et al., *IEEE Trans. Electron Devices* 58, 132 (2011).
- [10] W. Shockley et al., *Phys. Rev.* 87, 835 (1952).