# Stochastic Macro-model of Magnetic Tunnel Junction for Spice simulation about Writing Current Density Dependence on Resistance Variation

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## Abstract

A resistance distribution of magnetic tunnel junction (MTJ) shows non-uniformity according to various MTJ parameters. This resistance variation leads to write-current density variation which causes serious problem on realizing commercial Gbit STT-MRAM. In this paper, we investigated the stochastic behavior model on write current dependence on MTJ resistance variation. The proposed model is possible to analyze the write current density according to resistance variation of MTJ with various parameter variation. It can be very helpful considering MTJ variation for designing and simulating STT-MRAM.

# **1. Introduction**

Spin-Transfer-Torque Magneto Resistance RAM (STT-MRAM) is one of the most promising candidates for nextgeneration universal memory due to its non-volatility, high density, low power consumption and high operation speed. STT-MRAM is composed of Magnetic Tunnel Junction (MTJ), which is consisted of a thin tunneling oxide between pinned and free magnetization layer. The resistance of MTJ is defined to parallel state  $(R_P)$  and anti-parallel state  $(R_{AP})$ according to magnetization angle between the two ferromagnetic layers, and the resistance difference between two states is called Tunneling Magneto-resistance Ratio (TMR) [1]. Many researches proposed macro-model based on MTJ behavior principles, but they commonly used tunnel conductance model or constant value [2-4]. In 2014, we proposed a macro-model which applied the resistance variation of MTJ according to various parameters [5]. However, the resistance variation of MTJ simultaneously affect the amount of current required for switching the state of MTJ [6], and these may cause write failure of STT-MRAM. In this paper, we investigated Macro-model of MTJ write current density (Ic) of MTJ according to resistance variation related to tox, A, T and Vb variation using Verilog-A language.

# 2. Stochastic Modeling of MTJ Resistance and Writing current

#### MTJ resistance modeling

In previous research, we fabricated a MTJ with B2-ordered Co2FeAl (CFA) full-Heusler alloy in order to observe the MTJ resistance variation tendency [5]. We proposed MTJ resistance variation model according to  $t_{ox}$ , A, T, and V<sub>b</sub> variations [5]. Using Brinkman's tunneling conductance, resistance of MTJ at parallel state can be obtained by Eq. (1)



Fig. 1 (a) Comparison of MTJ resistance between simulation and measurement results and (b) the simulation results of P and  $\eta$  variation according to TMR variation

$$R_o = \frac{t_{ox}}{F \cdot \sqrt{\varphi_{M_go}} \cdot A} exp\left(1.025 \cdot t_{ox} \cdot \sqrt{\varphi_{M_go}}\right) \tag{1}$$

where  $t_{ox}$  and A is oxide thickness and surface area, and  $\varphi_{MgO}$  is potential barrier height for MgO which is 0.38~0.4 eV, and *F* is constant which is fitting parameter corresponding to  $t_{ox}$  and *A* [7]. Then, since the TMR is exponentially proportional to RA [8], actual TMR value according to RP variation can be obtained by Eq. (2)

$$MR_{real} = TMR_o \cdot (1 + (R_o - R_{oreal}) \cdot \beta)$$
<sup>(2)</sup>

where  $TMR_o$  is TMR at  $R_o$ ,  $R_{oreal}$  is the resistance with  $t_{ox}$  and A variation, and  $\beta$  is constant which is variation ratio between  $R_P$  and TMR. From previous studies [9,10], we confirmed that MTJ resistance is affected by various factors below

T

- 1. Increasing T decreases the spin polarization P according to spin polarized conductance model, and leads to MTJ resistance decrement [9].
- 2. Since the majority spin tunneling dominates the overall conductance,  $R_{AP}$  shows much stronger dependence on temperature than  $R_P$  [10].
- 3. Since the bias dependence of MTJ resistance is caused by Magnon-assist tunneling, R<sub>AP</sub> shows much stronger bias dependence than R<sub>P</sub> [10].

Analyzing the effects from measured data, we realized the actual resistance modeling of  $R_P$  and  $R_{AP}$  by Eq. (3) and (4)

$$R_{Preal} = R_{oreal} \cdot exp\left(-\frac{|V_b|}{\gamma}\right) \cdot exp\left(\frac{300 - T}{\eta T}\right)$$
(3)

$$R_{APreal} = R_{Preal} \cdot \left(1 + TMR_{real}\right) \cdot exp\left(-\frac{|V_b|}{\lambda(1 + \kappa\left(\frac{T - 300}{T}\right))}\right) \cdot exp\left(\frac{300 - T}{T}\right)$$
(4)

where  $R_{oreal}$  is determined from variable  $t_{oxreal}$  and  $A_{real}$ . V<sub>b</sub> is bias voltage induced at MTJ,  $\gamma$  is constant related to the V<sub>b</sub> dependence, and  $\eta$  is constant which related to the temperature dependence. Fig. 1 (a) shows comparison of MTJ resistance between the simulation and measured data with V<sub>b</sub> from -0.7 to 0.7 at various temperature (300K, 340K, 360K). *Write Current modeling* 

As previously mentioned, the variation in resistance leads to writing current variation. With larger resistance, larger amount of current if required to change the state of MTJ. According to Z. Diao, increasing TMR contributes to reduction in Jc0 significantly [6]. This is because of Polarization factor (P) which can be calculated by Eq. (5).

$$P = \sqrt{\frac{TMR}{2 + TMR}} \tag{5}$$

From the P factor, we can obtain spin transfer torque efficiency ( $\eta_{AP}$  and  $\eta_{P}$ ). Moreover, STT efficiency shows asymmetric value according to MTJ state, and is normally lager in AP state than P state. The asymmetric characteristics has not been clearly defined by theory, and we assumed the ratio between  $\eta_{AP}$  and  $\eta_{P}$  by Eq. (6), where 0.6 is obtained by fitting result with P-MTJ introduces by D. Suzuki [11]..

$$\eta_{AP} = \frac{2P}{1+P^2}, \qquad \eta_P = \frac{2(0.6P)}{1+(0.6P)^2}$$
 (6)

Fig. 1 (b) shows the simulation results of P and  $\eta$  variation according to TMR variation, where ideal TMR is set 1. From the STT efficiency obtained from Eq. (6), it is now able to obtain the switching current required to switch the MTJ state from AP to P and P to AP. Next, the write current density (J<sub>C</sub>) can be obtained by Eq. (7) and (8).

$$Jco_{AP \to P} = \frac{1}{\eta_P} \frac{2\alpha e}{\hbar} (M_{st_{free}}) H_{\kappa} = 1.5887 MA/cm^2$$
(7)

$$Ico_{P\to AP} = \frac{1}{\eta_{AP}} \frac{2\alpha e}{\hbar} (M_{stfree}) H_{K} = 1.5887 MA/cm^{2}$$
(8)

where  $\alpha$  is Gilbert damping constant  $M_S$  is saturation magnetization,  $t_{free}$  is free layer thickness and  $H_K$  is anisotropy field. *Simulation result* 

P-MTJ introduced by D. Suzuki in 2014 showed 50  $\mu$ A for P to AP and 70  $\mu$ A for AP to P switching [11]. Based on this, we simulated by assuming that I<sub>CPtoAP</sub> is 50  $\mu$ A for MTJ with cell diameter of 75 nm. Fig. 2 (a) shows I<sub>CP</sub> and I<sub>CAP</sub> fitting results between data proposed by D. Suzuki [11] and simulation results, and Fig. 2(a) shows the simulation results of I<sub>CP</sub> and I<sub>CAP</sub> variation according to TMR variation with 1 % tox, 10 % TMR variation and 500 mV bias voltage. The figure indicates that this model is well fitted to previous researches, and can be well fitted for MTJ simulation.

#### **3.** Conclusions

A macro-model of MTJ resistance and switching current



Fig. 2 (a) Comparison between measure data and simulated data and (b) the simulation results of  $I_{CP}$  and  $I_{CAP}$  according to TMR variation.

variation has been investigated for Hspice simulation. Our model would be helpful for the engineers to consider the resistance and switching current variation according to not only process variation but also operating conditions while performing STT-MRAM simulation.

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# References

- Shoji Ikeda et al., IEEE Trans. on Electron Devices 54 (2007) 991.
- [2] Sakimura, N. et al., In Circuits and Systems (ISCAS) IEEE International Symposium on (2012) 1971.
- [3] Zhao, Weisheng, et al., Behavioral Modeling and Simulation Workshop, Proceedings of the 2006 IEEE Int. (2006) 40.
- [4] Lee, Seungyeon, et al., Jpn. J. Appl. Phys. 44 (2005) 2696.
- [5] G. H. Kil, et al., Jpn. J. Appl. Phys., 54 (2015), 04DD12
- [6] Z. Diao, et al., Appl. Phys. Lett 87 (2005), 232502
- [7] Z. M. Zeng et al., Appl. Phys. Lett. 98 (2011) 0723512.
- [8] K. Masu et al., Advanced Metallization and Interconnect Systems for ULSI Applications (2000) 14.
- [9] Chang he Shang et al., Phys. Rev. B 58 (1998) R2917.
- [10] J. S. Moodera et al., Phys. Rev. Lett., 80 (1998) 2941.
- [11] D. Suzuki et. al., Jpn. J. Appl. Phys, 115 (2014) 17B74