Strong Impacts of Top Blocking Layer on Initial Flatband Voltage in Hf-based High-k Charge-Trapping Memory

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Abstract

Impacts of top blocking layers on initial flatband voltage are investigated in nMOS capacitors with a HfSiO₂ charge-trapping layer. While the top blocking layer on HfSiO₂ significantly affects the memory window in charge-trapping memory, we found that an initial flatband voltage also largely depends on the structure of top blocking layer such as Al₂O₃ and stacked Al₂O₃/SiON/Al₂O₃. Our experimental results indicate that this is related to oxygen vacancies in HfSiO2 charge-trapping layer. The SiON thickness in top blocking layer is a key factor to control the initial flatband voltage.

1. Introduction

Charge-trapping (CT) memory devices have been realized by using the oxide-nitride-oxide (ONO) structure for a long time. The replacement of ONO with other CT structures using high-k dielectrics allows us to scale the equivalent oxide thickness (EOT) for lowering operating voltage. HfO₂based high-k such as HfSiO₂ [1] and HfO₂ [2-3], and Al₂O₃based high-k dielectrics such as Al₂O₃ [4-5] and Al₂O₃/SiO₂/Al₂O₃ [6] have been intensively studied as alternative CT layers and top blocking layers, respectively.

When high-k dielectric stacks are used in MOS devices, flatband voltages ($V_{\rm fb}$) are anomalously shifted by interface dipoles [7-9] and oxygen vacancies [10-11] and so on. An initial $V_{\rm fb}$ significantly affects program/erase speed and the memory window. However, changes in the initial $V_{\rm fb}$ have rarely been investigated for CT structures with high-k dielectrics. In this paper, impacts of top Al₂O₃-based blocking layers combined with a HfSiO₂ CT layer on initial $V_{\rm fb}$ are investigated. It is found that a replacement of the top-Al₂O₃ blocking layer (Top-Al₂O₃) with Al₂O₃/SiON/Al₂O₃ (Top-AOA) induces a large positive $V_{\rm fb}$ shift. The possible mechanism is also discussed.

2. Experimental

nMOS capacitors with CT structures of top Al₂O₃-based blocking layer/HfSiO₂/bottom-oxide (Btm-Ox) were fabricated on p-type Si(100) substrate as shown in **Fig 1**. After the Btm-Ox formation, HfSiO₂ CT layers were deposited by atomic layer deposition (ALD) technique. The Si content {Si/(Si+Hf)} of ~20% is controlled in HfSiO₂ by changing cycle ratio in ALD [1]. Top-Al₂O₃ and Top-AOA blocking layers were also deposited by using ALD technique. N⁺poly-Si gate electrodes were formed and then activation annealing was performed for 5sec at 950°C. 100kHz capacitance-voltage (*C-V*) measurements were conducted to extract initial $V_{\rm fb}$ of these capacitors.

3. Results and Discussion

Figure 2 shows initial C-V curves of nMOS capacitors with Top-Al₂O₃/HfSiO₂/Btm-Ox and Top-AOA/HfSiO₂/Btm-Ox structures. A large positive shift is observed when the top blocking layer is changed from Top-Al₂O₃ to Top-AOA.

Figure 3 shows the $V_{\rm fb}$ for Top-AOA/HfSiO₂/Btm-Ox as a function of the SiON thickness in Top-AOA. Compared with Top-Al₂O₃, the $V_{\rm fb}$ remains almost unchanged for Top-AOA with thin SiON. The initial $V_{\rm fb}$ shifts to the positive gate direction about 1V for thick SiON. This result indicates that

the SiON thickness in Top-AOA has a strong impact on the initial $V_{\rm fb}$. The increase in initial $V_{\rm fb}$ raises program voltage for FN electron tunneling, as shown in **Fig. 4**.

Here we consider three possible origins for the large initial $V_{\rm fb}$ shift: (i) fixed charges in Top-AOA, (ii) interface dipoles in Top-AOA [7-9], and (iii) formation of oxygen vacancies in HfSiO₂ [10-11]. **Figure 5** shows the $V_{\rm fb}$ of nMOS capacitors for Top-AOA/HfSiO₂/Btm-Ox as a function of the Al₂O₃ thickness in Top-AOA. The initial $V_{\rm fb}$ is almost unchanged when the Al₂O₃ thickness is decreased in Top-AOA. Therefore, the positive initial $V_{\rm fb}$ shift by changing from Top-Al₂O₃ to Top-AOA is not related to fixed charges in Top-AOA [mechanism (i)].

Figure 6 shows the *C-V* curves for Top-OA, Top-AO and Top-AOA blocking layers. If interface dipoles in the Al₂O₃/SiON and SiON/Al₂O₃ interface are the main cause for the initial $V_{\rm fb}$ shift [mechanism (ii)], initial $V_{\rm fb}$ should be shifted by removing either Al₂O₃/SiON or SiON/Al₂O₃ interface. As shown in **Fig. 6**, the *C-V* curves are overlapped for Top-OA, Top-AO and Top-AOA except for Top-Al₂O₃. This result excludes the mechanism (ii) as the main cause for the positive initial $V_{\rm fb}$ shift in this study.

The above-mentioned discussions indicate that the mechanism (iii) is most likely. The positive shift in the initial $V_{\rm fb}$ is explained by considering formation of oxygen vacancies in HfSiO₂ according to the model for " $V_{\rm fb}$ roll-off" [10]. As illustrated in **Fig. 7**, in the case of Top-Al₂O₃ and Top-AOA with thin SiON, oxygen vacancies are formed in HfSiO₂ by out-diffusing oxygen atoms from HfSiO₂ to n⁺poly-Si for oxidation at the n⁺poly-Si/Al₂O₃ interface, assuming that Al₂O₃ has little function of an oxygen barrier. On the other hand, out-diffusion of oxygen atoms is suppressed from HfSiO₂ to n⁺poly-Si for Top-AOA with thick SiON thanks to its oxygen barrier characteristic. The initial $V_{\rm fb}$ apparently shifts to the positive gate voltage direction when using thick SiON in Top-AOA as shown in **Fig. 3**.

We expect that " V_{fb} roll-off" might occur by decreasing the Btm-Ox thickness in the case of Top-AOA with thick SiON, if the model for " V_{fb} roll-off" is applicable as mentioned above. **Figure 8** shows the V_{fb} as a function of the Btm-Ox thickness. An abrupt negative shift in the initial V_{fb} , namely " V_{fb} roll-off", is observed when decreasing the Btm-Ox thickness. This V_{fb} roll-off behavior for Top-AOA with thick SiON is consistent with mechanism (iii). A large negative shift is not observed for Top-Al₂O₃ in **Fig. 8**, since oxygen vacancies are thought to have already been formed by interface oxidation of n⁺poly-Si irrespective of the Btm-Ox thickness due to non-barrier properties of Al₂O₃.

4. Conclusions

The SiON thickness in a Top-AOA blocking layer has a strong impact on an initial $V_{\rm fb}$ in CT memory with HfSiO₂ CT layer. The initial $V_{\rm fb}$ shift is thought to be related to oxygen vacancy formation in HfSiO₂. Since oxygen vacancy is formed in HfSiO₂ by interfacial oxidation of both n⁺poly-Si gate and Si substrate, we should pay attention to both thicknesses of SiON or SiO₂ films which are located above or below HfSiO₂ to control the initial $V_{\rm fb}$.



Fig. 1 Process flow of MOS capacitors with HfSiO₂ charge-trapping layer. Top blocking layers of Al₂O₃ or AOA (Al₂O₃/SiON/Al₂O₃) are formed on HfSiO₂ by using ALD technique.



Fig. 3 Initial V_{fb} for Top-AOA/HfSiO₂/Btm-Ox as a function of SiON thickness in Top-AOA. An abrupt change is observed.







Fig. 8 Initial $V_{\rm fb}$ values (left) and schematic illustration of V_0^{++} formation (right) for (a) Top-AOA/HfSiO₂/Btm-Ox and (b) Top-Al₂O₃/HfSiO₂/Btm-Ox structures as a function of Btm-Ox thickness. (a) An abrupt negative $V_{\rm fb}$ sihft by decreasing the Btm-Ox thickness ($V_{\rm fb}$ roll-off) for Top-AOA and (b) almost no $V_{\rm fb}$ sihft are observed for Top-Al₂O₃.



Fig. 2 Initial *C-V* curves for Top-Al₂O₃/HfSiO₂/Btm-Ox and Top-AOA/ HfSiO₂/Btm-Ox structures. The *C-V* curve of Top-AOA shifts towards the positive gate voltage direction as compared to that of Top-Al₂O₃.



Fig. 4 V_{fb} shifts as a function of program voltage for Top-AOA with thin (open) and thick SiON (solid). Electrons are injected by FN tunneling.



Fig. 5 Initial V_{fb} for Top-AOA/HfSiO₂/ Btm-Ox as a function of Al₂O₃ thickness. V_{fb} remains unchanged.



Fig. 7 Schematic illustration of oxygen vacancy (V_0^{++}) formation by out-diffusing oxygen atoms from HfSiO₂ to n⁺poly-Si for oxidation at the n⁺poly-Si/Al₂O₃ interface. Apparently, V_{fb} shifts to the positive gate voltage direction as shown in Fig. 3 because V_0^{++} formation is suppressed for thick SiON in Top-AOA.

[1] M. Inoue et al., IWDTF, p.69, 2015. [2]
P.-H. Tsai et al., IEEE EDL, Vol.30, No.7,
p.775, 2009. [3] F. Driussi et al., IEEE TED, Vol.61, No.6, p.2056, 2014. [4] C. H.
Lee et al., IEDM, p.327, 2003. [5] H.-T.
Lue et al., ICSICT, p.807, 2008. [6] J.
Fujiki et al., IEDM, p.952, 2009. [7] K.
Kita et al., Ext. abstracts of SSDM, 2017,
pp.499-500. [8] K. Iwamoto et al., Appl.
Phys. Lett., 92, 132907 (2008). [9] H.
Kamata et al., Appl. Phys. Lett. 110, 102106
(2017). [10] K. Akiyama et al., Tech. Dig.
VLSI Symp. 2007, 72. [11] K. Akiyama et al., Tech. Dig. VLSI Symp. 2008, 80.

References