# Engineering of Ovonic Threshold Switching Selector: A Link from the Material Properties to the Electrical Performances

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Abstract - We present the engineering of Ovonic Threshold Switching (OTS) selectors integrating Ge<sub>3</sub>Se<sub>7</sub> based alloys. We show how doping allows the tuning of the material properties and of the device performances, enabling to fulfill different applications' requirements. In particular, we show that, whereas an optimized Sb&N-doping leads to an ultra-low leakage current, As<sub>2</sub>Te<sub>3</sub>-doping enables tunable and predictable parameters over a wide range of compositions and thicknesses.

## 1. Introduction

Crossbar Resistive Array (CRA) represents the main candidate for the development of Storage Class Memory. The leakage current  $I_{leak}$  and the sneak-paths, intrinsic to the parallelism of the CRA structure, require to add in series with the memory (1R) a selector device (1S). Selectors based on Ovonic Threshold Switching (OTS) mechanism [1] are considered today as good candidates among the different selector technologies, with already reliable demonstrations of co-integration with OxRAM [2,3] and PCM [4-6].

OTS materials are amorphous chalcogenide with the property to switch from a resistive state to a volatile conductive state, when the applied voltage reaches a threshold voltage  $V_{th}$ . The conductive state is sustained until the current is decreased below the holding current I<sub>h</sub>. OTS selector requires a first initialization pulse, called firing, characterized by a higher switching voltage ( $V_{fire}$ ) than the following stable  $V_{th}$ , and by an increase of  $I_{leak}$  (measured at  $V_{th}/2$ ). Reading operation in 1S1R relies on an indirect measurement of the memory that requires to adapt the OTS threshold current I<sub>th</sub> to the I-V characteristic of the high resistance state (HRS) of the memory [7]. Therefore, the functionality of the final 1R-1S device relies on a careful engineering of the OTS material to tune properly the selector electrical parameters.

In this paper, we present the engineering of OTS based on Ge<sub>3</sub>Se<sub>7</sub>(GS) alloy. We show how electrical parameters can be tuned by different dopants (**Table 1**), and we propose a benchmark between the different OTS materials developed. **2. Ultra-low leakage current in Sb&N-doped Ge<sub>3</sub>Se<sub>7</sub>** 

Doping GS with Sb to reduce  $V_{th}$  [8,9] induces the formation of Sb-Sb bonds (**Fig. 1**) that increases  $I_{leak}$  (Fig. 3), and degrades the thermal stability [9]. N-doping is known to improve the thermal stability of Ge-based chalcogenides [10]. By forming Ge-N bonds (**Fig. 2**) it permits to increase the glass rigidity and to suppress Sb-Sb bonds (Fig. 1), leading to very low  $I_{leak}$  and high thermal stability [7]. However, N-doping leads also to a high material disorder. Material reorganization upon thermal annealing at 400°C-30min occurs with the formation of a homogeneous GeN<sub>x</sub> phase, helping to decrease V<sub>fire</sub> by keeping a very low leakage current (**Fig. 3**). Sb&N-doped GS features an endurance of 10<sup>8</sup> cycles [11].

#### 3. Tunable parameters in As2Te3-doped Ge3Se7

As<sub>2</sub>Te<sub>3</sub> (AT), like Sb, can be used to decrease the high V<sub>th</sub> of GS. A limited chemical interaction between GS and AT (**Fig. 4**) leads to a linear decrease of E<sub>g</sub> with AT content (**Fig. 5**). Thus, both V<sub>th</sub> and I<sub>leak</sub> can be modeled by simple equations relying respectively on the linear decrease of the threshold electric field with AT content, and a linear decrease of the activation energy of the electrical conduction, mainly driven by the Poole-Frenkel mechanism (**Fig. 6**). Therefore, the OTS electrical parameters can be predicted over a wide range of compositions and thicknesses allowing a fine-tuning of the alloy. Reliability of AT-doped GS is demonstrated by an endurance of more than 10<sup>10</sup> cycles [12].

## 4. Correlation between Eg, Vfire and Ileak

Optical bandgap  $E_g$  is extracted by means of Tauc plot extrapolation from ellipsometry measurements performed on Sb&N and AT doped GS as-deposited materials [9,13], representative of as-fabricated devices (i.e. before firing). We observe a linear correlation between V<sub>fire</sub> and  $E_g$  (**Fig. 7**), and an exponential relation between V<sub>fire</sub> and  $E_g$  (**Fig. 8**). These results show an intrinsic correlation between V<sub>fire</sub> and I<sub>leak</sub>, independent of the OTS composition (**Fig. 9**). However, OTS materials have also to deal with their stability against high temperature and electrical stress that can induce a degradation of the device performances, as an increase of I<sub>leak</sub> after firing. **5. Benchmark of Ge<sub>3</sub>Se<sub>7</sub>-based OTS selectors** 

In **Fig. 10** we compare the electrical parameters of different OTS selectors based on GS: AT-doped [12] and Sb&N-doped [13]. AT allows reducing  $V_{th}$  at values equivalent to the ones achieved by Sb, but featuring a lower  $I_{leak}$ . An optimized Sb&N-doping allows a moderate reduction of  $V_{th}$  but extremely low  $I_{leak}$ . Since Sb-doping shows high  $I_{th}$ , such materials are more suitable for a co-integration with a memory characterized by a low resistance in its HRS (high resistance state). On the contrary, Sb&N-doping (i.e. with the lowest  $I_{th}$ ) shows compatibility with 1R devices with higher HRS resistance. AT-doping permits to tune  $I_{th}$  between the values of Sb- and Sb&N-doping.

#### 6. Conclusions

In this paper, we present the material engineering of OTS based on GS composition. The optimization of Sb- and N-doping in GS allows OTS selectors featuring extremely low  $I_{leak}$ . Then, tunable and predictable OTS electrical parameters are achieved by  $As_2Te_3$ -doping. We demonstrate the correlation between  $E_g$ ,  $V_{fire}$  and  $I_{leak}$ , showing that a reduction of  $V_{fire}$  implies an increase of  $I_{leak}$ . Finally, a benchmark between the different GS-based OTS selectors developed, demonstrates their suitability for different resistive memory technologies.

Name	Composition	tens
GS	Ge <sub>3</sub> Se <sub>7</sub>	느
GSS	Sb-doped GS	ama
GSSN	N-doped GS	1 R
AT	As <sub>2</sub> Te <sub>3</sub>	lize
GS-AT	As <sub>2</sub> Te <sub>3</sub> -doped GS	rma
Annealing: 400°C @ 30min		N

Table 1: Correspondence between alloys names used and **OTS** compositions



Fig. 1: Raman spectroscopy in Sb and/or N-doped GS alloys. An excess of Sb leads to the formation of Sb-Sb bonds that are suppressed by N-doping. After annealing, no material degradation occurs in GSSN.



phase appears.

nn Sc 25 •

D

Exp. data

Mode

GS

Fig. 4: Experimental data and fitting of a) threshold voltage Vth and

b) leakage current I<sub>leak</sub> for different compositions and thicknesses.

Electrical parameters in GS-AT alloys can be predicted by simple re-

lations thanks to the linear decrease of Eg with AT content. It is helpful

D E GS A As<sub>2</sub>Te<sub>3</sub> Content (% at.)



Fig. 2: IR spectroscopy high- Fig. 3: Sb-doping helps to decrease lighting the formation of Sb-Se V<sub>fire</sub> at the expense of a I<sub>leak</sub> inand Ge-N bonds. After the an- crease. N-doping helps to decrease nealing, a homogeneous GeNx Ileak but with an increase of Vfire. The Annealing decreases V<sub>fire</sub>, while keeping a very low Ileak.

E

-eakage Current

b) 10



Fig. 5: In GS + AT alloys, the local or- Fig. 6: ders of both GS and AT systems coexist, bandgap of AT-doped indicating that a limited chemical inter- alloys linearly decreases action occurs between the two alloys.



[13].



The optical with AT content.



Threshold Voltage (V)

a)

GS

Fig. 7: Evolution of V<sub>fire</sub> as a function Fig. 8: Evolution of I<sub>leak</sub> on as-fabricated devices as of Eg for different compositions. A lin- a function of Eg. Ileak increases exponentially with ear correlation is observed. Data for Eg, compatibly with a Poole-Frenkel electrical con-Sb&N-doped GS (GS-Sb-N) are from duction (data for GS-Sb-N are from [13]).



Fig. 9: Correlation between Vfire and Ileak. Decrease of Vfire induces an increase of Ileak (i.e. both parameters depend on Eg).



Fig. 10: Benchmark of a) Ileak vs Vth and b) Ileak vs Ith for different dopants in GS: AT, Sb and Sb&N [12]. The lowest Ileak is achieved with Sb&N-doping. AT allows the same Vth as Sb with the advantage of a lower Ileak. Sb allows the highest Ith, making Sb-doping suitable for memory with low HRS resistance. On the contrary, low Ith in Sb&N-doped GS is suitable for devices with high HRS resistance. Ith can be tuned with AT-doping between the values of Sb- and Sb&N- doping.



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### Acknowledgements

This work has been partially supported by the European 621217 PANACHE project and 783176 WAKeMeUP project.