Studies of Ferroelectric HfO₂ Materials before Memory and Logic Applications

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Abstract

 HfO_2 ferroelectrics are compatible with the advanced semiconductor processes, and are promising for memory and logic applications. However, electrical performances of HfO_2 ferroelectrics are still immature. This work investigates the status of HfO_2 ferroelectrics from the viewpoint of endurance performances.

1. Characteristics of HfO₂ Ferroelectrics

Since the first reports of ferroelectricity in HfO₂-based crystalline films in 2011 [1-2], many attractive characteristics of this material have been demonstrated. The metastable orthorhombic crystal structure is probable to be the origin of ferroelectricity [1, 3]. Many kinds of elements are useful develop ferroelectric films [4, 5]. Aggressive scaling of the film thickness as thin as 3 nm has been studied [6]. Superior ferroelectric capacitors could be prepared by annealing at 400 °C and less [7-9]. The polarization switching speeds faster than 10 ns have been reported [10, 11]. These characteristics promise the application of ferroelectric HfO₂ thin films in memory and logic applications.

2. Potential applications of HfO₂ ferroelectrics

Developments of electronic devices integrated with HfO₂ ferroelectrics are in progress. Ferroelectric materials such as Pb(Zr,Ti)O₃ (PZT) with 80 nm thickness have already been utilized as the FeRAM with 130 nm technology node [12]. An advantage of ferroelectric HfO₂ films is the thickness, around 10 nm. Because of its extremely thin structure, FeRAMs with ferroelectric HfO₂ can be integrated with the state-of-art DRAM process [13]. HfO₂ is thus expected to increase the capacity of FeRAM drastically. Application of HfO2 ferroelectrics in one-transistor-type memory (FeFET) is also investigated at advanced technology nodes [13-16]. A study to replace the 3D-NAND by vertical FeFETs is also reported [17]. Ferroelectric tunnel junction (FTJ) is a two-terminal memory, and is expected as a new type non-volatile memory [18, 19]. In addition to these memory devices, investigation of steep subthreshold swing logic devices using FeFETs is stimulated [20]. Although its device physics is still controversial [21], many challenges are ongoing.

Device technologies utilizing ferroelectric HfO_2 contribute to realize many kinds of useful circuits. In combination with the SRAM, the ferroelectric HfO_2 contributes to reduce the power consumption [22, 23]. Scalability of ferroelectric HfO2 is compatible to develop in-memory computing systems [24, 25]. A random switching behavior of multi-domains in ferroelectric capacitors is useful to emulate the human brain and to develop neuromorphic circuits [26-31]. Expectations of HfO_2 ferroelectrics for next generation LSI are increasing.

3. Status of Ferroelectric HfO₂ Performances

An important property of memory materials for LSI application is the tough endurance. Stability of memory property during the large number of bias cycle is the essential. HfO₂ ferroelectrics are no exception.

We fabricated $Hf_{0.5}Zr_{0.5}O_2$ capacitors by sputter deposition and annealing, and examined their electrical properties [32, 33]. Ferroelectric properties are summarized in Fig. 1. Remnant polarization larger than 20 μ C/cm² is attained when the applied electrical field is larger than 2 MV/cm. However, a hard breakdown occurs when the electrical field reaches 4 MV/cm. Endurance performance is correlated with the applied electrical fields. A smaller electrical field is preferable for the better endurance.

Endurance performances of HfO₂ ferroelectrics were reported by many groups [34-46]. Relationship between the endurance and the electrical field is summarized in Fig. 2. Irrespective of the deposition methods, the dopants, the electrodes, and the annealing conditions, the endurance performance is correlated with the electrical field. An important point it that the number of endurance cycle in HfO2 ferroelectrics even the best endurance cycle 10¹⁰ is still far below the performance of commercial FeRAM using PZT [12]. In order to utilize HfO₂ ferroelectrics in FeRAM, endurance cycles as large as 10^{12} is necessary. For the logic applications, endurance cycles of 10^{15} is mandatory. Thus, material property of ferroelectric HfO₂ is in the development stage.

References

- [1] T. S. Böscke et al., Appl. Phys. Lett. 99, 102903 (2011).
- [2] J. Müller et al., Appl. Phys. Lett. 99, 112901 (2011).
- [3] X. Sang et al., Appl. Phys. Lett. 106, 162905 (2015).
- [4] U. Schroeder et al., Jpn. J. Appl. Phys. 53 08LE02 (2014).
- [5] L. Xu, A. Toriumi et al., J. Appl. Phys. 122, 124104 (2017).
- [6] X. Tian, A. Toriumi et al., Appl. Phys. Lett. 112, 102902 (2018).
- [7] M. G. Kozodaev et al., Appl. Phys. Lett. 111, 132903 (2017).
- [8] S. J. Kim et al., et al., Appl. Phys. Lett. 111, 242901 (2017).
- [9] T. Onaya et al., Microelectronic Engin. 215, 111013 (2019).
- [10] W. Chung et al., 2018 Symp. VLSI Technol. Dig., pp. 89-90.
- [11] X. Lyu et al., 2019 Symp. VLSI Technol. Dig., T45-T45.
- [12] J. A. Rodriquez et al., IEEE Trans. Dev. Mater. Reliability 4, 436 (2004).
- [13] J. Müller et al., Tech. Dig. IEDM 2013, pp.280-283.
- [14] J. Müller et al., 2012 Symp. VLSI Technol. Dig., pp. 25-26.

- [15] M. Trentzsch et al., Tech. Dig. IEDM 2016, pp. 294-297.
- [16] S. Dünkel et al., Tech. Dig. IEDM 2017, pp. 485-488.
- [17] K. Florent *et al.*, 2017 Symp. VLSI Technol. Dig., T158 -T159.
- [18] S. Fujii et al., 2016 Symp. VLSI Technol. Dig., pp.148-149.
- [19] F. Mo et al., Tech. Dig. IEDM 2018, pp. 372-375.
- [20] S. Salahuddin and S. Data, *Nano Lett.* 8, 405 (2008).
- [21] M. Alam et al., Appl. Phys. Lett. 114, 090401 (2019).
- [22] M. Kobayashi *et al.*, 2017 Symp. VLSI Technol. Dig., T156-T157.
- [23] M. Kobayashi et al., J. Electron. Dev. Soc. 6, 280 (2018).
- [24] K. Ni et al., et al., Tech. Dig. IEDM 2018, pp. 364-367.
- [25] R. Berdan et al., 2019 Symp. VLSI Technol. Dig., T22-T23.
- [26] H. Mulaosmanovic et al., 2017 Symp. VLSI Technol. Dig., T176-T177.
- [27] K. Ni et al., Tech. Dig. IEDM 2018, pp. 296-299.
- [28] Z. Wang et al., Tech. Dig. IEDM 2018, pp. 300-303.
- [29] S. Dutta et al., 2019 Symp. VLSI Technol. Dig., T38-T39.
- [30] C. Chen et al., 2019 Symp. VLSI Technol. Dig., T136-T137.
- [31] S. Dutta et al., 2019 Symp. VLSI Technol. Dig., T140-T141.

- [32] S. Migita et al., Jpn. J. Appl. Phys. 57, 04FB01 (2018).
- [33] S. Migita, ECS Trans. 86, 13 (2018).
- [34] D. Zhou et al., Appl. Phys. Lett. 103, 192904 (2013).
- [35] S. Mueller et al., IEEE Trans. Dev. Mater. Reliability 13, 93 (2013).
- [36] T. Schenk et al., ACS Appl. Mater. Interfaces 7, 20224 (2015).
- [37] D. Zhou et al., Acta Materialia 99, 240 (2015).
- [38] P. Polakowski and J. Müller, Appl. Phys. Lett. 106, 232905 (2015).
- [39] M. H. Park et al., Appl. Phys. Lett. 107, 192907 (2015).
- [40] P. D. Lomenzo et al., Appl. Phys. Lett. 107, 242903 (2015).
- [41] P. D. Lomenzo et al., J. Appl. Phys. 117, 134105 (2015).
- [42] S. Zarubin et al., Appl. Phys. Lett. 109, 192903 (2016).
- [43] K. D. Kim et al., J. Mater. Chem. C 4, 6864 (2016).
- [44] K. Florent et al., IEEE Trans. Electron Dev. 64, 4019 (2017).
- [45] T. Mittmann et al., Microele. Engin. 178, 48 (2017).
- [46] A. Wei et al., J Alloy Compounds 731, 546 (2018).



Fig. 1. (a) *P*-*E* and *J*-*E* curves of the 10-nm-thick $Hf_{0.5}Zr_{0.5}O_2$ film capacitor with the increment of sweep bias. (b) Applied electrical field dependence of the remnant polarization P_R and the coercive field E_C . (c) Endurance performances with the applied electrical fields.



2. Endurance HfO₂-based Fig. of ferroelectric cap acitors with applied electrical fields. The red squares show our results in this work, and the blue circles are the data reported in the references prepared bv many dopants, various deposition methods, various electrodes, and various annealing conditions. Lower electrical fields are preferable to achieve a larger endurance cycles. Performances of HfO2 ferroelectrics are still far below the commercial FeRAM.