

## Comparison Studies on Various Perovskites Fabricated on Transparent Conducting Oxide Free Substrates

Univ. of Electro-Communications<sup>1</sup>, Univ. of Tokyo<sup>2</sup>, °Shahrir Razey Sahamir<sup>1</sup>, Muhammad Akmal Kamarudin<sup>1</sup>, Gaurav Kapil<sup>1</sup>, Ajay Kumar Baranwal<sup>1</sup>, Qing Shen<sup>1</sup>, Shuzi Hayase<sup>1</sup>

E-mail: [shahrir@uec.ac.jp](mailto:shahrir@uec.ac.jp), [hayase@uec.ac.jp](mailto:hayase@uec.ac.jp)

The replacement of indium tin oxide as anodic electrode in p-i-n type perovskite solar cell (PSC) devices have gained attention recently. The reason for increasing interest in transparent conducting oxide (TCO) free PSCs was mainly due to the limitation of TCO devices in terms of fabrication and flexibility. PSCs with TCO employing high temperature and high energy processes which are not compatible with flexible devices due to its rigidity. TCO free PSCs offer various advantages such as low processing temperature and also serving as functional electrodes in flexible devices [1-3]. Organic based and hybrid-metallic organic electrodes have been chosen as substitute for TCO electrodes. They are generally flexible and easy to process. In this research, various types of perovskite devices, i.e. lead, tin-lead and tin perovskites were fabricated on TCO free substrates and their performance were evaluated via various characterisations. The electrodes were fabricated with poly(3,4-ethylenedioxythiophene):poly(styrene sulfonate) (PEDOT:PSS, PH1000) and treated with ethylene glycol (EG) and dimethyl sulfoxide (DMSO) mixed at various concentrations. The surface treated electrodes has enabled the perovskite to adhere to the surface of the electrodes due to the change in the hydrophobic to hydrophilic behavior of the surface [1], simultaneously boosting the performance of the devices. The fabrication of lead (over 10% efficiency) and tin-lead TCO-less perovskite manage to yield high performance devices. However, the tin based Tco-less perovskite performance were way poorer than the others. In addition, the ability to retain their (TCO free devices) efficiency as opposed to their counterpart devices fabricated on TCO was higher in lead and tin-lead PSCs. The impedance spectroscopy studies revealed the difference in terms of charge transfer mechanisms in TCO and TCO free devices. Based on these results, it is hopeful that better device configurations yielding higher performance will be realized in the near future.

### References

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