

## Effect of forming gas annealing on the enhancement of the electron spin lifetime in the inversion channel of Si-based spin MOSFETs

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Long spin lifetime is essential for highly-efficient spin transport in Si-based spin MOSFETs [1]. Our previous study revealed that the spin lifetime  $\tau_s$  is proportional to the electron momentum lifetime  $\tau$ , and the ratio  $\tau_s/\tau$  ( $\sim 25000$ ) was almost constant, irrespective of the applied gate voltage  $V_{GS}$  [2,3], indicating that the Elliott-Yafet mechanism governs the spin relaxation [4,5]. From this result, the enhancement of  $\tau_s$  is expected with increasing  $\tau$ . In this study, we perform a forming gas annealing (FGA), which is effective for increasing  $\tau$ , on a spin MOSFET and studied the changes of  $\tau_s$  and  $\tau$  by FGA at various temperatures  $T$ .

Figure 1 shows our bottom-gated spin MOSFET device examined in this study [2], which has Fe/Mg/MgO/ $n^+$ -Si ferromagnetic tunnel junctions at the Source/Drain and a 8-nm-thick  $p$ -Si channel ( $N_A \sim 1 \times 10^{15} \text{ cm}^{-3}$ ) with a 10  $\mu\text{m}$  in length. Before FGA, Hanle precession signals were measured at  $T = 4 - 295 \text{ K}$  and  $\tau_s$  was estimated by fitting the signals as the same procedure described in [3]. On the other hand, the electron mobility  $\mu$  and  $\tau$  was estimated from another Hall-bar type device with the same channel structure. After that, the samples were annealed (FGA) at 250°C for 30 min in a 96%  $\text{N}_2 + 4\% \text{ H}_2$  ambient. Then,  $\tau_s$ ,  $\mu$ , and  $\tau$  were estimated using the same procedure and compared with those estimated before the FGA, respectively.

Figure 2 shows  $\mu$  as a function of the effective gate electric field  $E_{\text{eff}}$  [3], in which  $\mu$  becomes about twice at 295 K, and becomes larger by six-times at 4 K by the FGA. We see remarkable enhancement of  $\mu$  at the lower  $E_{\text{eff}}$  ( $\sim 0.1 \text{ MV/cm}$ ), indicating that the Coulomb scattering was suppressed by the hydrogenation of fixed negative charges at the Si/SiO<sub>2</sub> interface. This is consistent with the fact that the flat-band voltage was negatively shifted after the FGA. Figure 3 shows the comparison of  $\tau_s$  (right axis) and  $\tau$  (left axis) before and after FGA at various temperatures. We found that FGA increases  $\tau_s$  along with  $\tau$ , whereas the ratio  $\tau_s/\tau$  remains almost constant ( $\sim 25000$ ). Therefore, we conclude that longer  $\tau$  leads to longer  $\tau_s$  in the Si inversion channel.

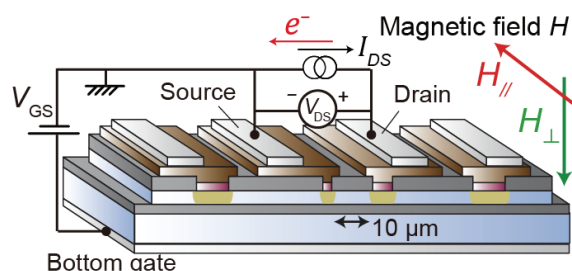


Fig. 1 Schematic illustration of our spin MOSFET device structure and the measurement setup.

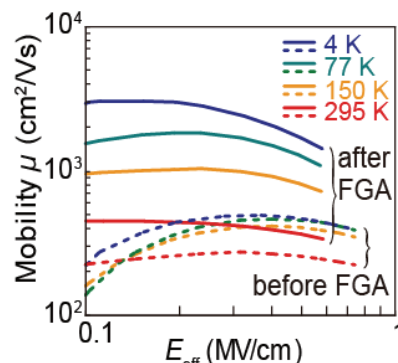


Fig. 2 Electron mobility  $\mu$  in the inversion channel before FGA (dashed curve) and after FGA (solid curve).

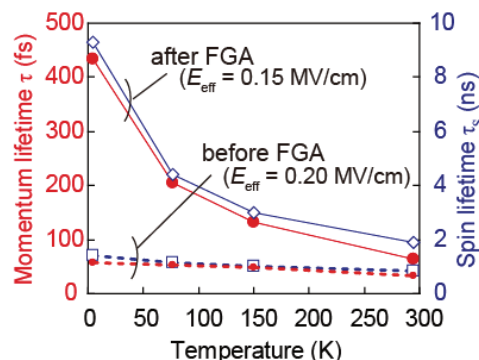


Fig. 3 Spin lifetime  $\tau_s$  (blue, right axis) and momentum lifetime  $\tau$  (red, left axis) before and after FGA.

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**References** [1] S. Sugahara and M. Tanaka, Appl. Phys. Lett. **84**, 2307 (2004). [2] S. Sato et al., Phys. Rev. B. **99**, 165301 (2019). [3] S. Sato et al., Phys. Rev. B. **102**, 035305 (2020). [4] R. J. Elliott, Phys. Rev. **96**, 266 (1954). [5] Y. Yafet, Sol. St. Phys. **14**, 1 (1963).