

Impact of Zr Concentration on Time-Dependent Dielectric Breakdown of HfZrO-based Ferroelectric Tunnel Junction (FTJ) Memory

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Abstract

We systematically investigated the impact of Zr concentration on time-dependent dielectric breakdown (TDDB) of HfZrO-based ferroelectric tunnel junction (FTJ) memory. The time-to-breakdown (T_{bd}) becomes shorter with increasing Zr concentration. One plausible mechanism is that the HfZrO capacitance becomes higher as Zr concentration increases, and thus voltage applied to the interfacial layer SiO₂ becomes higher, which accelerates the defect generation and leads to shorter T_{bd} . A reliable FTJ with long T_{bd} and high ON/OFF ratio was demonstrated by controlling HfZrO deposition temperature and Zr concentration.

1. Introduction

HfO₂ based ferroelectric materials and related devices such as FeFET, FeRAM and FTJ are attracting much attention for next generation memory applications. Among them, we have demonstrated HfO₂ based FTJs with characteristics desirable for both high-density low-cost applications and AI applications; low operation current, rectifying property, and analog switching [1-5]. In addition, 2-terminal structure is suitable for cross-point architecture. Although the FTJ shows excellent memory performance, stress induced breakdown during program/erase cycling is one of the critical issues [6-7]. To realize highly reliable FTJ, it is necessary to understand the failure mechanisms and establish a guideline for time-to-breakdown (T_{bd}) improvement. In this work, we systematically investigated the impact of Zr concentration on the T_{bd} of HfZrO-based FTJs, and demonstrated the improvement of both T_{bd} and ON/OFF ratio by optimization of the HfZrO deposition process.

2. Impact of Zr concentration on T_{bd}

Fig.1 shows the device structure of the FTJ. A ferroelectric HfZrO layer (FE-HfZrO) and a paraelectric SiO₂ interfacial layer (IL-SiO₂) are sandwiched between top electrode (TE) and bottom electrode (BE). Fig.2 shows I-V characteristics of the FTJs. Polarization reversal in the HfZrO causes ON/OFF switching.

We carried out TDDB measurements for 3 types of devices with different Zr concentration in the HfZrO. We used DC stress for the TDDB measurements (Fig.3(a)), and monitored current across the stack until breakdown occurs (Fig.3(b)). Fig.4 shows Weibull distribution of T_{bd} of the FTJs under (a) positive stress of 4.4V and (b) negative stress of -5.1V. Each T_{bd} distribution normalized by the device size is located on a single curve. This result suggests that the percolation model, which is well-known to explain the conventional oxide breakdown, is applicable to the FTJ failure.

As shown in Fig.4, the average T_{bd} for both polarity become shorter with higher Zr concentration (x_{Zr}). On the other hand, the Weibull slope β s do not depend on the x_{Zr} . According to the percolation model, Weibull distribution of T_{bd} is described by the following equation [8],

$$\ln(-\ln(1-F)) = \beta \cdot \ln(\lambda(t)) + \ln(N) \\ = \frac{t_{ox}}{a_0} \cdot B \cdot \ln(T_{bd}) + \frac{t_{ox}}{a_0} \cdot \ln(k) + \ln\left(\frac{A_0}{a_0^2}\right) \quad (1)$$

where a_0 is defect size, B is time-exponent of defect generation rate, k is proportionality factor of the defect generation rate, and A_0 is device area, respectively. Here, we assumed t_{ox} corresponds to the thickness of the IL-SiO₂, since our previous work revealed that the defects generation in the IL-SiO₂ triggers the breakdown of the FTJ [7]. Since the Weibull β s for the FTJs in Fig.4 show the same value irrespective of the Zr concentration (x_{Zr}), t_{ox} , a_0 and B for the FTJs

do not change with x_{Zr} . Hence, shorter T_{bd} in higher x_{Zr} might be caused by increase in the k in the equation (1). In the previous work, we confirmed that the k , that is, acceleration factor of the defect generation is increased by stress voltage [6-7]. Therefore, the T_{bd} degradation for higher x_{Zr} is presumably due to higher electric field across the IL-SiO₂ which accelerates the defect generation.

We found that the initial leakage current ($I_{initial}$) before applying stress voltage increases for the FTJ with high x_{Zr} (Fig.5). Since the tunneling current across the FTJ stack is determined by the electric field to the IL-SiO₂ (E_{SiO_2}), higher $I_{initial}$ for high x_{Zr} also suggests that the E_{SiO_2} is increased with increasing the x_{Zr} . One plausible mechanism is that the HfZrO capacitance becomes higher with x_{Zr} , therefore the voltage applied to the IL-SiO₂ becomes higher, which results in shorter T_{bd} as shown in Fig.6. Fig.7 shows that T_{bd} decreases with increasing $I_{initial}$. From Figs.4 and 5, we can understand that both $I_{initial}$ increase and T_{bd} degradation observed for the FTJ with high x_{Zr} derive from E_{SiO_2} increase and enhanced defect generation. As a result, reduction of the E_{SiO_2} is necessary to ensure long T_{bd} .

3. Demonstration of longer T_{bd} and high ON/OFF ratio

To reduce the E_{SiO_2} and enhance tolerance to breakdown, high temperature deposition of the HfZrO is found to be an effective solution (Fig.8). However, increase in the deposition temperature degrades ON/OFF ratio (Fig.9). Therefore, there is a trade-off between T_{bd} and ON/OFF ratio.

To overcome the trade-off, we further investigated material properties for the FE-HfZrO layers deposited at a high temperature. Fig.10 shows dependence of remnant polarization (P_r) on Zr concentration (x_{Zr}) for high temperature FE-HfZrO. There is an optimal x_{Zr} to maximize P_r . In general, P_r corresponds to ON/OFF ratio [9]. Therefore, Zr optimization could compensate the ON/OFF degradation, and thus improves the trade-off. To demonstrate high ON/OFF ratio for the FTJ with high temperature HfZrO, we designed x_{Zr} based on Fig.10. Fig.11 shows ON/OFF ratio vs $I_{initial}$ for the FTJ with high temperature HfZrO deposition. Simultaneous achievement of low leakage current and high ON/OFF ratio is demonstrated by the x_{Zr} control. We also confirmed that the T_{bd} is improved for the optimized FTJ with low leakage current and high ON/OFF ratio as shown in Fig.12(a)(b), indicating E_{SiO_2} is reduced by Zr optimization. Optimization of HfZrO deposition temperature and Zr concentration is a key to realize highly reliable FTJ.

4. Conclusions

We revealed that the suppression of defect generation by reducing the electric field applied to IL-SiO₂ is a solution to improve the reliability of the FTJ. The simultaneous improvement of the T_{bd} and the ON/OFF ratio was achieved by optimization of HfZrO deposition temperature and Zr concentration. Considering the advantages such as low operation current, rectifying property, and analog switching, the HfO₂-based FTJ has high potential for various emerging memory applications.

References

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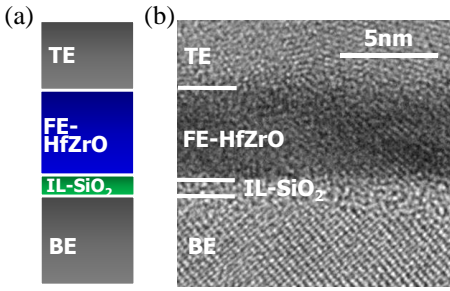


Fig.1 Device structure of HfZrO FTJ. (a) Schematics and (b) cross-sectional TEM.

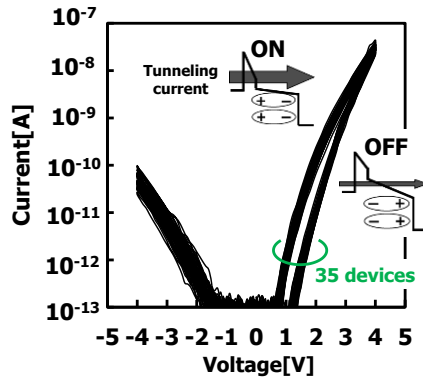


Fig.2 I-V characteristics of the FTJs.

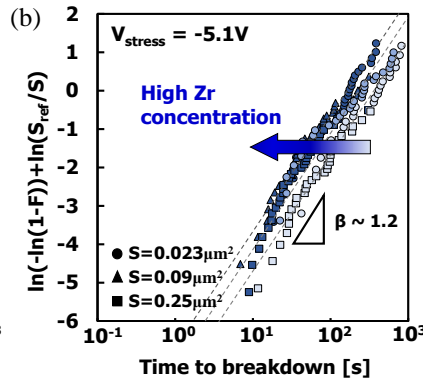
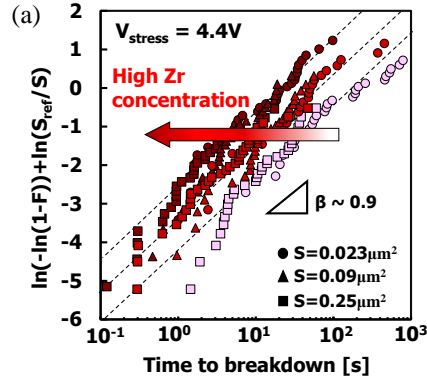


Fig.4 Weibull distribution of time-to-breakdown (T_{bd}) of FTJs under (a) positive stress of 4.4V and (b) negative stress of -5.1V.

Ex) Positive stress

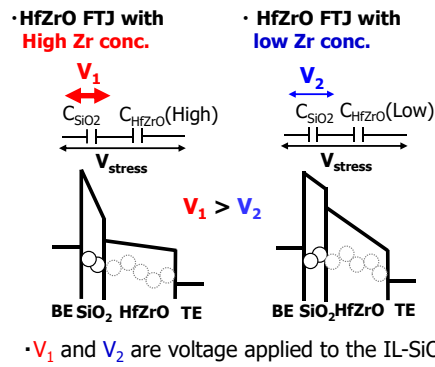


Fig.6 Breakdown model of HfZrO FTJ. High HfZrO capacitance accelerates defects generation in IL-SiO₂ because higher voltage is applied to IL-SiO₂.

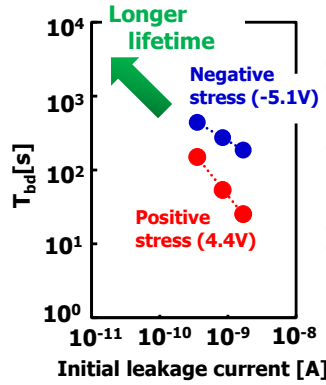


Fig.7 Relation between initial leakage current and time-to-breakdown (T_{bd}).

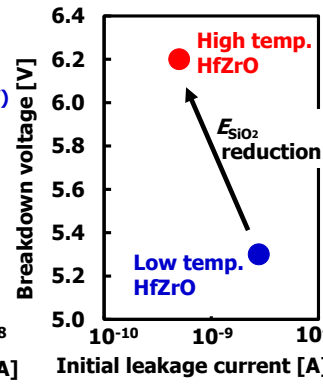


Fig.8 Dependence of initial leakage current and breakdown voltage on HfZrO deposition temperature.

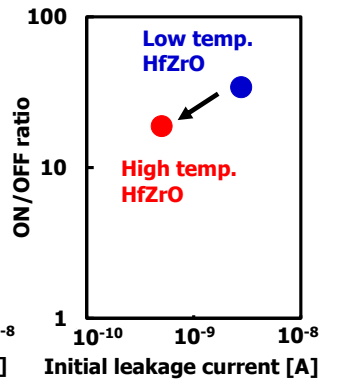


Fig.9 Dependence of initial leakage current and ON/OFF ratio on HfZrO deposition temperature.

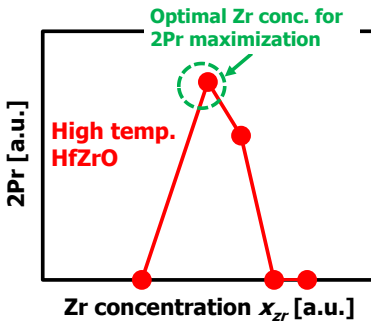


Fig.10 Dependence of remnant polarization (Pr) on Zr concentration (x_{Zr}) for high temperature FE-HfZrO.

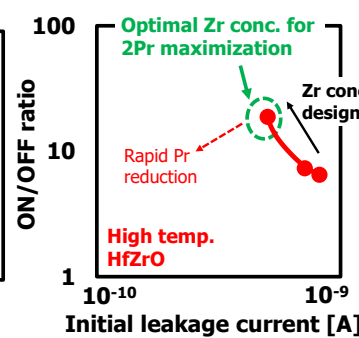


Fig.11 ON/OFF ratio vs initial leakage current for the FTJ with high temperature HfZrO.

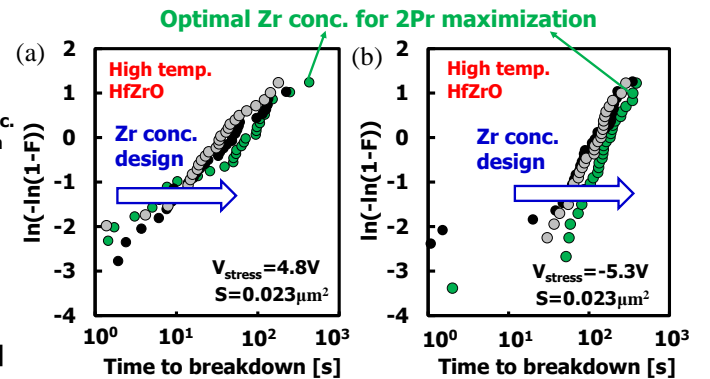


Fig.12 Demonstration of time-to-breakdown (T_{bd}) improvement by optimization of HfZrO deposition temperature and Zr concentration. (a) Positive stress. (b) Negative stress.