

# Power Scaling in Photonic Crystal Surface Emitting Laser Arrays

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## ABSTRACT

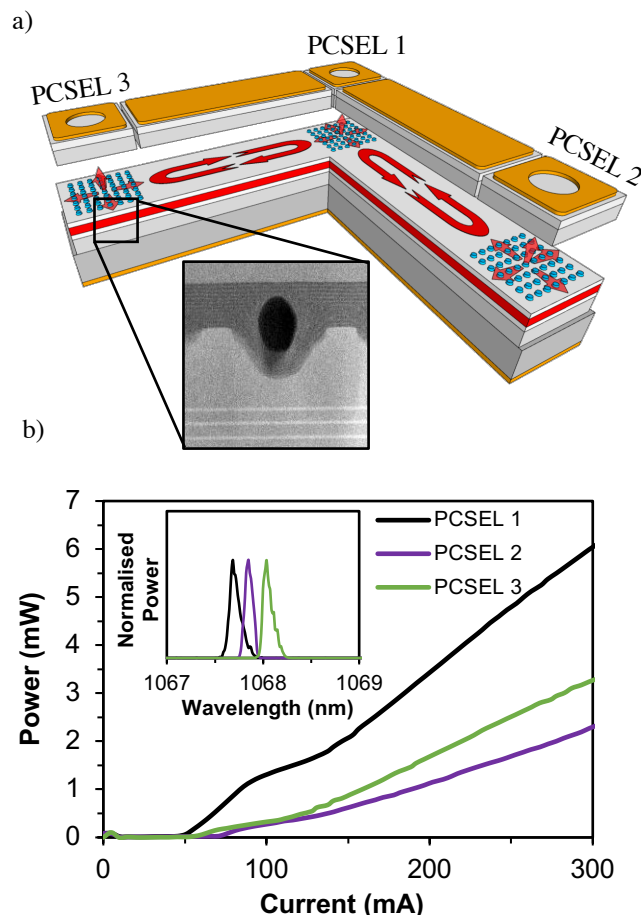
We describe power scaling in photonic crystal surface emitting laser arrays and demonstrate how such arrays lead to increased efficiency in a three-element array due to a reduction of in-plane loss of optical power.

## I. INTRODUCTION

Photonic crystal surface emitting lasers (PCSELS) are a new class of semiconductor laser which incorporate a 2D photonic crystal (PC) layer into a semiconductor laser structure. They offer the possibility high power single mode surface emitting laser with narrow divergence[1], and through design of the photonic crystal (PC) region ultimate control over wavelength, polarization[2], and beam shape of emission[3]. Unlike vertical cavity surface emitting lasers, PCSELS allow for large single-mode powers through scaling the emission area. Allowing for the realization of high brightness laser diodes[4]. However, there are practical limits to the size and power of a single device, relating to self-heating, difficulties in achieving uniform carrier distributions, optical loss in current spreading layers, and maintaining single mode emission. Coherently coupled PCSEL arrays are the next logical step in achieving high PCSEL power and further improvements to brightness[5], [6]. We demonstrate power scaling in a three PCSEL array, showing how improved efficiencies can be achieved through recycling of in-plane loss.

## II. METHODS

The PCSEL array considered in this paper is shown in fig.1 a). Each individual PCSEL device consists of a 2D PC of circular holes in a square lattice, etched into a GaAs layer directly above GaAs/InGaAs MQWs. This wafer is then regrown in one step, infilling the holes with an AlAs/GaAs superlattice. Fig. 1 a) shows a cross sectional TEM of the PC region after regrowth, demonstrating how growth has given rise to voids within the PC holes. Each PCSEL devices is 150um x 150um, and each PCSEL is connected by a 150um x 1000um coupler region. The coupler regions are the same structure as the PCSELS, but without the PC patterning. This allows for these regions to be electrically pumped to transparency in order to control the interaction of adjacent devices. The individual PCSELS will hence be able to communicate with each other through the light emitted perpendicularly at the edges of the devices, known to be large in our symmetrical circular PC design[7], and achieve in-plane coupling. The devices were pumped using a multichannel pulsed current source, custom built for the purpose of delivering synchronous pulses to the PCSEL and coupler regions, using a 1% duty cycle and 2us pulse width.

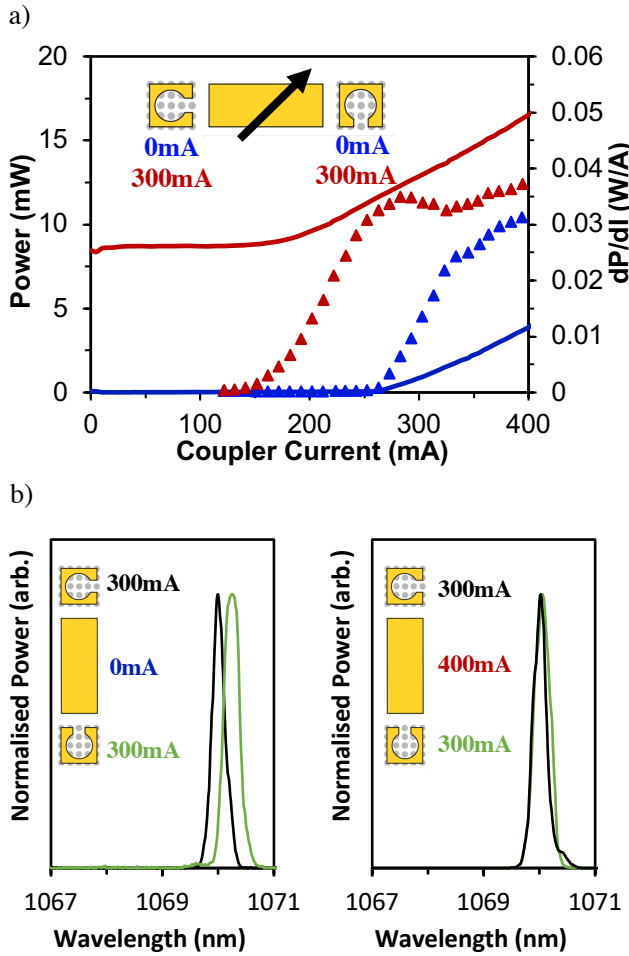


**Figure 1.** a) Schematic of PCSEL array and TEM showing details of the PC structure, and b) L-I characteristics of the PCSELS operated individually.

## III. RESULTS

Fig. 1 b) is a plot of the LI characteristics of each of the individual PCSEL devices (i.e. no current applied to the coupler contacts). Inset are spectra from each individual device at 150ma. The three devices show very similar behavior, and have a comparatively low output power due to the symmetric PC structure used, loss of ~half the output to the substrate, and coverage of the PCSEL by the contacts. Lasing wavelengths are observed to be within 0.5nm of each other.

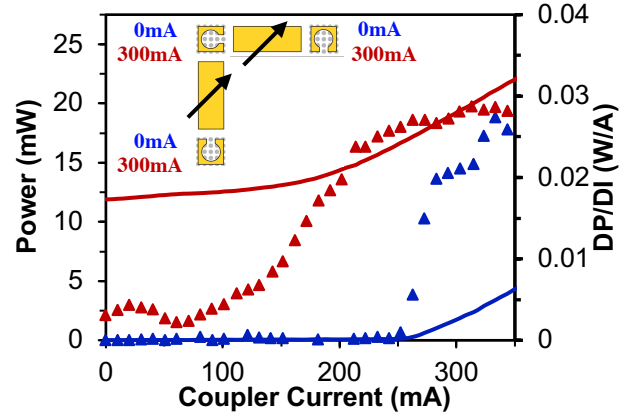
Fig.2 a) considers two PCSEL devices and their connecting coupler region. The line plot is a plot of the total power of the light emitted perpendicularly from the system, as the current to each coupler region is concurrently varied. It can be seen that when there is no current supplied to the PCSELS (blue) there is little emission from the system until the system begins to lase at ~250mA. When the PCSELS are held at 300mA (red)



**Figure 2.** a) Line plots show the power of light emitted from two PCSEL devices for varying current supplied to coupler region. Blue is with no current being supplied to the PCSEL devices and red is with 300mA supplied to each device. Scatter plot is of the dP/dI of each power plot. b) Spectra of individual PCSELs at 300mA with no current and 400mA in the coupler region.

the initial power is the sum of the power from each PCSEL at 300mA. As coupler current is increased, there appears to be a soft turn-on at ~150mA. This is attributed to the increasing transparency of the coupler regions with increasing current. Characterisation of the device suggests a transparency current for the coupler of 235mA. For two PCSELs connected by transparent couplers, a 25% increase in output power is observed (8.46 mW c.f. 10.63 mW), and there is an increase of differential efficiency from 0.02 W/A for the two PCSEL elements on their own to 0.026 W/A for the array. Fig. 2 b) shows that when there is sufficient current applied to the coupler, there is a locking of the individual PCSEL wavelengths, to within the bandwidth resolution of our measuring system. This suggests that the devices in our arrays are coherently locking.

For a system of three connected PCSELs, the total power increase at coupler transparency increases to 36%, as shown in fig. 3. The differential efficiency of the system is 0.026 W/A at the transparency point for the couplers, compared to 0.019 as the average for three independent PCSEL elements.



**Figure 3.** Line plots show the power of light emitted from all PCSEL devices for varying current supplied to each coupler region. Blue is with no current being supplied to the PCSEL devices and red is with 300mA supplied to each device. Scatter plot is of the dP/dI of each power plot.

A maximal differential efficiency of 0.028 W/A is observed at ~350 mA.

At the conference, we will discuss further the power and efficiency benefits of using PCSEL arrays, compare these benefits to increasing the size of a single device using device simulation, and extrapolate our results to talk about the possibilities of full 2D PCSEL arrays.

#### Acknowledgements

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